Arch Solutions

تمرین کامپیوتری سری دوم

سوال اول

```
/start the program from location with address 0x100 in memory (assembler directive)
      ORG
              100
                            /load A
      LDA
              A
      ADD
                            /add B to A
      STA
                            /store sum
      LDA
              В
                            /load B in accumulator
                            /1's complement B
      CMA
                            /2's complement B
      INC
      ADD
             A
                            /add A
      STA
              D
                            /store A – B
                            /increment D and jump out from the loop if D is zero
LOP,
      ISZ
              D
                            /repeat loop again
      BUN
              LOP
                            /halt the computer
      HLT
                            /A is stored here
       DEC
              10
Α,
                            /B is stored here
      DEC
              20
В,
       DEC
                            /A – B (counter) is stored here
D,
              0
                            /A + B is stored here
S,
       DEC
              0
                            /end of assembly code (assembler directive)
      END
```

سوال دوم

```
ORG
               100
                              /start the program from location with address 0x100 in memory (assembler directive)
      CLE
                             /clear carry flag
LOP,
              Y
                             /load multiplier
      LDA
                              /transfer multiplier bit to E
       CIR
       STA
                              /store shifted multiplier
       SZE
                              /check if bit is zero
                              /bit is one; go to one
              ONE
       BUN
                             /bit is zero; go to zero
       BUN
              ZRO
                             /load multiplicand
       LDA
ONE,
                              /add to partial product
              PRT
       ADD
                              /store partial product
       STA
              PRT
                             /clear E
       CLE
      LDA
              X
                             /load multiplicand
ZRO,
                              /shift left
       CIL
              X
                              /store shifted multiplier
       STA
                             /increment counter
       ISZ
              CTR
                              /counter not zero; repeat loop
       BUN
              LOP
      HLT
                              /counter is zero; halt the computer
                              /this location serves as a counter
               ~8
CTR,
      DEC
               209
                              /multiplicand stored here
Χ,
       DEC
Υ,
       DEC
               154
                              /multiplier stored here
                              /product formed here
      DEC
PTR,
              0
                              /end of assembly code (assembler directive)
       END
```