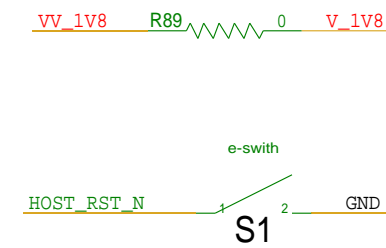
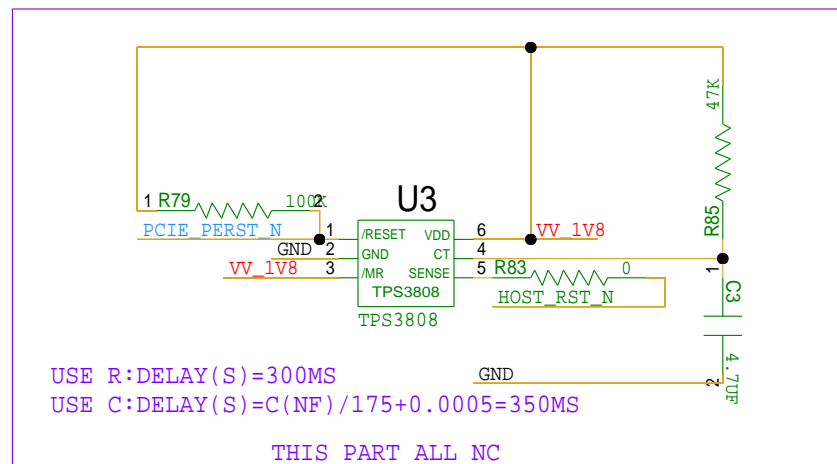
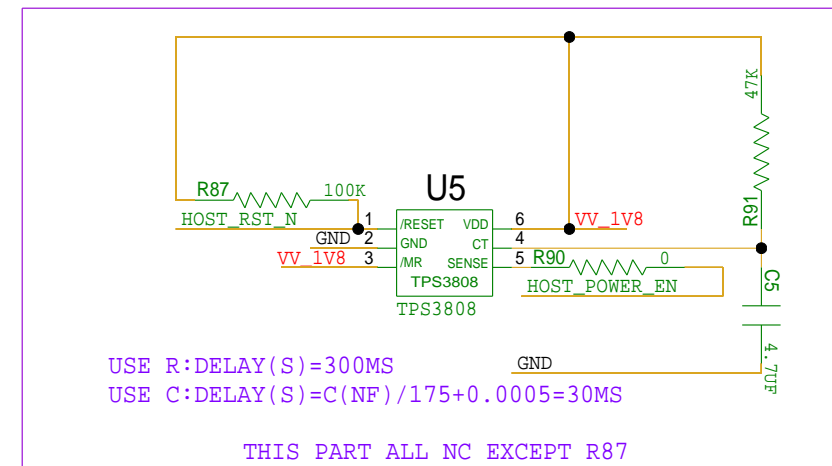
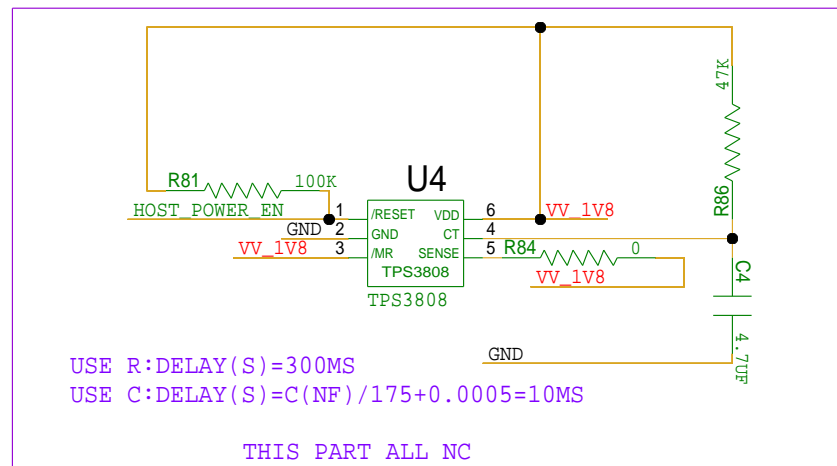


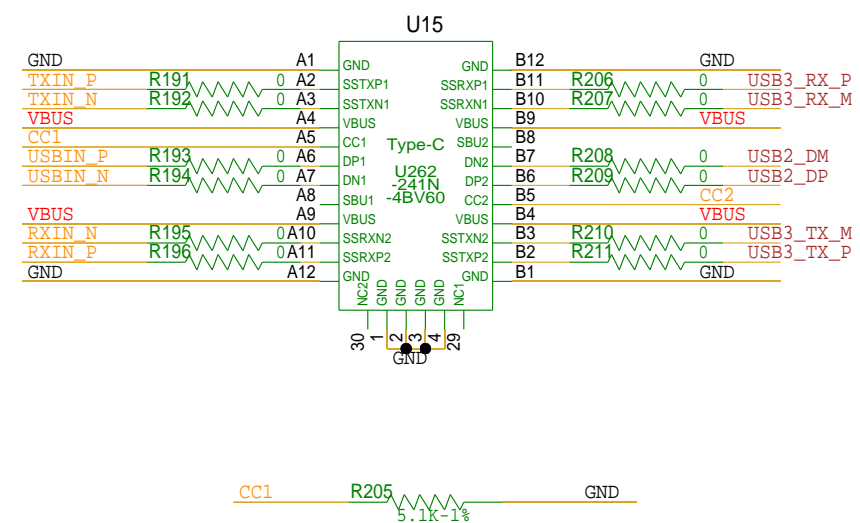
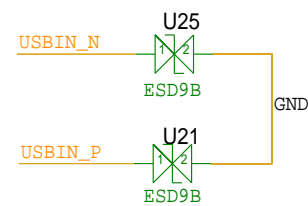
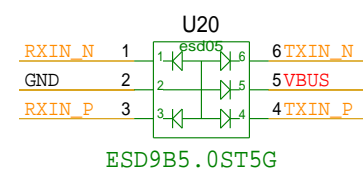
ATLAS200 DEV BOARD

VERSION: 2.0

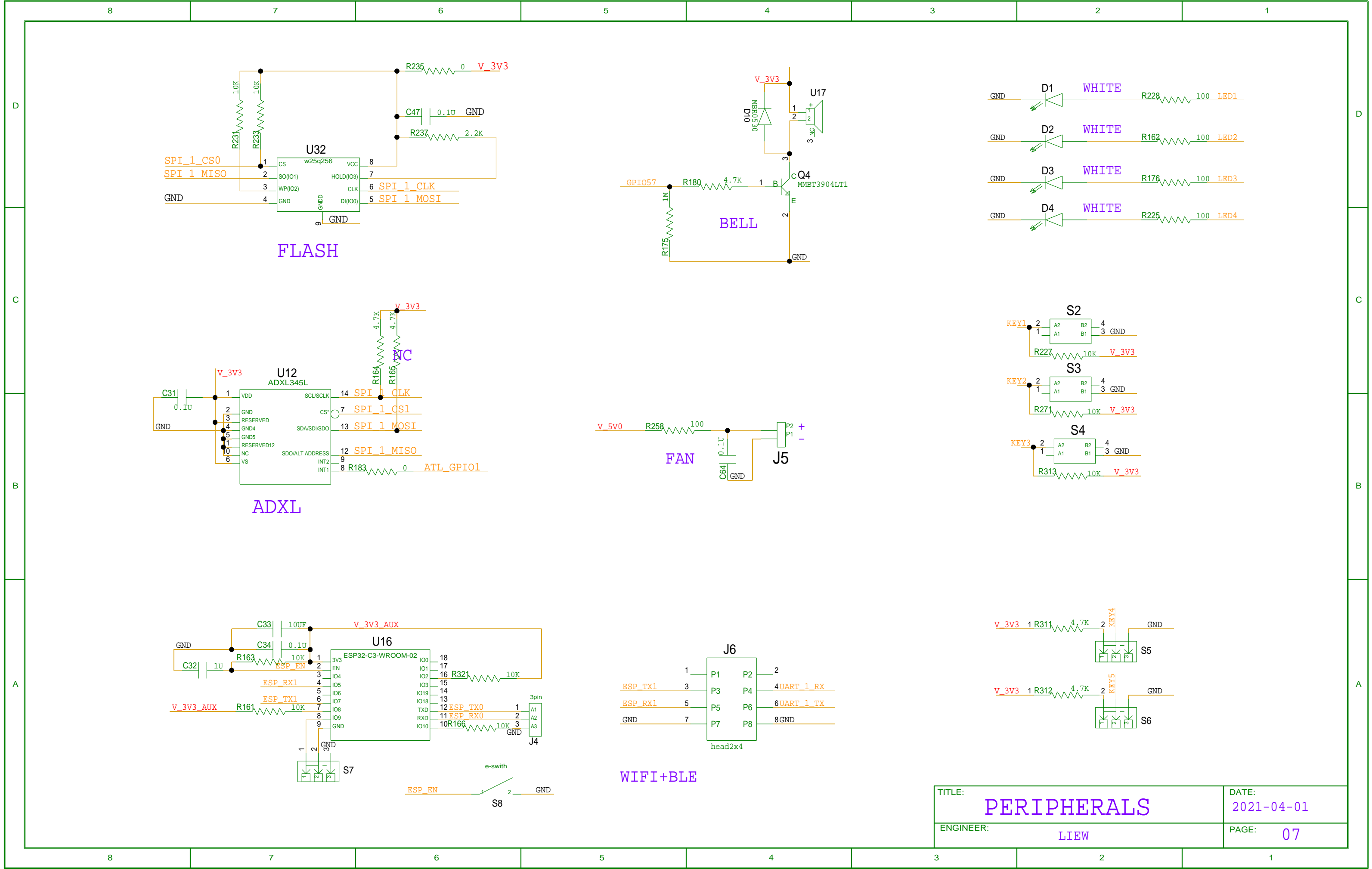
SHEET#	DESCRIPTION
01	COVER PAGE
02	CPU
03	RESET
04	ETHERNET
05	USB3.0
06	SD CARD
07	PERIPHERALS
08	I2C
09	LEVEL CONVERSION
10	INTERFACE
11	PCIE TO USB
12	POWER1
13	POWER2
14	SYSTEM CHART

TITLE:	COVER PAGE	DATE:	2021-08-03
ENGINEER:	LIEW	PAGE:	01

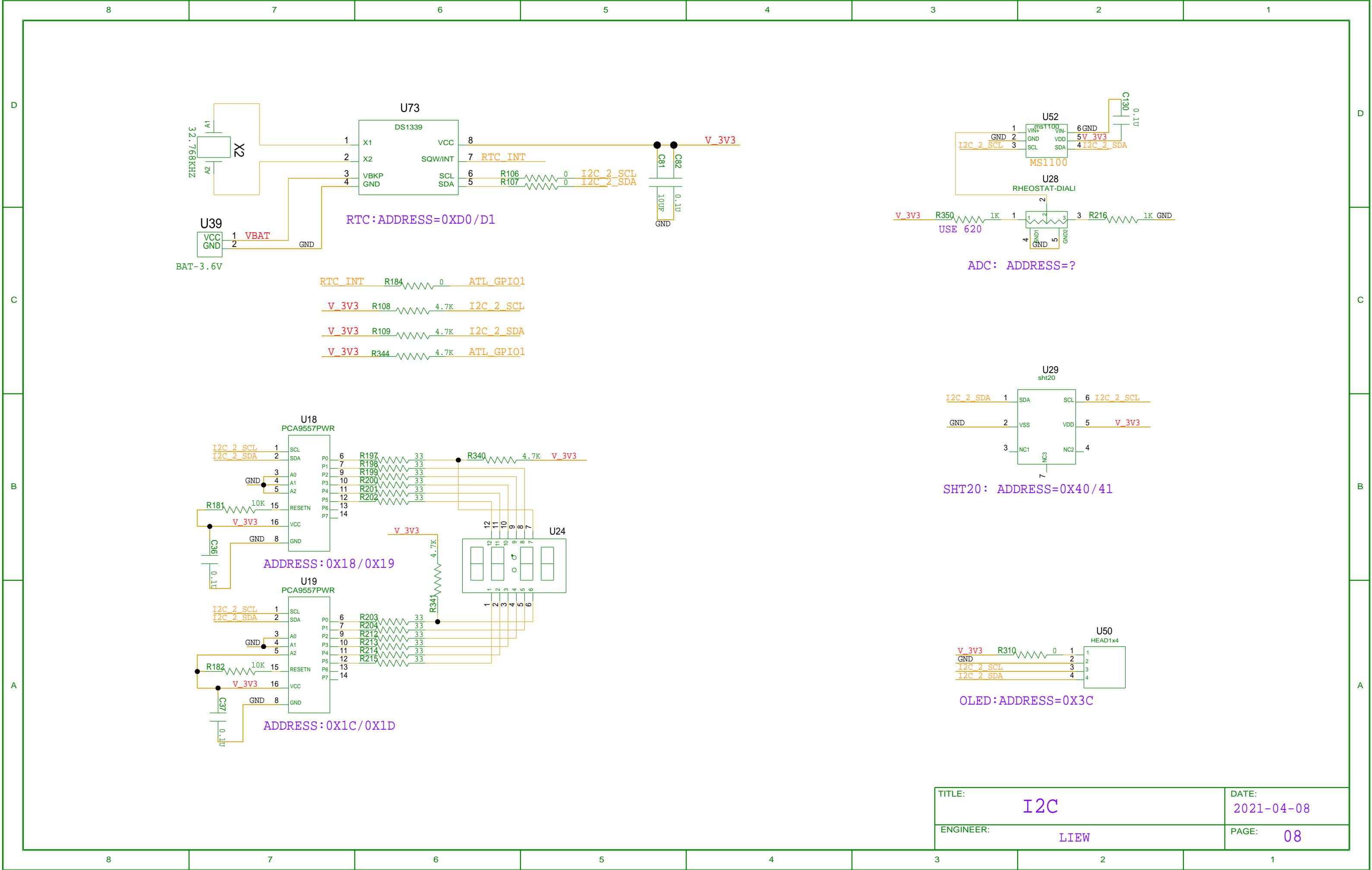


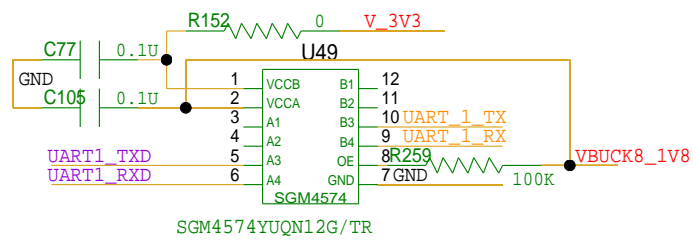
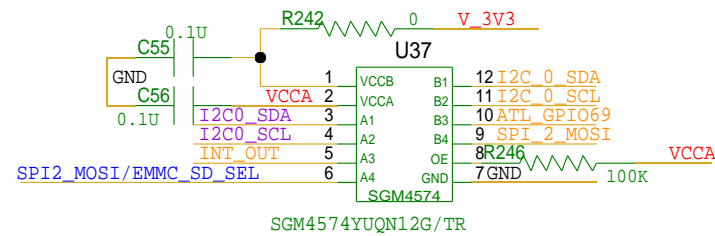
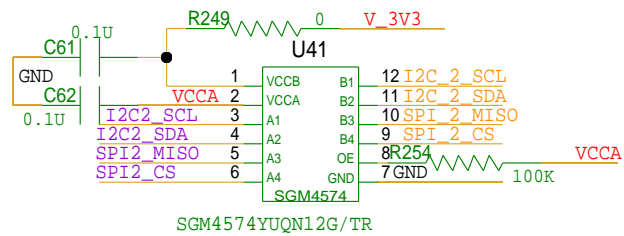
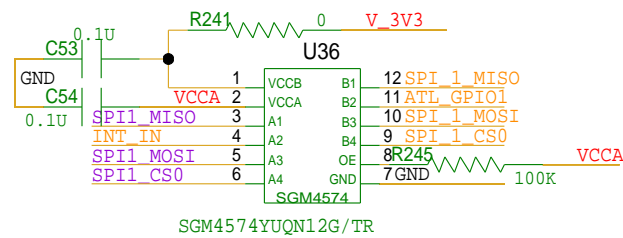
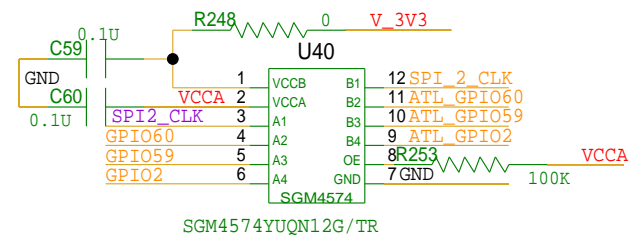
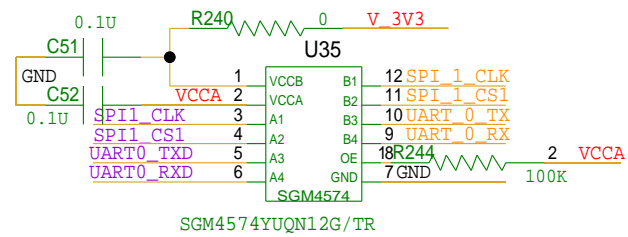
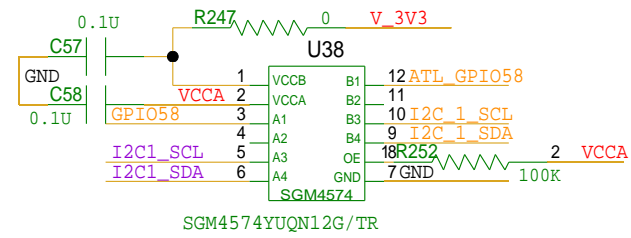


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ENGINEER:	LIEW	PAGE:	05

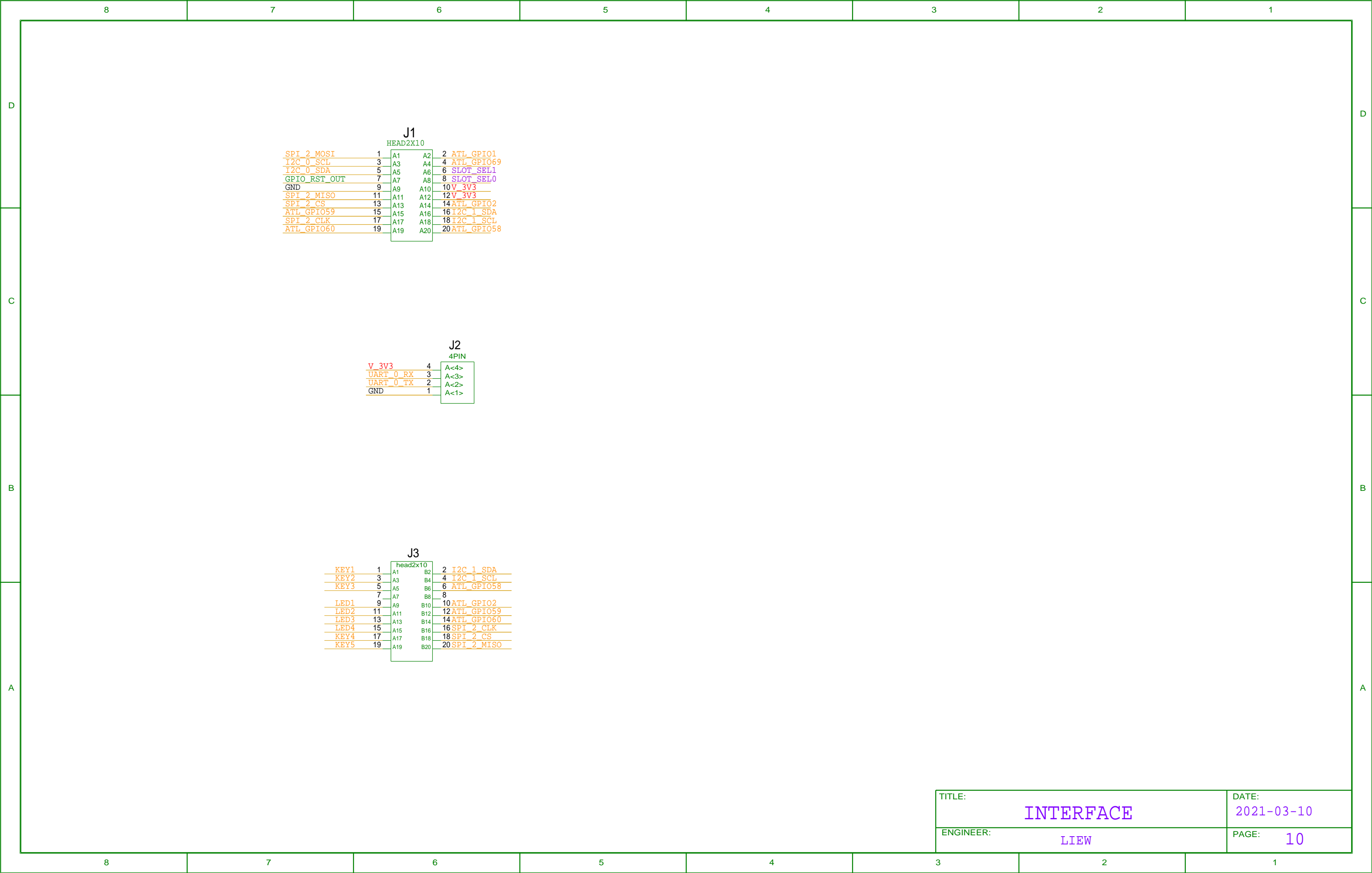


TITLE:	PERIPHERALS	DATE:	2021-04-01
ENGINEER:	LIEW	PAGE:	07





TITLE: LEVEL CONVERSION	DATE: 2021-03-10
ENGINEER: LIEW	PAGE: 09



TITLE:

INTERFACE

ENGINEER:

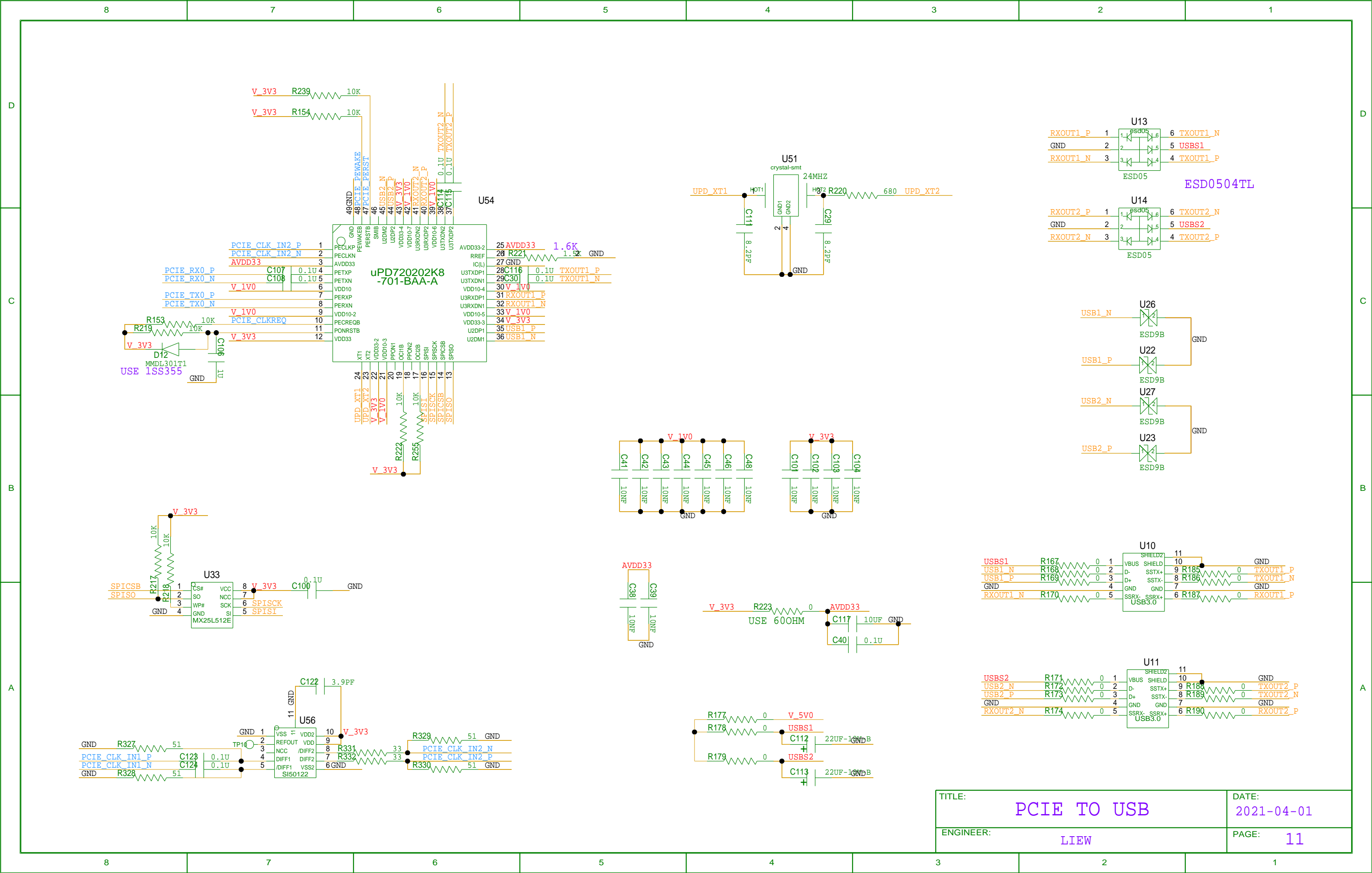
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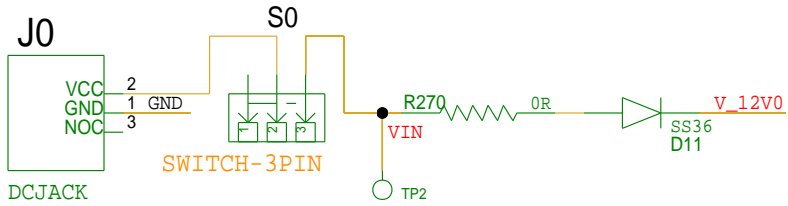
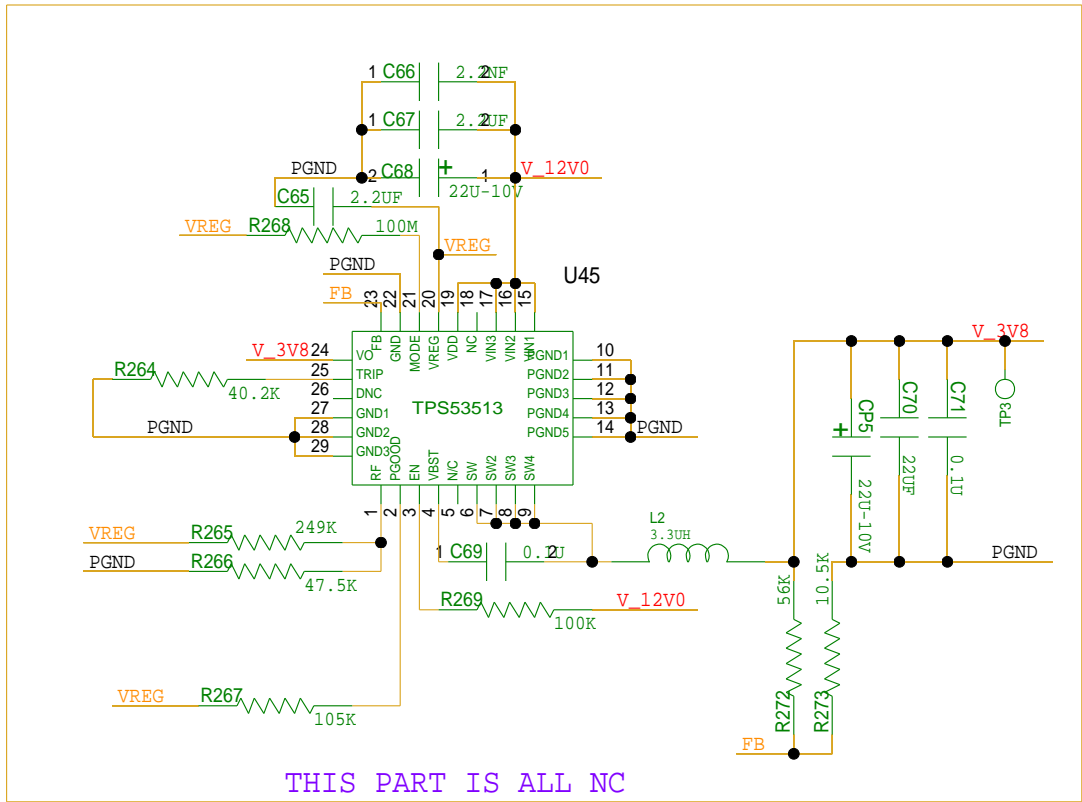
DATE:

2021-03-10

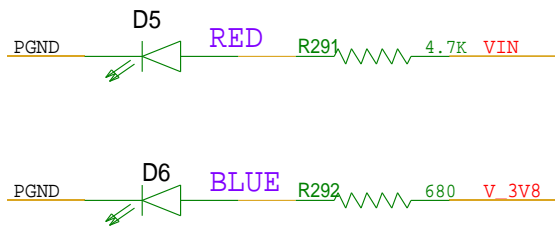
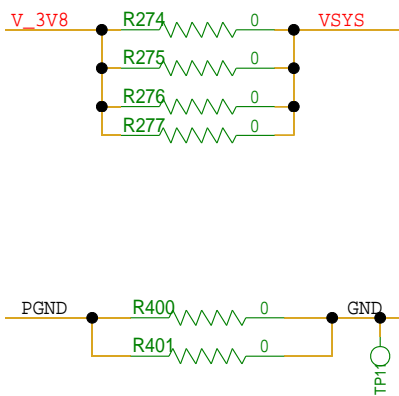
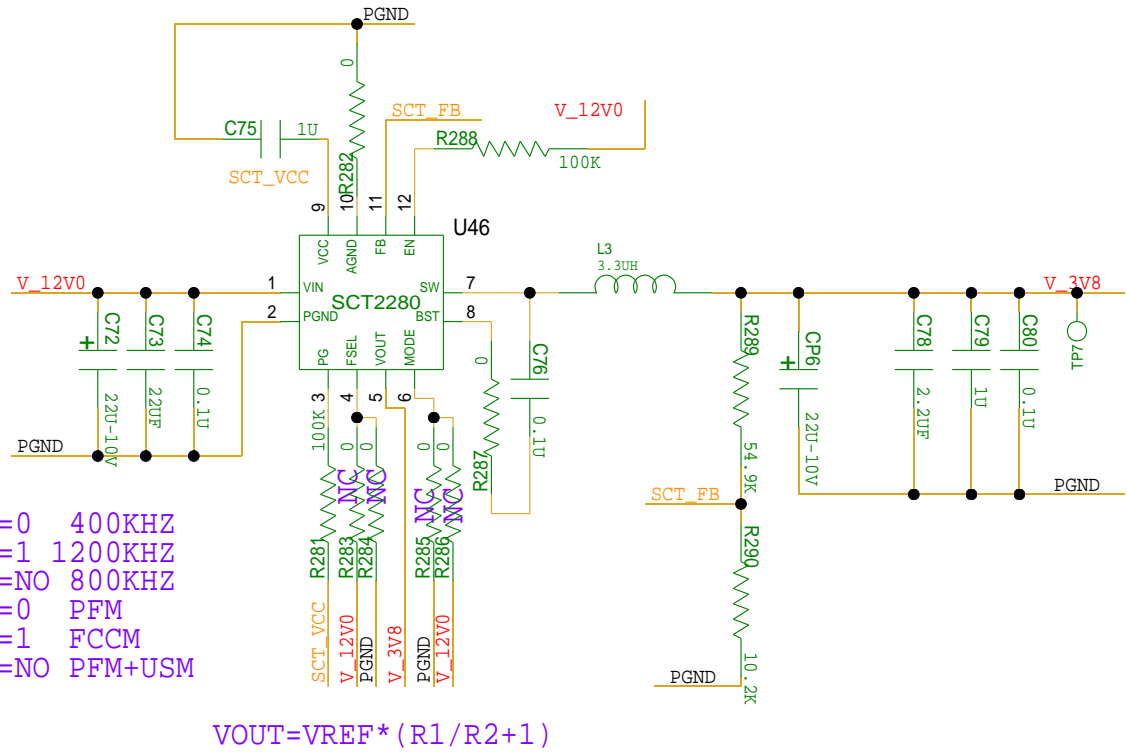
PAGE:

10

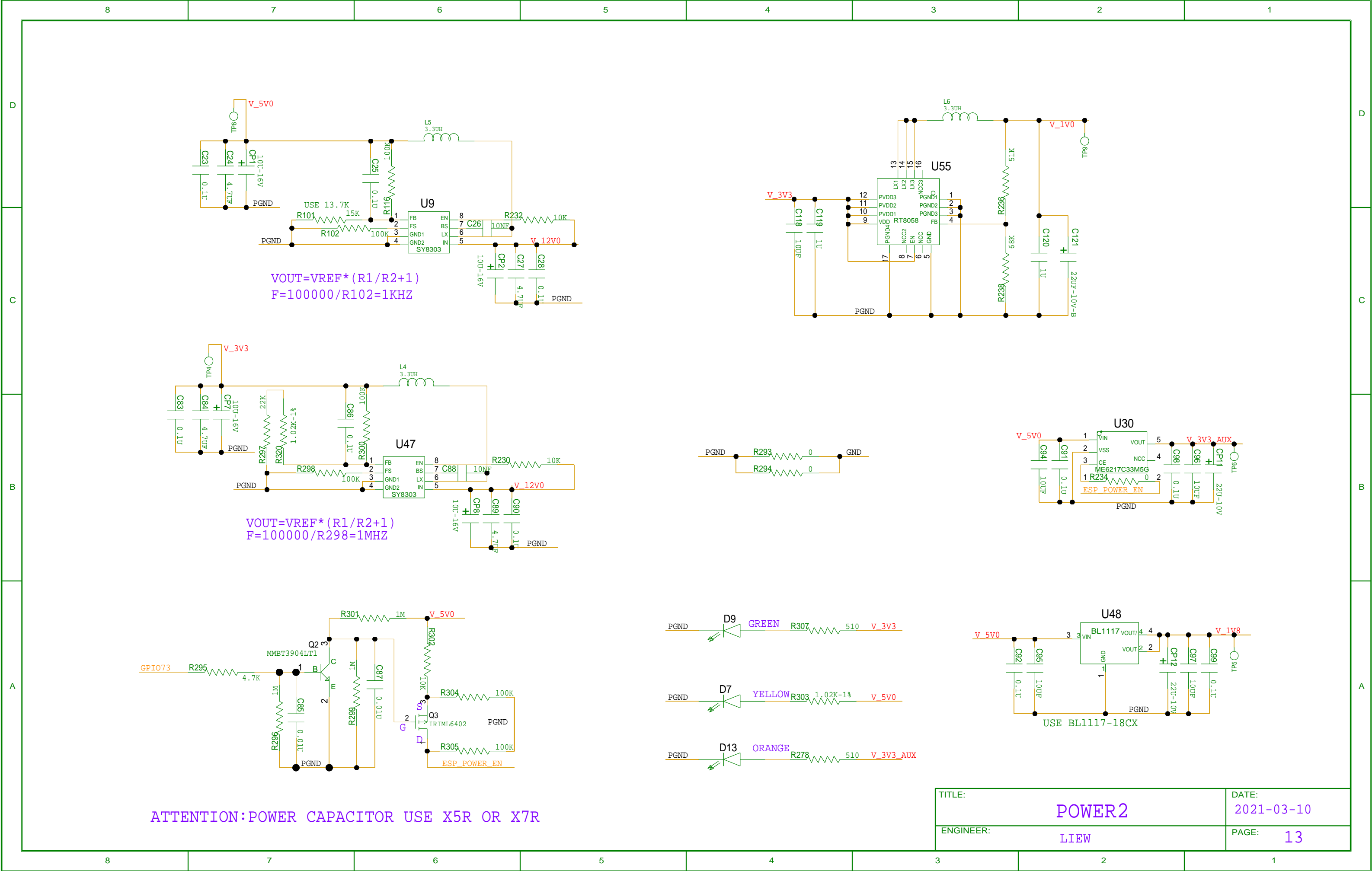




ATTENTION:POWER CAPACITOR USE X5R OR X7R



TITLE:	POWER1	DATE:	2021-03-10
ENGINEER:	LIEW	PAGE:	12

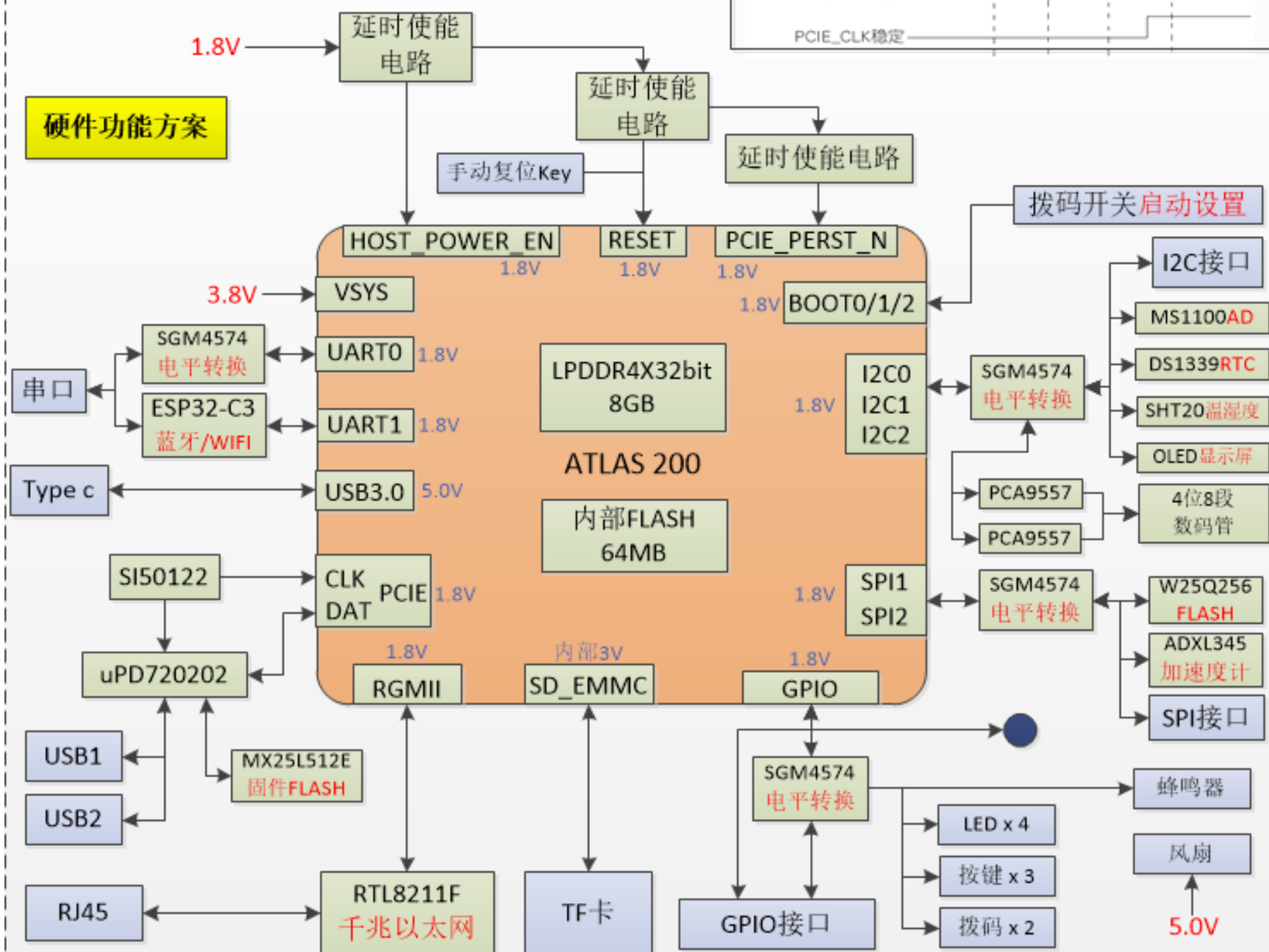


典型功耗 (22TOPS)	典型功耗 (16TOPS)	典型功耗 (8TOPS)	典型功耗 (4TOPS)
11.0W	9.5W	9.0W	7.0W

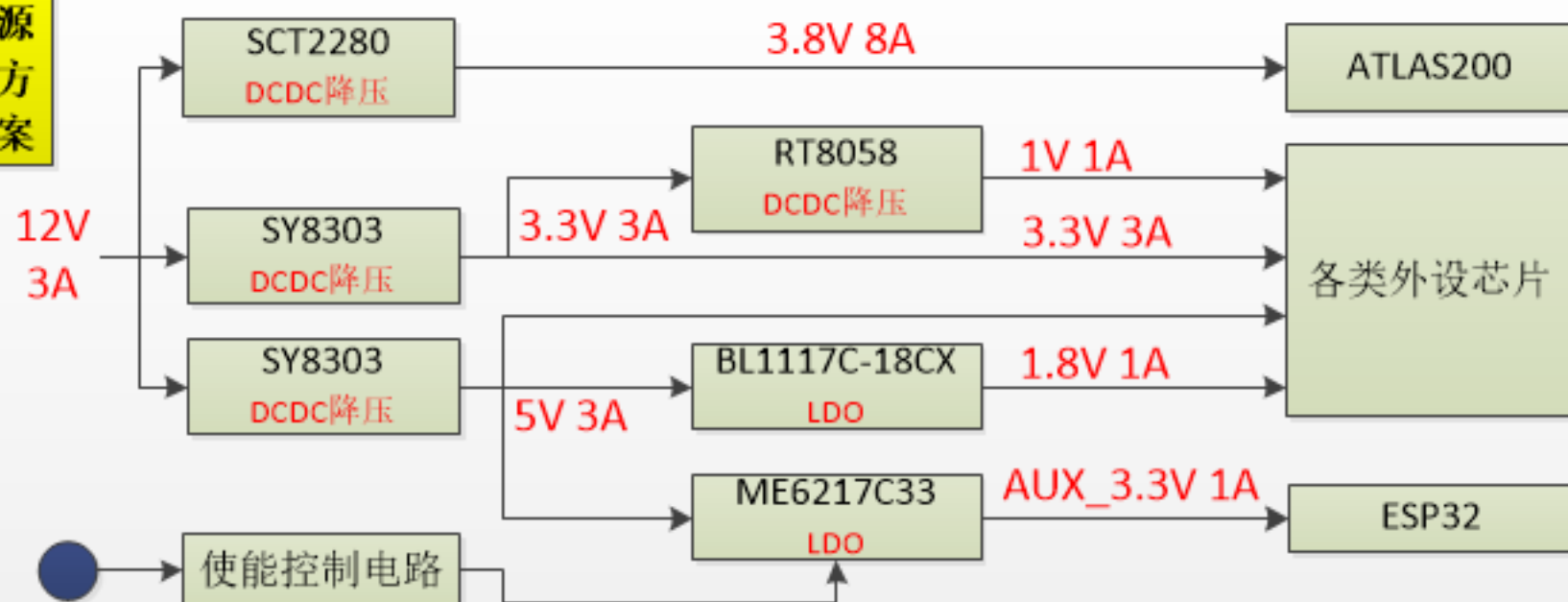
(可不控)



1.8V



12V



Atlas200Dev
HV2.0
硬件方案框图
20210820
By Liew