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# The choochoos Kernel

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# **Project Structure**

### Navigating the Repo

choochoos follows a fairly standard C/C++ project structure, whereby all header files are placed under the include directory, with corresponding implementation files placed under the src directory.

At the moment, both the choochoos kernel and userspace live under the same include and src directory, though we plan to separate kernel-specific files and userspace-specific files at some point.

### Building choochoos

For details on building the project, see the README.md at the root of this repository.

#### **Architectural Overview**

Our entry point is not actually main, but \_start defined in src/boilerplate/crt1.c. \_start does couple key things for the initialization of the kernel: - The link register is stored in the redboot\_return\_addr variable, so we can exit from any point in the kernel's execution by jumping directly to redboot\_return\_addr - The BSS is zeroed - All global variable constructors are run

Then we jump into main, which configures the COM2 UART and jumps to kmain.

kmain executes our scheduling loop, using a singleton instance of a class called Kernel:

```
int kmain() {
    kprintf("Hello from the choochoos kernel!");
    kern.initialize(FirstUserTask);

    while (true) {
        int next_task = kern.schedule();
        if (next_task < 0) break;
        kern.activate(next_task);
    }

    kprintf("Goodbye from choochoos kernel!");
    return 0;
}</pre>
```

The scheduling loop differs from the one described in lecture only in that Kernel::activate does not return a syscall request to handle. Instead, activate only returns after the syscall has been handled. Nonetheless, the kernel can be broken down into three main parts: scheduling, context switching, and syscall handling.

#### Scheduling

schedule() has a remarkably simple implementation:

```
int schedule() {
    int tid;
    if (ready_queue.pop(tid) == PriorityQueueErr::EMPTY) return -1;
    return tid;
}
```

We have a ready\_queue, which is a FIFO priority queue of Tids. Tasks that are ready to be executed are on the queue, and schedule() simply grabs the next

one.

Our priority queue is implemented as a template class in include/priority\_queue.h. A PriorityQueue<T, N> is a fixed size object containing up to N elements of type T, implemented as a modified binary max-heap. Usually, when implementing a priority queue as a binary heap, the elements within the heap are compared only by their priority, to ensure that elements of higher priority are popped first. However, only using an element's priority does not guarantee FIFO ordering within a priority.

Instead, we extend each element in the priority queue to have a ticket counter. As elements are pushed onto the queue, they are assigned a monotonically increasing ticket. When elements with the same priority are compared, the element with the lower ticket is given priority over the element with the higher ticket. This has the effect of always prioritizing elements that were added to the queue first, and gives us the desired per-priority FIFO.

Using this ticketed binary heap has both drawbacks and benefits over a fixed-priority implementation (be that a vector of queues, or an intrusive linked list per priority). The benefit is that we can permit p priorities in O(1) space instead of O(p) space. This allows us to define priorities as the entire range of int, and will allow us to potentially reliable arithmetic on priorities in the future (however, whether or not we'll need such a thing is yet to be discovered).

The drawback is that we lose FIFO if the ticket counter overflows. Right now, the ticket counter is a size\_t (a 32-bit unsigned integer), so we would have to push 2^32 Tids onto ready\_queue in order to see an overflow. This definitely won't happen in our k1 demo, but it doesn't seem impossible in a longer running program that is rapidly switching tasks. We're experimenting with using the non-native uint64 t, and we will profile such a change as we wrap up k2.

#### Context Switching - Task Activation

Once the scheduler has returned a Tid, the kern.activate() method is called with the Tid. This method updates the kernel's current\_task with the provided Tid, fetch the task's saved stack pointer from its Task Descriptor, and hand the stack pointer to the \_activate\_task Assembly routine.

This assembly routine performs the following steps in sequence: - Saves the kernel's register context onto the kernel's stack (i.e. r4-r12,lr) - r0-r3 are not saved, as per the ARM C-ABI calling convention - Note: this does mean that in the future, when implementing interrupt-handling into our kernel, this routine will either have to be modified and/or supplemented by a second routine which saves / restores the entirety of a user's registers. - Switches to System mode, banking in the last user tasks's SP and LR - Moves the new task's SP from r0 to SP - Pops the saved user context from the SP into the CPU - Pops the SWI return value into r0, the SWI return address into r1, and the user SPSR into r2 - Registers r4-12 and LR are restored via a single ldmfd instruction (r0)

through r3 are *not* restored, as the ARM C ABI doesn't require preserving those registers between method calls) - Moves the saved SPSR from r2 into the SPSR register - Calls the movs instruction to jump execution back to the saved swi return address (stored in r1), updating the CPSR register with the just-updated SPSR value

The task will then continue its execution until it performs a syscall, at which point the "second-half" of context-switching is triggered.

#### Context Switching - SWI Handling

Invoking a syscall switches the CPU to Supervisor mode, and jumps execution to the SWI handler. Our SWI handler is written entirely in Assembly, and was given the extremely original name swi handler.

This routine is a bit more involved than the <code>\_activate\_task</code> routine, mainly due to some tricky register / system-mode juggling, but in a nutshell, the <code>\_swi\_handler</code> routine preforms the following steps in sequence: - Save the user's r0 through r12 on the user's stack (requires temporarily switching back to System mode from supervisor mode) - Keep a copy of the user's SP in r4, which is used to stack additional data onto the user stack - Save the SWI return address & user SPSR from the <code>Supervisor</code> mode LR and SPSR onto the <code>User</code> mode SP (using the SP saved in r4) - Use the SWI return address offset by -4 to retrieve the SWI instruction that was executed, and perform a bitmask to retrieve it's immediate value (i.e: the syscall number) - Call the <code>handle\_syscall</code> routine (implemented in C) with the syscall number and the user's SP - See the "Handling Syscalls" section below for more details - Push the <code>handle\_syscall</code> return value onto the user stack - Restore the saved kernel context from the kernel stack, returning execution back to the instruction immediately after the most recently executed <code>\_activate\_task</code> call (i.e: back to the <code>kern.activate()</code> method)

The kern.activate() method proceeds to check to see what state the last-executed task is in, and queues it up to be re-scheduled if it's READY. This completes a single context switch, and execution flow is returned back to the scheduler.

#### Handling Syscalls

handle\_syscall switches on the syscall number, calling the corresponing method on the Kernel instance. The user's stack pointer is cast to a SwiUserStack\*, which allows the user's registers (and the rest of their stack) to be read by our C++ code. This allows us to pass parameters to our C++ syscall handlers. Let's enumerate the handlers:

- MyTid() returns the kernel's current\_tid, which is updated to the most recent Tid whenever as task is activated.
- Create(priority, function) determines the lowest free Tid, and constructs a task descriptor in tasks[tid]. The task descriptor is assigned a

stack pointer, and the user stack is initialized by casting the stack pointer to a FreshUserStack\*, and writing to that struct's fields. Notably, we write stack->lr = (void\*)User::Exit, which sets the return address of function to be the Exit() syscall, allowing the user to omit the final Exit(). The task's parent\_tid is the current value of MyTid().

- MyParentTid() looks up the TaskDescriptor by current\_tid, which holds the parent Tid. (The parent Tid is set to the value of current\_tid when
- Yield() does nothing. Since it leaves the calling task in the READY state, the task will be re-added to the ready\_queue in activate().
- Exit() clears the task descriptor at tasks[MyTid()]. This prevents the task from being rescheduled in activate(), and allows the Tid to be recycled in future calls to Create().

### System Limitations

Our linker script, ts7200\_redboot.ld, defines our allocation of memory. Most notably, we define the range of memory space allocated to user stacks from \_\_USER\_STACKS\_START\_\_ to \_\_USER\_STACKS\_END\_\_. Each user task is given 256KiB of stack space, so the maximum number of concurrent tasks in the system is (\_\_USER\_STACKS\_END\_\_ - \_\_USER\_STACKS\_START\_\_) / (256 \* 1024). However, given the variable size of our BSS and data sections (as we change code), we can't compute the optimal number of concurrent tasks until link time. So instead, we hardcode a MAX\_SCHEDULED\_TASKS, and assert at runtime that no task could possibly have a stack outside of our memory space. Currently, this value is set to 48 tasks. The kernel is given 512KiB of stack space.

### New in K2

In K2 we implemented the Send-Receive-Reply mechanism, as well as the Name Server.

### Send-Receive-Reply

SRR is implemented by adding a state field to each TaskDescriptor, which is the following tagged union:

```
struct TaskState {
   enum uint8_t { UNUSED, READY, SEND_WAIT, RECV_WAIT, REPLY_WAIT } tag;
union {
    struct {
    } unused;
    struct {
    } ready;
    struct {
        const char* msg;
    }
}
```

```
size_t msglen;
            char* reply;
            size_t rplen;
            int next;
        } send_wait;
        struct {
            int* tid;
            char* recv_buf;
            size t len;
        } recv_wait;
        struct {
            char* reply;
            size_t rplen;
        } reply wait;
    };
};
```

So each TaskDescriptor can be in any of the states described by tag, at which point the fields in the corresponding struct under the union will be populated. Let's explain each of the states:

- UNUSED: the TaskDescriptor does not represent a running task.
- READY: the task is on the ready\_queue, waiting to be scheduled.
- SEND\_WAIT: the task is waiting to Send() to another task that hasn't called Receive() yet.
- RECV\_WAIT: the task has called Receive(), but no task has sent a message to it yet.
- REPLY\_WAIT: the task called Send() and the receiver got the message via Receive(), but no other task has called Reply() back at the task.

To implement these state transitions, each task has a send\_queue of tasks that are waiting to send to it (and thus, must be in the SEND\_WAIT state). This send queue is built as an intrusive linked list, where the next "pointer" is actually just another Tid, or -1 to represent the end of the list. When a task wants to Send() to another task that is not in RECV\_WAIT, it will be put on the receiver's send queue (denoted by the fields send\_queue\_head and send\_queue\_tail, also Tids), and the sending task will be put in SEND\_WAIT. Note that the send\_wait branch of the union contains everything that the sender passed to Send(), plus the next pointer, if any other task get added to that same send queue.

If a task is in SEND\_WAIT, it can only be blocked sending to one receiving task, so we only need at most one next pointer per task. The result is very memory efficient: by storing one word on each task descriptor, we can support send queues up to length MAX\_SCHEDULED\_TASKS - 1.

Let's step through the two possibilities for an SRR transaction: sender-first and receiver-first.

#### Sender-first

If a sender arrives first, that means the receiver is not in RECV\_WAIT, and therefore does not yet have a recv\_buf to be filled. If this is the case, we add the sender to the end of the receiver's send queue, and put the sender in SEND WAIT.

When the receiver finally calls Receive(), it will see that is has a non-empty send queue. So it will pop the first TaskDescriptor off the front of its send queue, copy the msg into recv\_buf, and transition the sender into REPLY\_WAIT, using the same char\* reply that the sender saved when they went into SEND\_WAIT.

#### Receiver-first

If the receiver arrives first, it checks its send queue. If the send queue is non-empty, then we follow the same procdure as sender-first, and the new sender is placed on the send of the receiver's send queue. If the send queue is empty, the receiver goes into RECV\_WAIT, holding onto the recv\_buf to be copied into once a sender arrives.

When the sender arrives, it notices that the receiver is in RECV\_WAIT, so it writes the msg directly into recv\_buf, wakes up the receiver (transitioning it to READY and pushing it onto the ready\_queue), and goes into REPLY\_WAIT.

#### Reply

Reply(tid, reply, rplen) only delivers the reply if the task identified by tid is in REPLY\_WAIT. The kernel can then immediately copy the reply into the char\* reply stored in the reply\_wait branch of the task's state union. This way, reply never blocks - it only returns an error code if the task is not in REPLY\_WAIT.

#### Error cases

The SRR syscalls return two possible error codes: -1 and -2. -1 is returned whenever a tid does not represent a task - either it is out of range, or it points to a TaskDescriptor in the UNUSED state. -2 is returned in two cases where a SRR transaction cannot be completed:

- If a task tries to Reply() to a task that is not in REPLY\_WAIT this would mean that the task never called Send() and thus is not expecting a reply.
- If a task is in SEND WAIT in a receiver's send queue, and the receiver exits.

When a receiver exits, if it has a non-empty send queue, those tasks will never get a reply because they won't make the transition into REPLY\_WAIT, and thus would never wake up. Instead, we iterate through the send queue, waking ever SEND\_WAIT task up, and writing the syscall return value of -2.

### NameServer

# K2 Output (RPS)

### **Transcript**

```
1 Hello from the choochoos kernel!
2 random seed (>= 0): 2
3 pause after each game (y/n)? n
4 num players (0-32): 3
5 player 1 priority (default 1): 1
6 player 1 num games (default 3): 3
7 player 2 priority (default 1): 2
8 player 2 num games (default 3): 5
9 player 3 priority (default 1): 3
10 player 3 num games (default 3): 3
11 [Client 3] I want to play 3 games. Sending signup...
12 [Client 4] I want to play 5 games. Sending signup...
13 [RPSServer] matching tids 4 and 3
14 [Client 4] received signup ack
15 [Client 4] I want to play 5 more games. Sending scissors...
16 [Client 5] I want to play 3 games. Sending signup...
17 [Client 3] received signup ack
18 [Client 3] I want to play 3 more games. Sending paper...
19 [Client 4] I won!
20 [Client 4] I want to play 4 more games. Sending paper...
21 [Client 3] I lost :(
22 [Client 3] I want to play 2 more games. Sending rock...
23 .....
24 [Client 4] I won!
25 [Client 4] I want to play 3 more games. Sending rock...
26 [Client 3] I lost :(
27 [Client 3] I want to play 1 more game. Sending rock...
28 -----
   [Client 4] it's a draw
29
30 [Client 4] I want to play 2 more games. Sending rock...
31 [Client 3] it's a draw
32 [Client 3] sending quit
33 ......
34 [RPSServer] tid 3 quit, but tid 5 is waiting. Matching tids 4 and 5
35 [Client 5] received signup ack
36 [Client 5] I want to play 3 more games. Sending scissors...
37 [Client 3] exiting
38 [Client 4] I won!
39 [Client 4] I want to play 1 more game. Sending scissors...
40 [Client 5] I lost :(
```

# Explanation