

UNIVERSITÄT HEIDELBERG
INSTITUTE FOR COMPUTER ENGINEERING
(ZITI)

MASTER OF SCIENCE COMPUTER ENGINEERING
PARALLEL COMPUTER ARCHITECTURE

Exercise 5

Group 04
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Due date 2019-12-10, 14:00

5.1 Matrix-Multiplikation revisited

5.1.1 Parallele Implementierung mittels PThreads

@todo: code

5.1.2 Experimente und Evaluation

a.

Tabelle 1: Ausführungszeit $t_{compute}$ (s)

Threads \ n	2	3	4	5	6
1					
2					
4					
8					
16					
32					

Tabelle 2: Ausführungszeit t_{wall} (s)

Threads \ n	2	3	4	5	6
1					
2					
4					
8					
16					
32					

b.

@todo: complete

5.2 Vektorrechner - Memory Interleaving

a.

@todo: answer

b.

@todo: answer