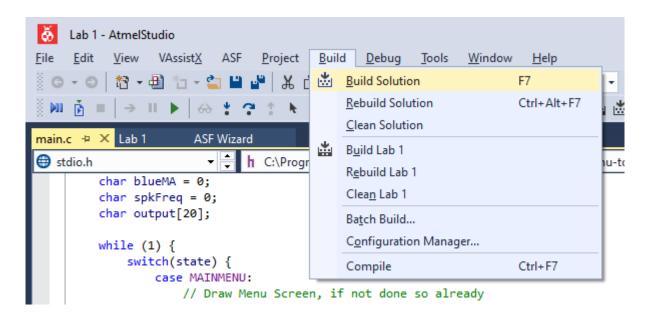
# Checking your Code



Use the Build function (F7) in ATMEL studio to check for simple errors:

- Syntax Errors
- Undeclared Variables / Variables not in scope
- Incorrect use of functions

## Ways to check Inputs

#### A. By using Polling

- A protocol for how to check for inputs
- What you have been using up till now
- Requires constant checking ("polling") of inputs
  - A waste of CPU cycles, impacting performance
  - Hinderance in Real Time Applications

#### B. By implementing Interrupts

- A hardware mechanism of the μC
- CPU only used when interrupts are triggered
- Allows for instantaneous actioning of data
- Interrupts any current instruction of your application
  - Make sure you consider how your interrupt may impact application in run-time (hence, keep your interrupts short)

### Interrupt Vectors Table

Use this table to identify the vectors you need to use:

- Find the subsystem you need (ex. USART)
- Find the trigger you want to use (ex. Rx Complete)

Ensure you enable the appropriate interrupt registers for the device (ex. RXCIE Flag in UCSR1B)

No 1	Address		
4			
	\$0000	RESET_vect	Reset
2	\$0002	INT0_vect	External Interrupt Request 0
3	\$0004	INT1_vect	External Interrupt Request 1
4	\$0006	INT2_vect	External Interrupt Request 2
5	\$0008	INT3_vect	External Interrupt Request 3
6	\$000A	INT4_vect	External Interrupt Request 4
7	\$000C	INT5_vect	External Interrupt Request 5
8	\$000E	INT6_vect	External Interrupt Request 6
9	\$0010	INT7_vect	External Interrupt Request 7
10	\$0012	PCINT0_vect	Pin Change Interrupt Request 0
11	\$0014	USB_General_vect	USB General Interrupt request
12	\$0016	USB_Pipevect	USB Endpoint/Pipe Interrupt request
13	\$0018	WDT vect	Watchdog Time-out Interrupt
14	\$001A	TIMER2 COMPA vect	Timer/Counter2 Compare Match A
15	\$001C	TIMER2 COMPB vect	Timer/Counter2 Compare Match B
16	\$001E	TIMER2 OVF vect	Timer/Counter2 Overflow
17	\$0020	TIMER1 CAPT vect	Timer/Counter1 Capture Event
18	\$0022	TIMER1_COMPA_vect	Timer/Counter1 Compare Match A
19	\$0024	TIMER1 COMPB vect	Timer/Counter1 Compare Match B
20	\$0026	TIMER1 COMPC vect	Timer/Counter1 Compare Match C
21	\$0028	TIMER1_OVF_vect	Timer/Counter1 Overflow
22	\$002A	TIMER0_COMPA_vect	Timer/Counter0 Compare Match A
23	\$002C	TIMER0 COMPB vect	Timer/Counter0 Compare match B
24	\$002E	TIMER0 OVF vect	Timer/Counter0 Overflow
25	\$0030		
26	\$0032	USART1 RX vect	USART1 Rx Complete
27	\$0034	OOMINIT_ODINE_VOOL	OO/ATT F Data Trogistor Empty
28	\$0036	USART1_TX_vect	USART1 Tx Complete
29	\$0038		inning Companies
30	\$003A	ADC vect	ADC Conversion Complete
31	\$003C	EE READY vect	EEPROM Ready
32	\$003E	TIMER3 CAPT vect	Timer/Counter3 Capture Event
33	\$0040	TIMER3_COMPA_vect	Timer/Counter3 Compare Match A
34	\$0042	TIMER3 COMPB vect	Timer/Counter3 Compare Match B
35	\$0044	TIMER3 COMPC vect	Timer/Counter3 Compare Match C
36	\$0046	TIMER3 OVF vect	Timer/Counter3 Overflow
37	\$0048	TWI vect	2-wire Serial Interface
38	\$004A	SPM_READY_vect	Store Program Memory Ready

### Using Interrupts in your Program

- Include the <avr/interrupt.h> library
- Create Interrupt Service Routines (ISRs) using the function ISR()
  - Make sure you also declare the function definition
  - Ensure that you pass in the name of the interrupt vector used
  - Write the code inside the function that you want to use
- Enable Interrupts using the sei() function, and setting the appropriate registers.

```
// Include the Interrupt Library
#include <avr/interrupt.h>

// Declare the Function Definition
    for the ISR
ISR(Your Vector's Name);

// Write the code to run within
    your interrupt
ISR(Your Vector's Name)
{
        // Your Code Here
}

// Enable Interrupts in your
    setup() function
sei();
```

### Example: Receiving Data over USART

This code takes the 8-bit value received over the USART and displays on PORTC:

The example is the basic outline to show you how vectors can be used.

- Use the USART1\_RX\_vect
- Enable Interrupts in setup()
  - Using the sei() function
- Create Interrupt Service Routines (ISRs) using the function ISR()
  - Stores the value from the USART
  - Writes to PORTC
- Ensure interrupts are enabled on the USART registers
  - UCSR1B: RXCIE

```
ISR(USART1 RX vect);
void setup(void);
int main (void)
    setup();
// Every Time USART recieves data, show on PORTC
ISR(USART1 RX vect)
    char valueToDisplay = UDR1;
    PORTC = valueToDisplay;
void setup(void)
    DDRC = 0xFF;
   UCSR1B = 0b10010000;
   UCSR1C = 0b00000110;
   UBRR1L = 8;
   // Enable Interrupts in your setup() function
    sei();
```