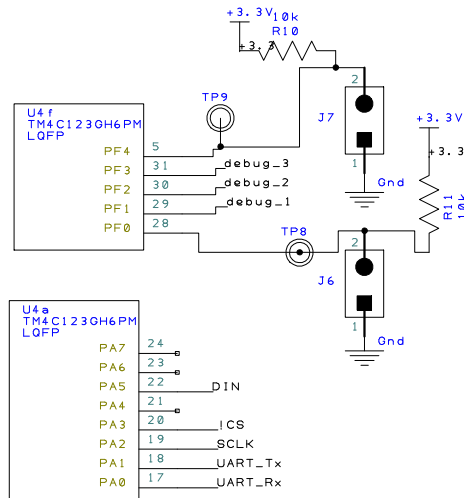
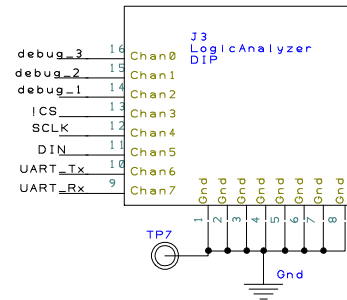


## TM4C Peripheral Connections

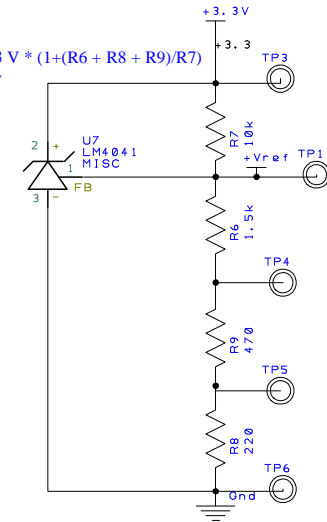


## Logic Analyzer Connections



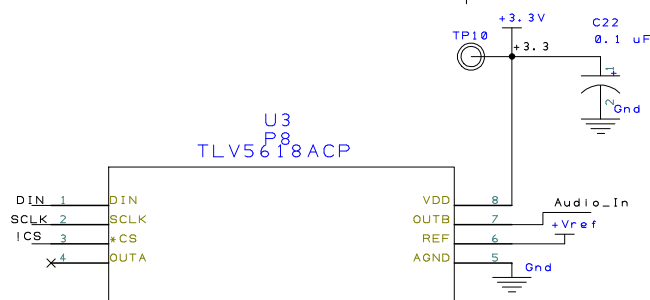
$$V_{ref} = 1.233 \text{ V} * (1 + (R6 + R8 + R9)/R7)$$

$$V_{ref} = 1.5 \text{ V}$$

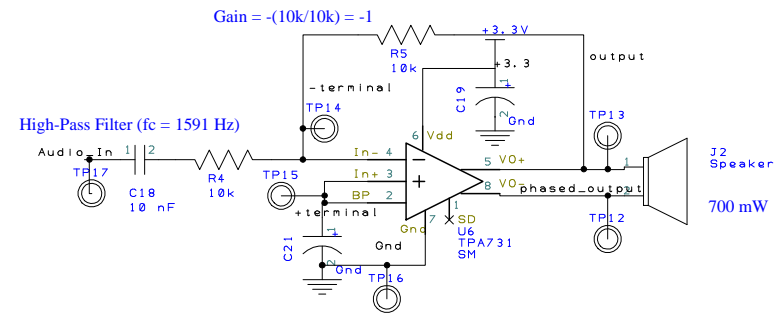


## 1.5V Reference for DAC

## Digital to Analog Converter



## Audio Amplifier Circuit and Speaker



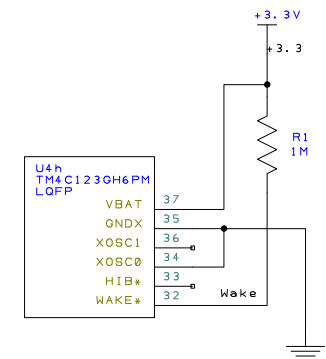
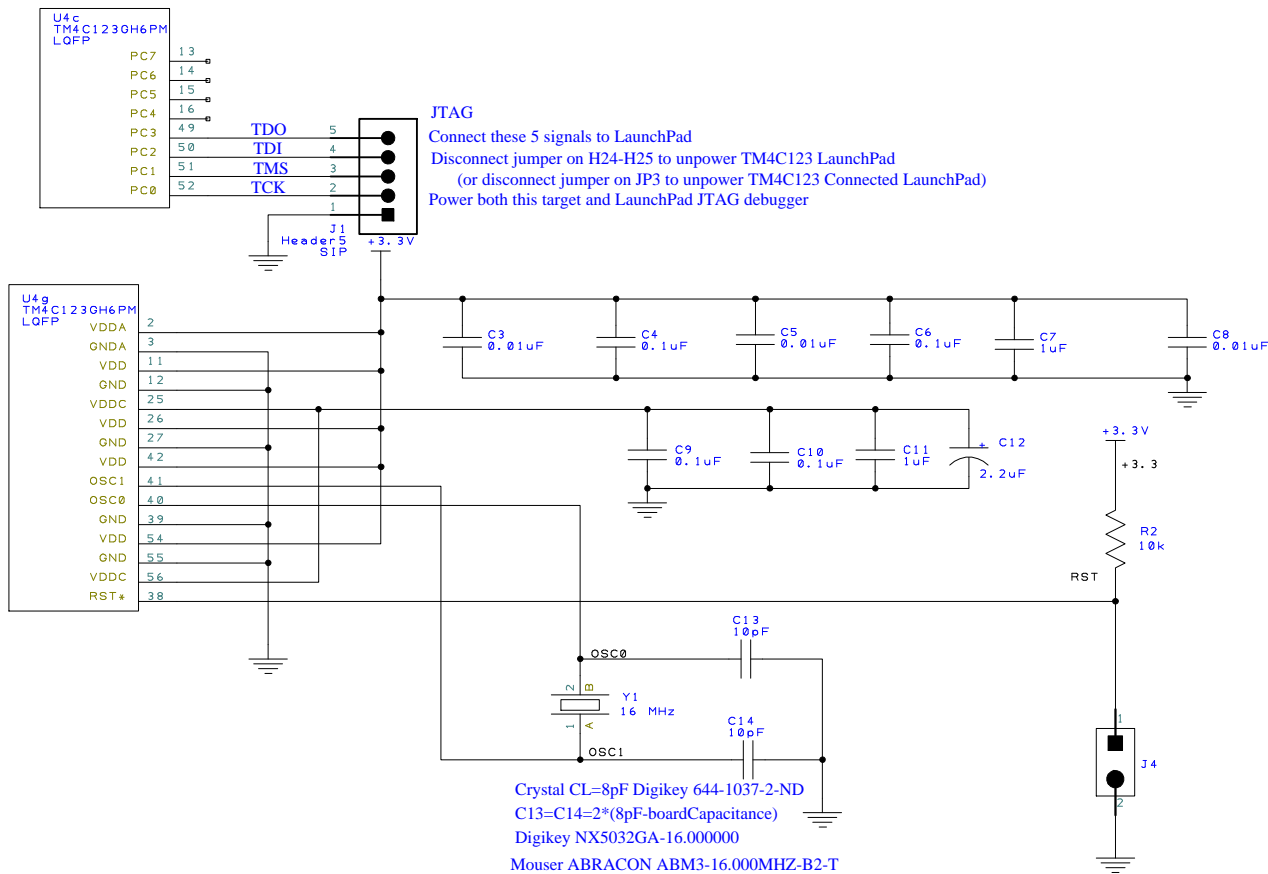
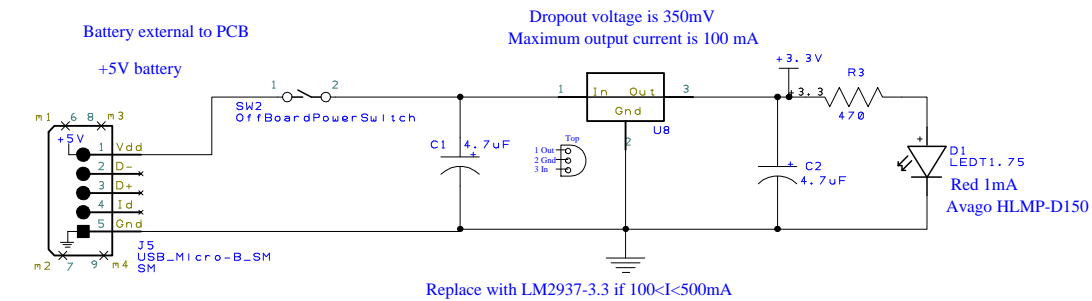
University Of Texas At Austin

Schematic Name: Lab 6 starter

Name(s): Daniel Diamont

Date: 2/27/2018

Semester: Spring 2018



University Of Texas At Austin

Schematic Name: Lab 6 starter file Power Clock JTAG

Name(s): Daniel Diamont

Date: 2/27/2018

Semester: Spring 2018