

Daniele Laganà

COMPUTER SCIENCE AND ENGINEERING STUDENT

Milan, Italy

☎ (+39) 3334211858 | ✉ danielelagana@outlook.it | 🏠 www.danielelagana.tech | 📷 danielelagana | 🌐 danielelagana

Summary

High-energy student currently pursuing a MSc in Computer Science and Engineering specializing in Artificial Intelligence while developing new skills and gaining real-world experience. Highly organized and responsible with strong communication and critical thinking skills. Available for relocation.

Education

Politecnico di Milano

MSC IN COMPUTER SCIENCE AND ENGINEERING

Track: Artificial Intelligence (T2I)

Milan, Italy

Sept. 2023 - Current

Università della Calabria

BSC IN COMPUTER ENGINEERING

- Grade: 102/110
- Thesis: "Outlier Detection for Time Series: models and techniques"

Milan, Italy

Sept. 2019 - Sept. 2023

Liceo Scientifico "Leonardo Da Vinci"

SCIENTIFIC HIGH SCHOOL DIPLOMA

- Grade: 98/100

Reggio Calabria, Italy

Sept. 2014 - June 2019

Software Skills

Programming	Python, Java, C, Shell, LaTeX
Database	SQL, Neo4j, MongoDB, Elasticsearch, Redis, Spark, Cassandra
Libraries	scikit-learn, pandas, numpy, matplotlib, tensorflow

Soft Skills

Personal Skills	Teamwork and Collaboration, Flexible and Adaptable, Problem Solving, Communication
Languages	Italian (mother tongue), English (TOEIC® C1)

Projects

Credit card fraud detection using ML Techniques

NUMERICAL ANALYSIS FOR ML PROJECT

- Evaluated machine learning techniques for credit card fraud detection, addressing data imbalance using **SMOTE** and under-sampling. Compared models like **SVM, KNN, Random Forest, and ensemble methods**, with the proposed model showing superior accuracy and efficiency. Future improvements include deep learning and dynamic data sampling for better fraud detection scalability.

4-operand circuit using VHDL

DIGITAL ELECTRONICS PROJECT

- Developed a 4-operand pipelined circuit with two clock cycles of latency using both behavioral and structural **VHDL** approaches. Implemented on Zedboard Zynq, achieving high efficiency and synchronization with minimal resource usage. Simulated the design at 8-bit and 16-bit, ensuring optimal performance through timing analysis and power consumption evaluation.

LTI System Design and Stability Analysis

FOUNDATIONS OF AUTOMATICS PROJECT

- Analyzed continuous-time LTI systems by deriving transfer functions, identifying poles/zeros, and verifying **BIBO** stability. Simulated system responses (impulse, step, ramp) and performed frequency analysis using Bode plots. Used MATLAB to optimize control strategies for system performance.

Work Experience

Liceo Scientifico "Leonardo Da Vinci"

DIGITAL ARCHIVING ASSISTANT - HIGH SCHOOL APPRENTICESHIP PROGRAMME

- Used Microsoft Word and other software tools to digitally catalogue materials from the school laboratories.
- Collaborated with team members to achieve target results.

Reggio Calabria, Italy

October 2016 - May. 2018