

Sub Circuits

adc

blame: saleh sabti

File: adc.kicad_sch

lora_module

blame: daniel monahan

File: lora_module.kicad_sch

power_management

blame: manaf alali

File: power_management.kicad_sch

Sheet: /
File: lora_daq_module.kicad_sch

Title:

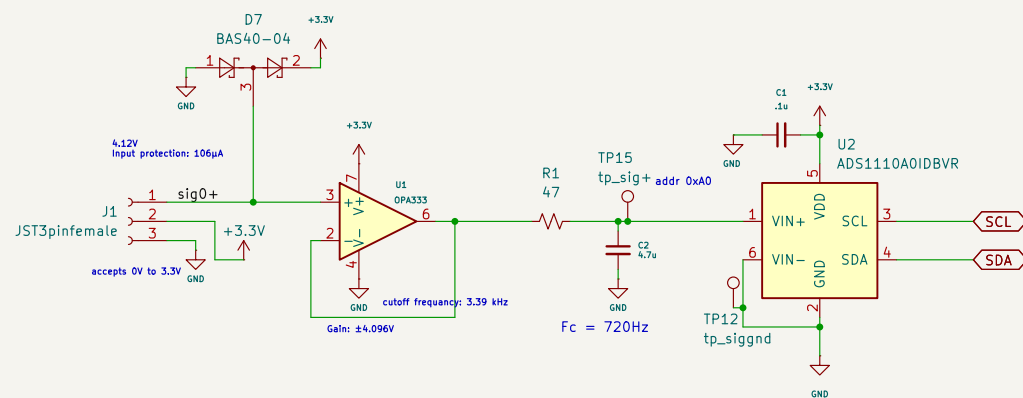
Size: A4

Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 1/4



Sheet: /adc/
File: adc.kicad_sch

Title:

Size: A4

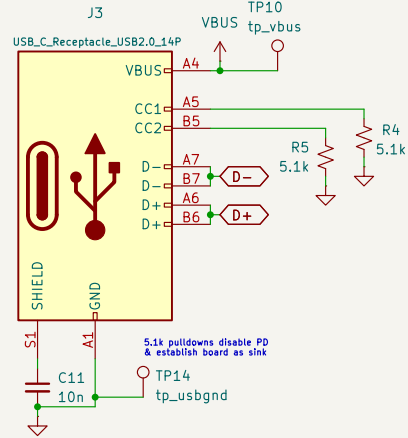
Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 2/4

USB Input and UART Bridge

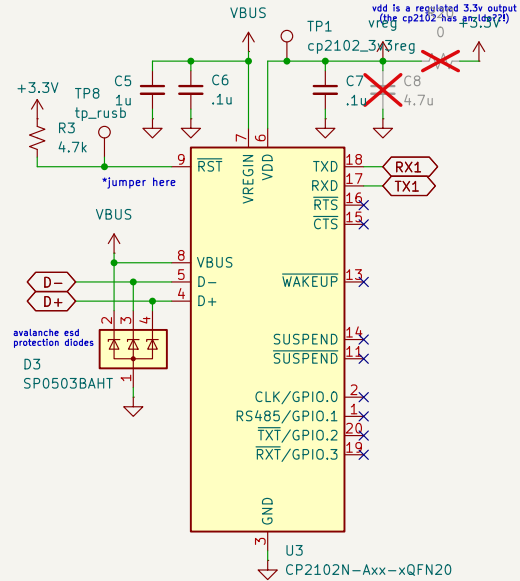


*add subghz, make sure xtal pins match wio-e5, add swi interface

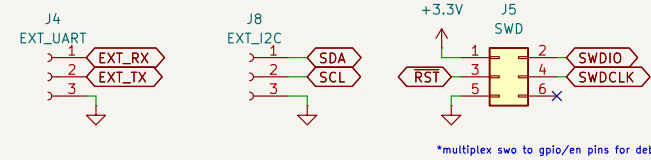
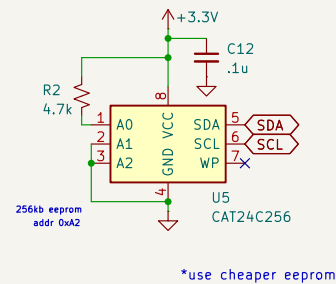
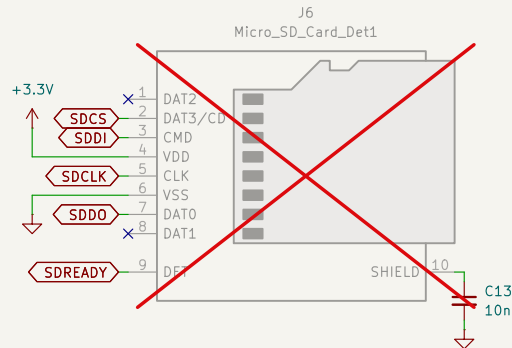
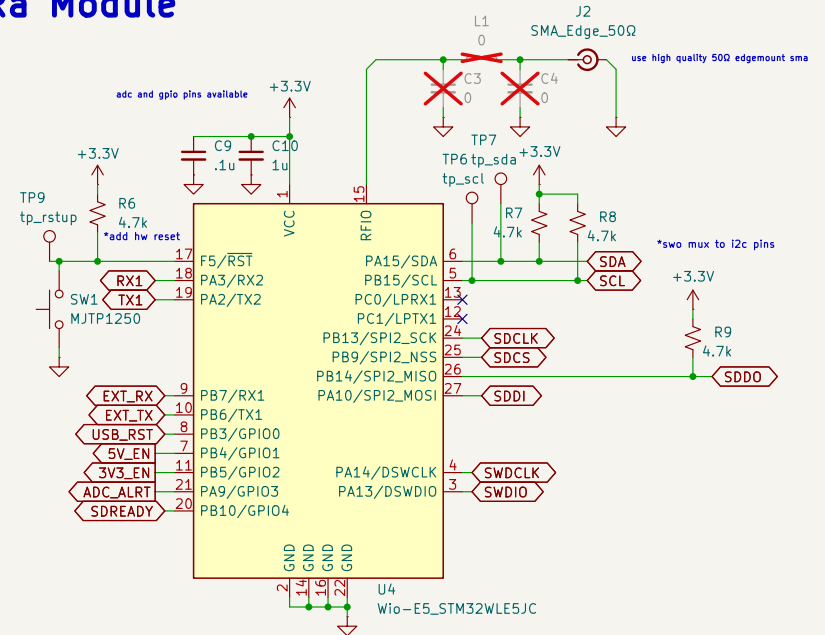
*add connector and spi input for SD card

*connect gpio for 3.3en, 5en

*match pinout to table 1 on datasheet



LoRa Module



drawn by daniel monahan
ECE Capstone Team 14 at Portland State University

Sheet: /lora_module/
File: lora_module.kicad_sch

Title: lora module subcircuit

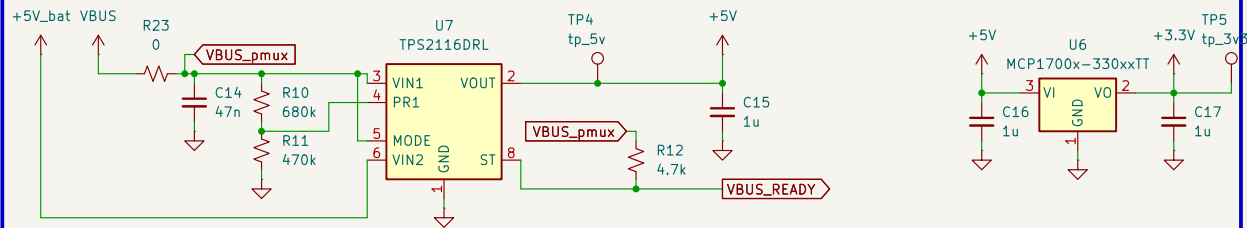
Size: A4 Date: 2025-11-11

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Rev: rev0

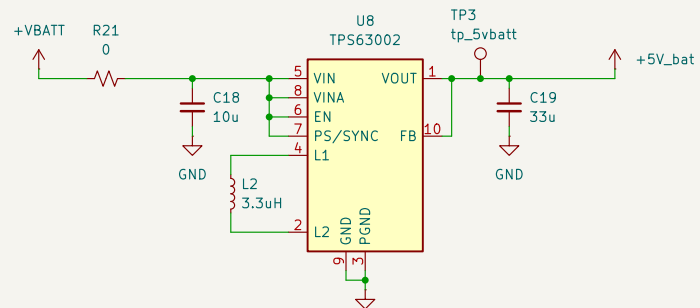
Id: 3/4

Power Supply

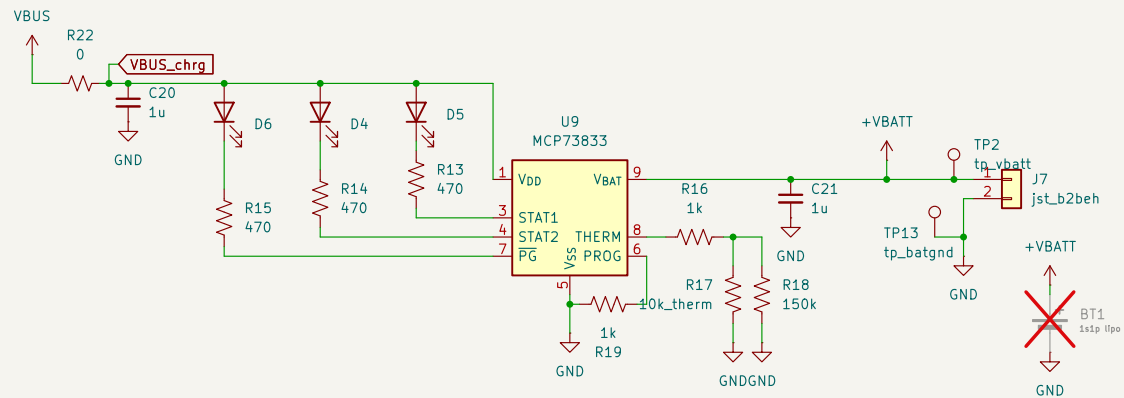


Battery Management

Boost



Charger



Drawn by Eyad Ahmed
Drawn by Manaf alali

Sheet: /power_management/
File: power_management.kicad_sch

Title: *Power management schematic*

Size: A4	Date: 2025-10-26
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Rev: rev0

Id: 4/4