

Lab # 05 Task

1. Design and implement the circuitry for a Half adder and simulate it on Logically.
2. Implement a half adder using AND, OR and NOT gates only and simulate it on Logically.
3. Design and implement the circuitry of a full adder and simulate it on Logically.
4. Implement the half adder using NAND Gates only and simulate it on Logically.
5. Design and implement the circuitry for 2 bit binary number addition on Logically.

Add the following two bit binary numbers on Logically.

A = 11, B = 10

Bonus Task

1. Design and implement the circuitry for 4 bit binary number addition on Logically.

Add the following four bit binary numbers on Logically.

A = 1011, B = 1001