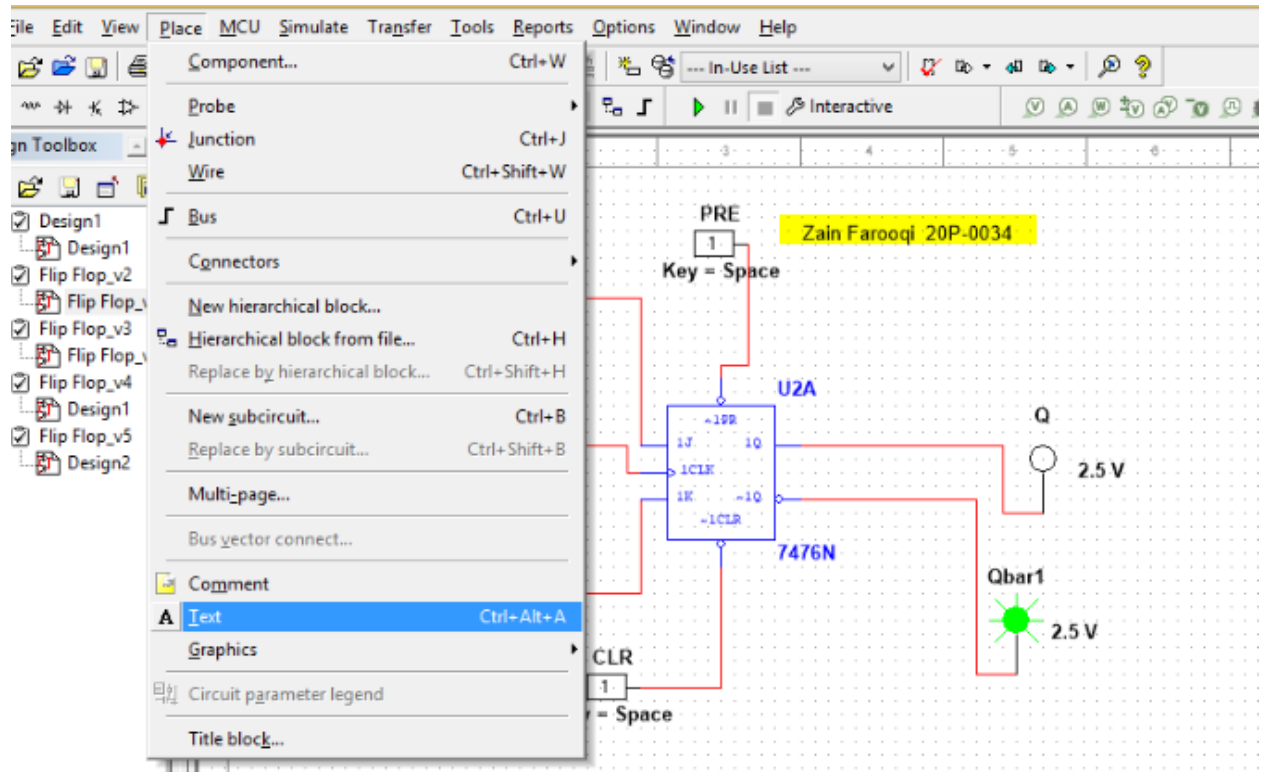# EL227- Digital Logic Design Lab

Lab # 12 FlipFlops

- **Use Multism to design circuit diagrams.**
- **Label your inputs and outputs properly on Multism.**
- Note: Attach your name and roll no with each screen shot of circuit as shown below. Otherwise your lab task would not be acceptable.

1. Test the functionality of an edge triggered D-flipflop using IC-74LS74.
2. Test the functionality of JK-flipflop using IC-74LS76.
3. Implement the SR Flip Flop using NOR and AND logic gates.