

EL227- Digital Logic Design Lab

Lab # 11 Latches

- Use **Multism** to design circuit diagrams.
 - Label your inputs and outputs properly on Multism
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1. Implement SR-latch using NOR-gates.
 2. Implement SR-latch using NAND-gates.
 3. Implement SR-latch with enable using NAND-gates.
 4. Implement D-latch using NAND-gates.
 5. Elaborate the difference between Combinational and Sequential Circuits.