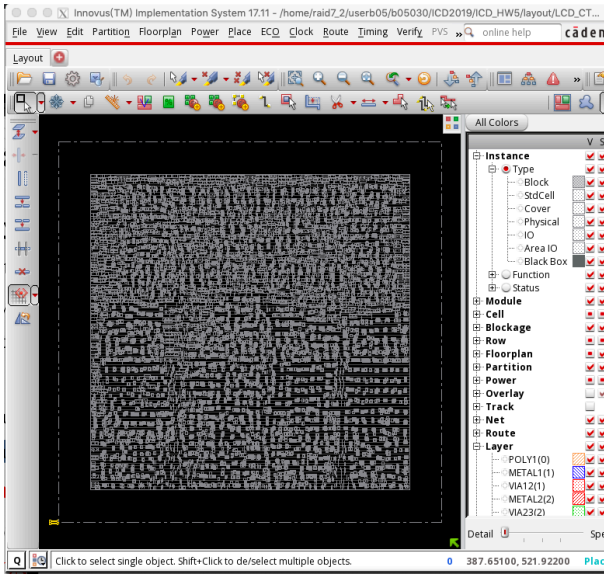
Integrated Circuit Design
Homework #5 Innovus Laboratory Exercise

Report

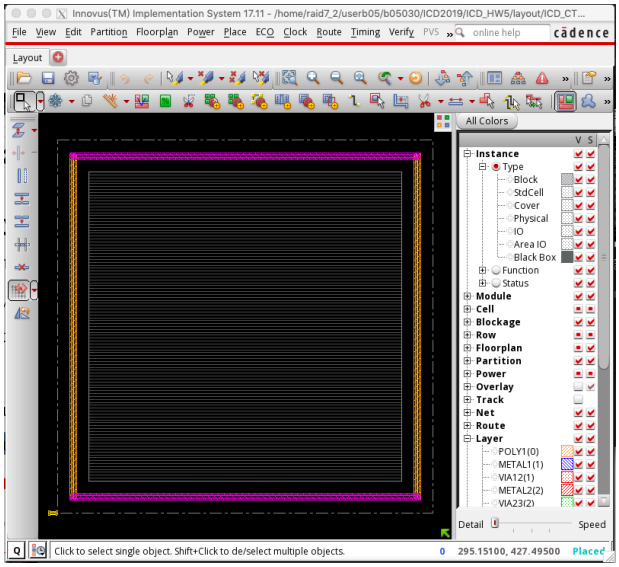
B05901030 電機三 陳欽安

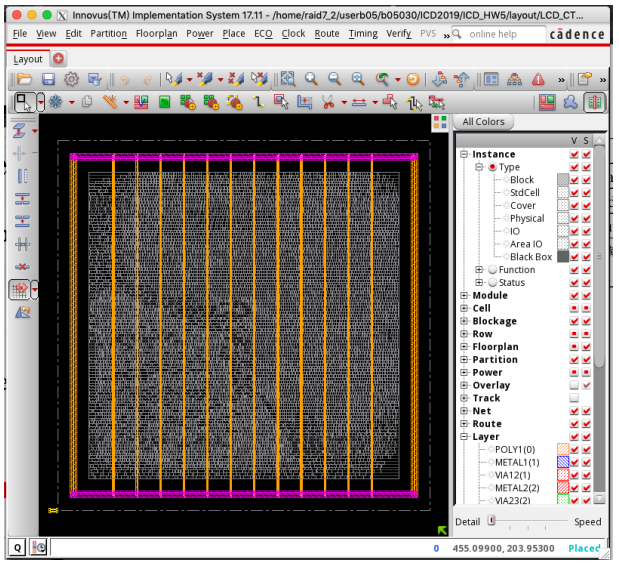
I. Innovus 步驟截圖

A. *Floorplan*

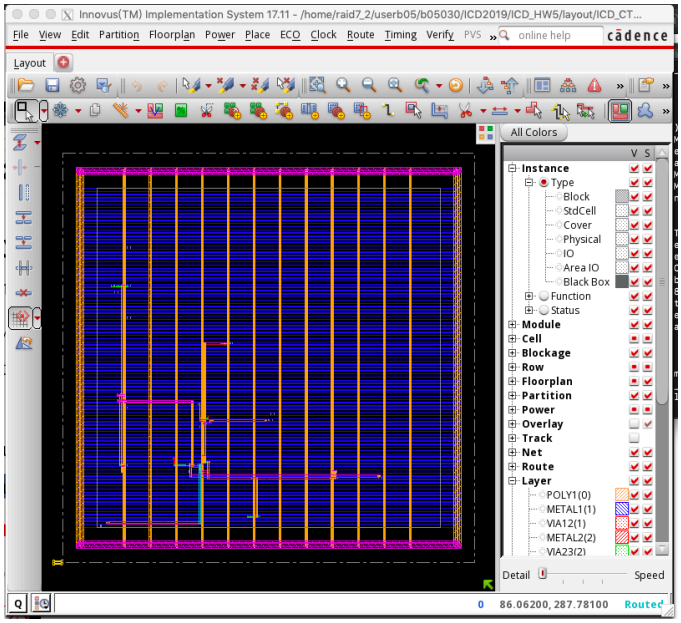


B. *Power ring (left) & Power stripe (right)*

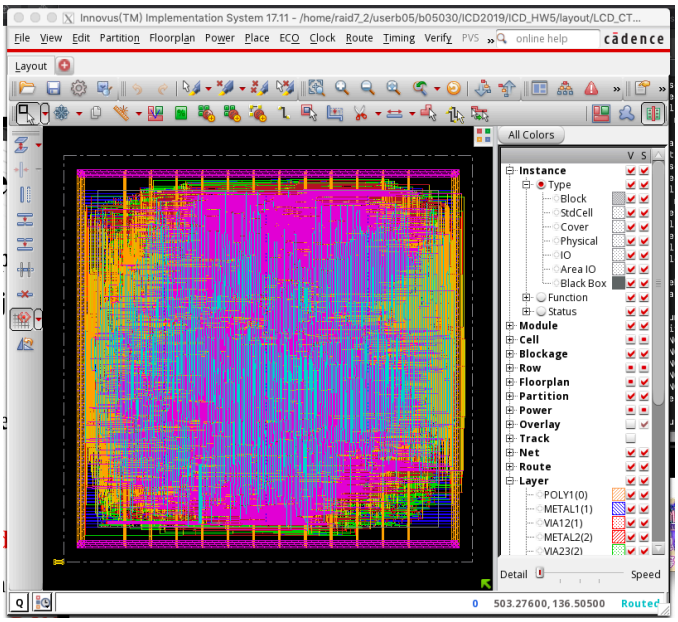




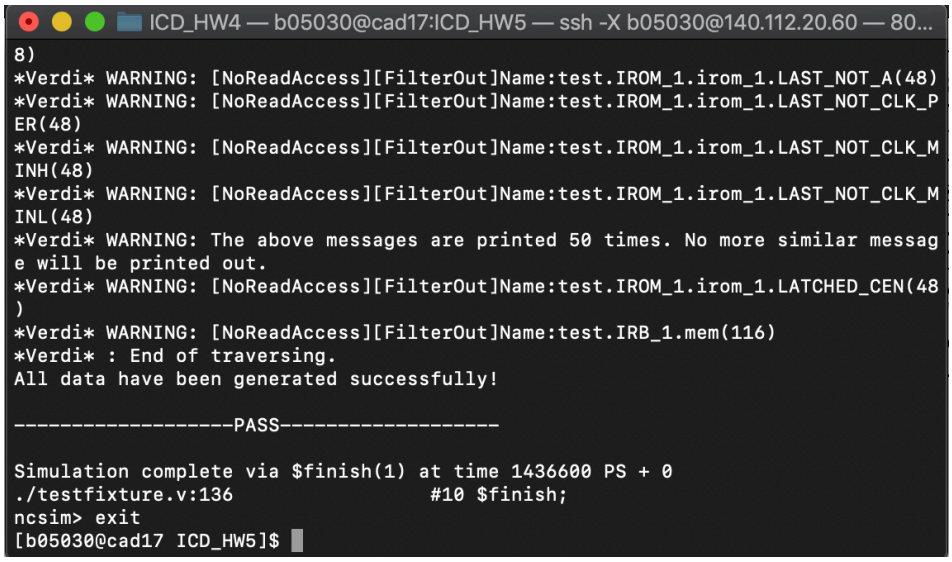
C. ***Special Route***



D. ***NanoRoute***



II. Post-APR simulation 通過截圖



III.通過 Post-APR simulation的cycle time

Cycle time = 10.0 ns

testfixture.v

```
`timescale 1ns/10ps
`define CYCLE    10.0                          // Modify your clock period here
```

lcd_ctrl_APR.v

```
# You can only modify clock period


set cycle 10
```