

5	Table of Contents
1	TITLE, TOC & REV
2	NTAG I2C XQFN16 ANT
3	APPENDIX-JUMPER/SWITCH/DNP

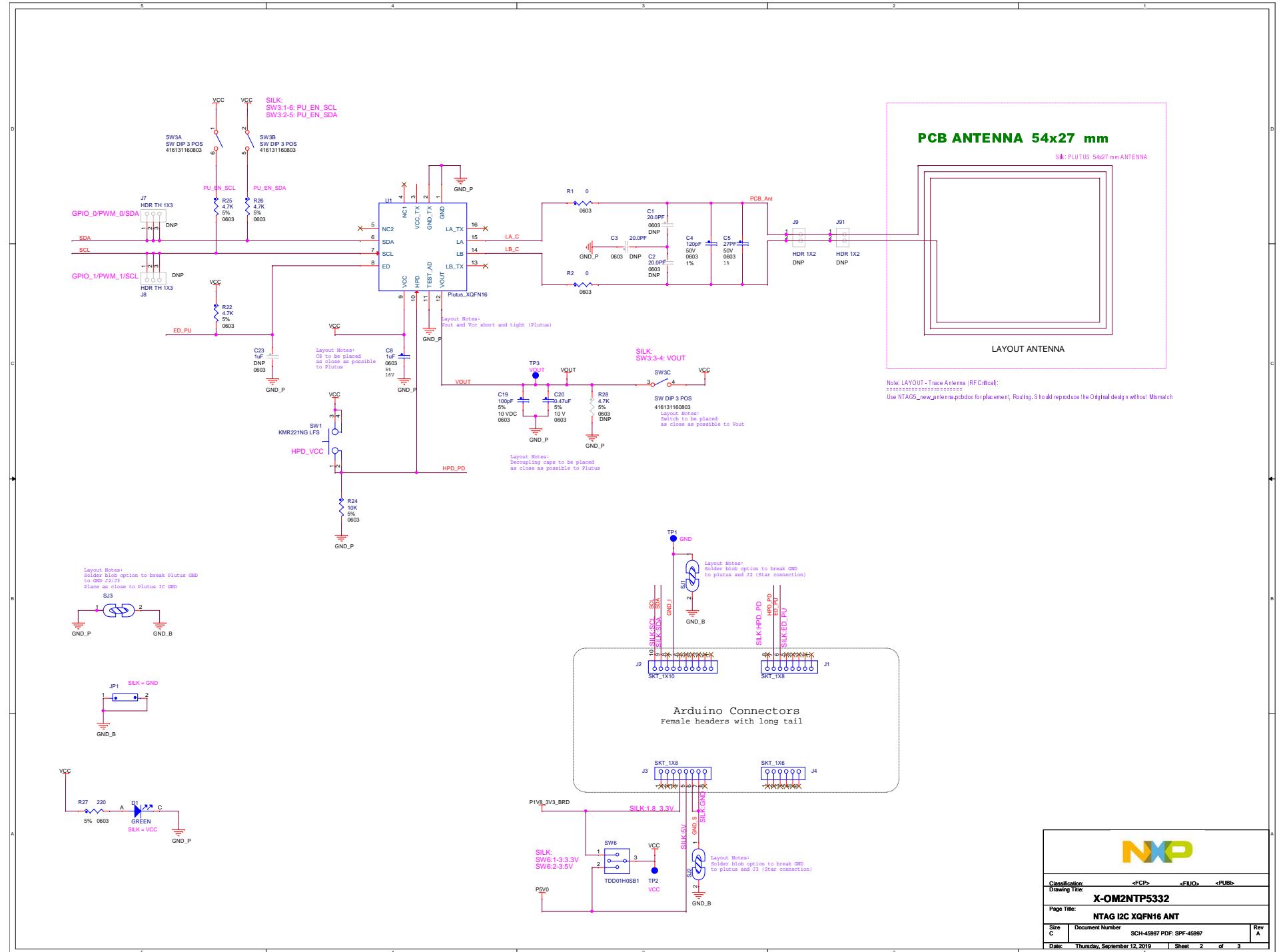
Revision History

Rev	Description	Date	Approved
X1	First Draft Schematics updated as per reference schematic mail from Peter J11,J12, J26, J25 jumpers changed to switches J9,J91 as DNP; R28 changed to 0805; BoM update J14 jumper option added for GND; Layout notes added C19,C20 changed to 10V parts; J14 removed, instead solder blobs added(option to break gnd) C4 = 120pF & C5 = 27pF updated; SW6 updated A055 Release; A070 Release	26-Aug-2019 28-Aug-2019 29-Aug-2019 30-Aug-2019 03-Sep-2019 04-Sep-2019 04-Sep-2019 05-Sep-2019	Vijay Vijay Vijay Vijay Vijay Vijay Vijay Vijay
X2	GND_P and GND_B Separated; R22,R25,R26,R28 updated; SW6 :pin-1 and pin-2 swapped	09-Sep-2019	Vijay
A	Silk added for SW6 and D1 A085 (Final Schematic, BoM) , Prototype Release	11-Sep-2019	Vijay

X-OM2NTP5332

NTAG 5 Link - Development Expansion Board

		NXP, Bangalore 6501 William Cannon Drive West Austin, TX 78735-8598
This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.		
(C) NXP SEMICONDUCTORS Classification: <FCP> <FIJO> <PUBB>		
Designer: Vijay	Drawing Title:	X-OM2NTP5332
Drawn by: Bharat	Page Title:	TITLE, TOC & REV
Approved: Vijay C	Size C	Document Number SCH-45997 PDF: SPF-45997
		Rev A
	Date: Wednesday, September 11, 2019	Sheet 1 of 3



DNP TABLE:

REF DES	ASSY_OPT	PAGE NAME
J7,C1,C23,J8,C2,J9,R28,J91,C3	DNP	02-NTAG I2C XQFN16

JUMPER TABLE:

REF DES	JUMPER(DEFAULT)	JUMPER(OPTION1)	PAGE NAME
SJ3,SJ1,SJ2	SHORT	OPEN	02-NTAG I2C XQFN16

SWITCH TABLE:

REF DES	SWITCH(DEFAULT)	SWITCH(OPTION1)	SWITCH(OPTION2)	PAGE NAME
SW6	OFF: DISABLE VCC <> ARDUINO	VCC <> P1V8_3V3_BRD	VCC <- P5V0 ARDUINO	02-NTAG I2C XQFN16
SW3	OFF: DISABLE VCC	ON: ENABLE VCC		02-NTAG I2C XQFN16

