

FPGA Synthesis Results

Design Name	Cyclone IV GX
Target FPGA Device	EP4CGX150DF31I7AD
Total Logic Elements	17,348 / 149,760 (12%)
Total Combinational Functions	12,185 / 149,760 (8%)
Dedicated Logic Registers	12,218 / 149,760 (8%)
Total Registers	12,218
FMax (Slow 1200mV 100°C model)	103.85 MHz
Total Thermal Power Dissipation	430.20 mW
Core Dynamics	209.62 mW
Core Static	119.86 mW
I/O	100.72 mW
Peak Throughput	12.8 GOPS/s (0.0128 TOPS/s)
Energy Efficiency	61.0 GOPS/W (0.061 TOPS/W)

Notes:

- Power estimated with PowerPlay Power Analyzer using vectorless estimation and a 20% default input activity factor.