

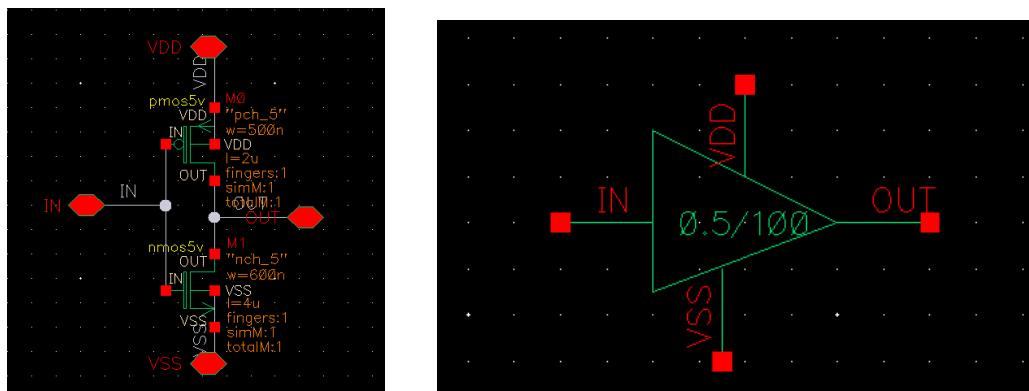
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This document demonstrates an example of the schematic-related part in the assignments. Please also add some descriptions, results and screenshots etc, to make your report complete.

1. Details of the inverter:

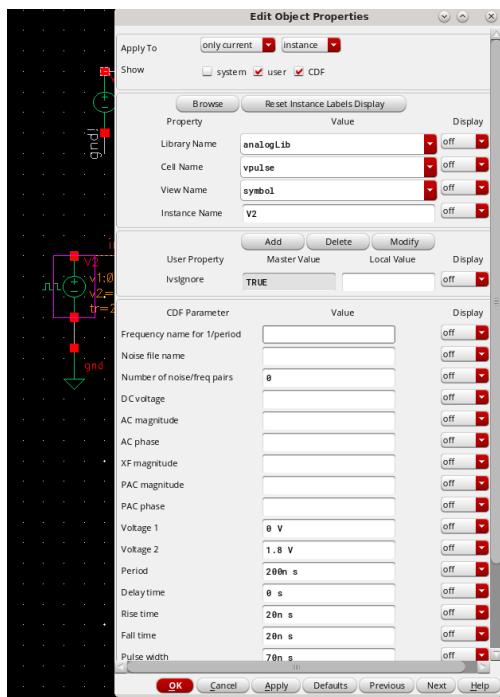
PMOS: width-- length--
NMOS: width-- length--

2. Schematic screenshot about your inverter and symbol:



3. Screenshot about your test-bench

4. Input signal screenshot (press q to show the input property) and power supply:



5. Screenshot of output signal: show the rising and falling time of your inverter:

