This document is an example of layout-related part on your report.
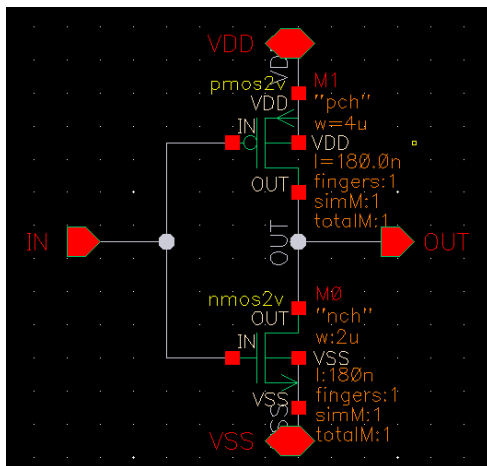
Here are some points that you must include.

Welcome to add some notes, descriptions and results beside them, to make your report complete and easy to understand.

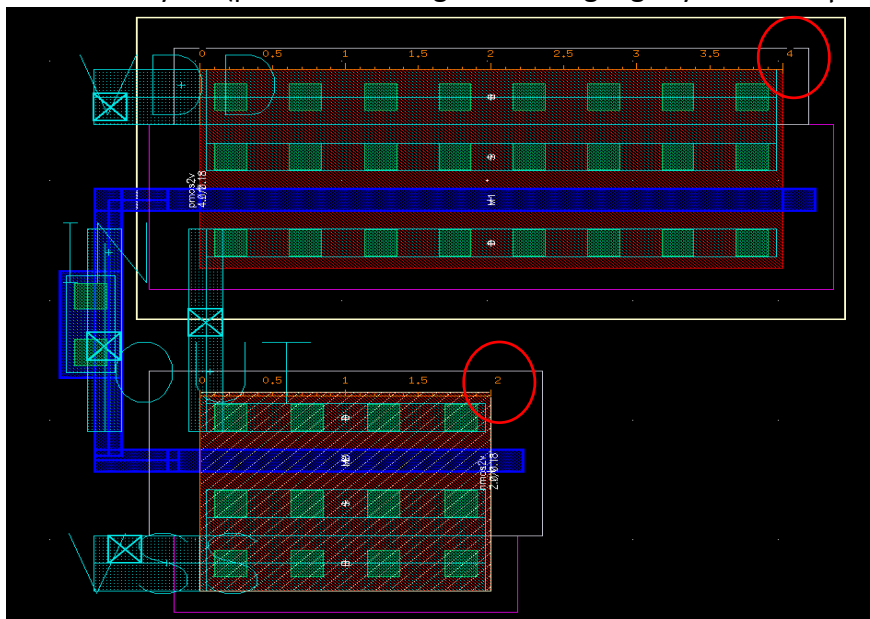1. Details of the inverter:
   PMOS: width--   length--
   NMOS: width--   length--

2. Schematic screenshot about your inverter and symbol (the same schematic as in your assignment 1):

3. Show the layout (press 'k'→ using ruler to highlight your size: 4µm & 2µm):

4. Show the final DRC errors: