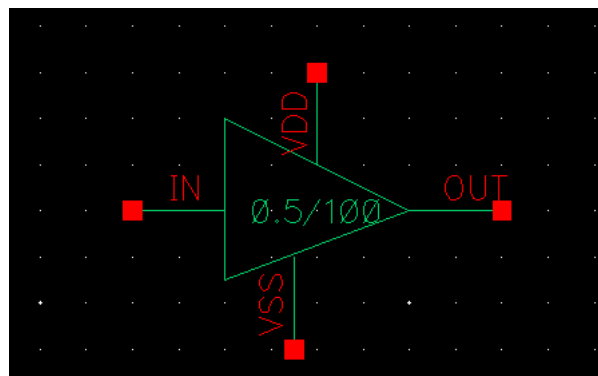
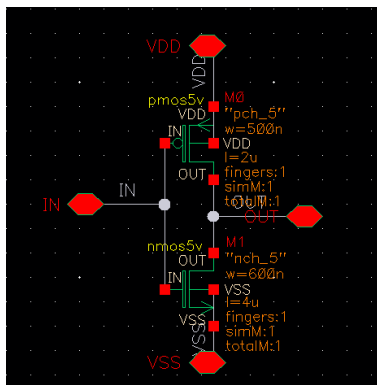


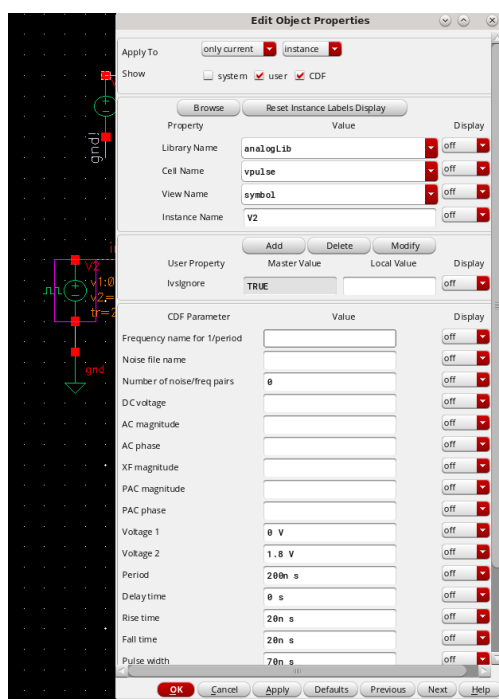
Student No.	NetID	Last Name	First Name	Email	Group
123456	abc	efg	abc	adc@tudelft.nl	1

*This document demonstrates an example of the schematic-related part in the assignments.  
Please also add some descriptions, results and screenshots etc, to make your report complete.*

- Details of the inverter:
  - PMOS: width-- length--
  - NMOS: width-- length--
- Schematic screenshot about your inverter and symbol:



- Screenshot about your test-bench
- Input signal screenshot (press q to show the input property ) and power supply:



5. Screenshot of output signal: show the rising and falling time of your inverter:

