PCB

Board size: 65.75x37.75 mm (2.59x1.49 inches)

• This is the size of the rectangle that contains the board

• Thickness: 1.6 mm (63 mils)

Material: FR4Finish: NoneLayers: 2

• Color: Green

Silk screen: TOP / BOTTOM

• Color: White

Stackup:

Name	Туре	Color	Thickness	Material	Epsilon_r	Loss tangent
F.SilkS F.Paste F.Mask F.Cu dielectric 1	Top Silk Screen Top Solder Paste Top Solder Mask copper core	00101	10 35 1510	FR4	4.5	0.020
B.Cu B.Mask B.Paste B.SilkS	copper Bottom Solder Mask Bottom Solder Paste Bottom Silk Screen		35 10	1101		0.020

Important sizes

Clearance: 0.2 mm (8 mils)

Track width: 0.25 mm (10 mils)

• By design rules: 0.0 mm (0 mils)

Drill: 0.5 mm (20 mils)

- Vias: 0.5 mm (20 mils) [Design: 0.4 mm (16 mils)]
- Pads: 1.1 mm (43 mils)
- \bullet The above values are real drill sizes, they add 0.1 mm (4 mils) to plated holes (PTH)

Via: 0.8/0.4 mm (31/16 mils)

• By design rules: 0.5/0.3 mm (20/12 mils)

- Micro via: yes [0.2/0.1 mm (8/4 mils)]
- Buried/blind via: yes
- Total: 5 (thru: 5 buried/blind: 0 micro: 0)

Outer Annular Ring: 0.15 mm (6 mils)

• By design rules: 0.3 mm (12 mils)

Eurocircuits class: 4B - Using min drill $0.5~\mathrm{mm}$ for an OAR of $0.15~\mathrm{mm}$

General stats

Components count: (SMD/THT)

- Top: 16/4 (SMD + THT)
 Bottom: 0/0 (NONE)
- Defined tracks:

Used tracks:

- 0.25 mm (10 mils) (36) defined: no
- 0.5 mm (20 mils) (15) defined: no
- 1.5 mm (59 mils) (45) defined: no

Defined vias:

Used vias:

- 0.8/0.4 mm (31/16 mils) (Count: 3, Aspect: 2.0 A) defined: no
- 1.4/0.8 mm (55/31 mils) (Count: 2, Aspect: 1.1 A) defined: no

Holes (excluding vias):

- 1.0 mm (39 mils) (4)
- 1.2 mm (47 mils) (4)

Oval holes:

Drill tools (including vias and computing adjusts and rounding):

- 0.5 mm (20 mils) (3)
- 0.9 mm (35 mils) (2)
- 1.1 mm (43 mils) (4)
- 1.3 mm (51 mils) (4)