

## PCB

Board size: 84.5x92.0 mm (3.33x3.62 inches)

- This is the size of the rectangle that contains the board
- Thickness: 1.6 mm (63 mils)
- Material: FR4
- Finish: None
- Layers: 4
- Copper thickness: 35  $\mu$ m

Solder mask: TOP / BOTTOM

- Color: Green

Silk screen: TOP / BOTTOM

- Color: White

Stackup:

Name	Type	Color	Thickness	Material	Epsilon	Loss tangent
F.SilkS	Top Silk Screen					
F.Paste	Top Solder Paste					
F.Mask	Top Solder Mask		10			
F.Cu	copper		35			
dielectric 1	prepreg		100	FR4	4.5	0.020
In1.Cu	copper		35			
dielectric 2	core		1240	FR4	4.5	0.020
In2.Cu	copper		35			
dielectric 3	prepreg		100	FR4	4.5	0.020
B.Cu	copper		35			
B.Mask	Bottom Solder Mask		10			
B.Paste	Bottom Solder Paste					
B.SilkS	Bottom Silk Screen					

## Important sizes

Clearance: 0.2 mm (8 mils)

Track width: 0.2 mm (8 mils)

- By design rules: 0.0 mm (0 mils)

Drill: 0.4 mm (16 mils)

- Vias: 0.4 mm (16 mils) [Design: 0.4 mm (16 mils)]
- Pads: 1.1 mm (43 mils)
- The above values are real drill sizes, they add 0.1 mm (4 mils) to plated holes (PTH)

Via: 0.6/0.3 mm (24/12 mils)

- By design rules: 0.5/0.3 mm (20/12 mils)
- Micro via: yes [0.2/0.1 mm (8/4 mils)]
- Buried/blind via: yes
- Total: 12 (thru: 12 buried/blind: 0 micro: 0)

Outer Annular Ring: 0.1 mm (4 mils)

- By design rules: 0.23 mm (9 mils)

Eurocircuits class: 6C - Using min drill 0.35 mm for an OAR of 0.13 mm

## General stats

Components count: (SMD/THT)

- Top: 14/3 (SMD + THT)
- Bottom: 0/13 (THT)

Defined tracks:

Used tracks:

- 0.2 mm (8 mils) (163) defined: no

Defined vias:

Used vias:

- 0.6/0.3 mm (24/12 mils) (Count: 12, Aspect: 2.7 A) defined: no

Holes (excluding vias):

- 1.0 mm (39 mils) (46)
- 1.1 mm (43 mils) (49)
- 2.7 mm (106 mils) (4)
- 3.0 mm (118 mils) (4)

Oval holes:

- 2.0x2.8 mm (79x110 mils) (2)

Drill tools (including vias and computing adjusts and rounding):

- 0.4 mm (16 mils) (12)
- 1.1 mm (43 mils) (46)
- 1.2 mm (47 mils) (49)
- 2.1 mm (83 mils) (2)
- 2.8 mm (110 mils) (4)
- 3.0 mm (118 mils) (4)