

Microcontrolled based systems

Homework

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HF14R8

“I, Brodt Daniil, declare that this is my and only my own solution” - 2021.12.08

Task description:

Counting the number of "0" bits in a 128 bit pattern being in the internal memory.

Input: Start address of the pattern (pointer)

Output: The number of "0" bits in 1 register

First we need to calculate the number of zeroes in given data manually: 0x42, 0x1A, 0x7F, 0x80, 0x55, 0xAA, 0xA0, 0xCC, 0x12, 0x13, 0x11, 0x10, 0x05, 0xAA, 0x42, 0x34.

There are 82 zeroes in these 16 hexadecimal numbers, while the number we want to compare in assembly will be 52H since 82 DEC will be 52 in HEX.

Implemented algorithm: we need two subroutines to solve the given task

CODE2IRAM: requires 3 inputs and 0 outputs, and utilizes few registers, accumulator and DPTR

- 1) Move base address in IRAM to ACC, and then move ACC to R0 since we are going to use it for indirect addressing
- 2) Move the BITFIELD_LEN to R2 to use it for loop condition check (16 cells)
- 3) Move R5 and R6 to DPH and DPL accordingly since we are going to use DPTR
- 4) Then in the LOOP1 we move the HEX numbers from code memory to internal memory. The loop proceeds as such : clear ACC (to avoid jumping extra cell because of the stored address from the previous time), move data in DPTR to ACC, move ACC to base address in IRAM via @R0. Then we increment the value of R0 to move to the next HEX number and DJNZ to check R2 value to know whether we are done with all the HEX numbers or not

Code Memory				address												value			
	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F			
00	80	0E	00	00	00	00	00	00	00	00	00	00	00	00	00	00			
10	7D	00	7E	70	7F	40	11	1E	7F	40	11	30	80	FC	EF	F8			
20	7A	10	ED	F5	83	EE	F5	82	E4	93	F6	A3	08	DA	F9	22			
30	7D	00	7A	10	EF	F8	E6	7B	08	33	40	01	0D	DB	FA	08			
40	DA	F4	22	00	00	00	00	00	00	00	00	00	00	00	00	00			
50	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00			
60	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00			
70	42	1A	7F	80	55	AA	A0	CC	12	13	11	10	05	AA	42	34			

System Clock (MHz) 12.0
SBUF
R/O W/O TH0 TL0
0x00 0x00 0x00 0x00
RXD TXD
1 1
SCON 0x00 TMOD 0x00 TCON 0x00
pins bits
0xFF 0xFF P3 0x00 0x00
0xFF 0xFF P2 0x00 0x00
0xFF 0xFF P1 0x00 0x00
0xFF 0xFF P0 0x00 0x00
PC 0x0032
PSW 0 0 0 0 0 0 0 1
8051
Data Memory
addr 0x00 0x00 value

0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	00	50	00	00	00	00	70	40	1C	00	00	00	00	00	00
10	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
20	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
30	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
40	42	1A	7F	80	55	AA	A0	CC	12	13	11	10	05	AA	42
50	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
60	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
70	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

RST Step Run New Load Save Copy Paste
Executed 0x0030: MOV R5, #00H | Time: 944us - Instr
0030| mov R5, #0x00
0032| MOV R2, #BITFIELD_LEN
0034| mov A, R7
0035| mov R1, A
input :
0036| mov A, @R1
0037| mov R4, #08h ; binary numbers
up :
0039| RLC A
003A| jc if_1
003C| inc R5

We can see from above that it perfectly transferred all the data from code memory to internal memory

COUNT_0: 1 input and 1 output, and utilized 4 registers and 1 acc

- 1) We reset the value of R5 to 0, same procedure for R2 as in the previous subroutine. Then we move the base address of IRAM to A and then from A to R1 to use for indirect addressing.

- 2) Enter the LOOP2 and we move the value from R1 to A to work with a pointer in the loop. Since we work on bytes, we also need to check the loop on the value 8 in R3 for DJNZ
- 3) Then we enter LOOP3 that analyzes each bit with a help of rotation and carry (RLC) and increments our output R5 in case of 0. After that it decrements R3 and jumps back
- 4) After the LOOP3 we increment R0 to point to the next cell and loop back to LOOP2 to process other HEX numbers.

The screenshot displays the Proteus IDE interface for an 8051 microcontroller simulation. The left pane shows the register and pin status, with the PC register highlighted at 0x0030. The right pane shows the assembly code being executed, including instructions like 'mov R5, #0x00', 'MOV R2, #BITFIELD_LEN', 'mov A, R7', 'mov R1, A', 'mov A, @R1', 'mov R4, #08h ; binary numbers', 'RLC A', 'jc if_1', and 'inc R5'. The bottom pane shows the data memory table with values for addresses 00 to 70.

addr	0x00	0x01	0x02	0x03	0x04	0x05	0x06	0x07	0x08	0x09	0x0A	0x0B	0x0C	0x0D	0x0E	0x0F
00	00	00	50	00	00	00	00	52	70	40	1C	00	00	00	00	00
10	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
20	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
30	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
40	42	1A	7F	80	55	AA	A0	CC	12	13	11	10	05	AA	42	34
50	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
60	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
70	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

At the end of the counting we get 52 in HEX which means it counted 82 zeroes in all given HEX numbers which proves that the algorithm works. Then it starts counting again in the infinite loop.

Flow-chart with arrows that are in the opposite direction

