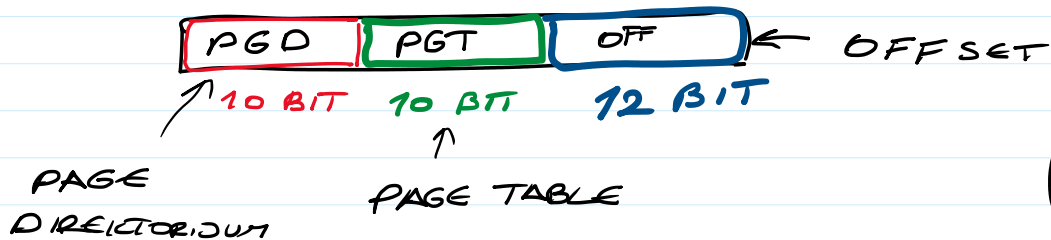




UPRAVLJANJE MEMORIJO

LINEARNA = VIRTUALNA MEMORIJA

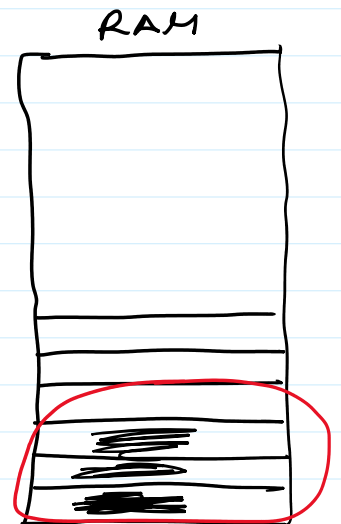
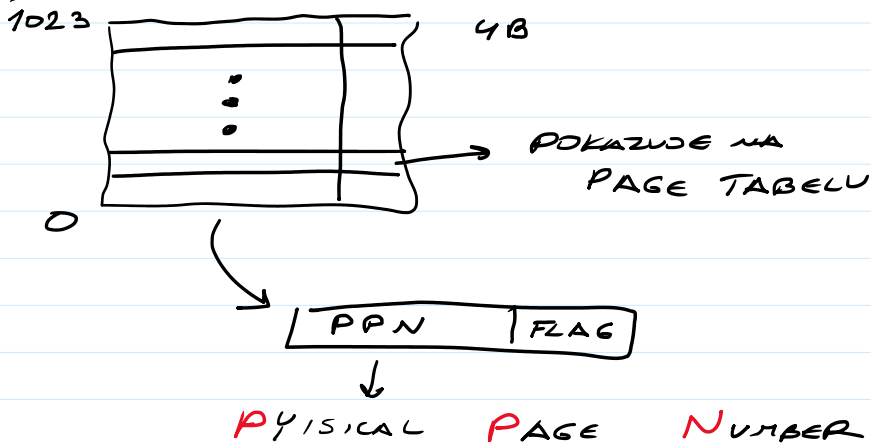


- STRANICE :

4 KB

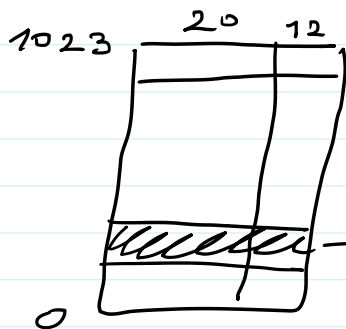
1024 STAVKE OD PO 4 BAJTA

- DIREKTORIJUM STRANICA / PAGE DIRECTORY



SVAKI PROCES RADI SA SVOJOM TABELOM I SA SVOJOM MEMORIJO (VIRTUALNO)

- TABELA STRANICA / PAGE TABLE

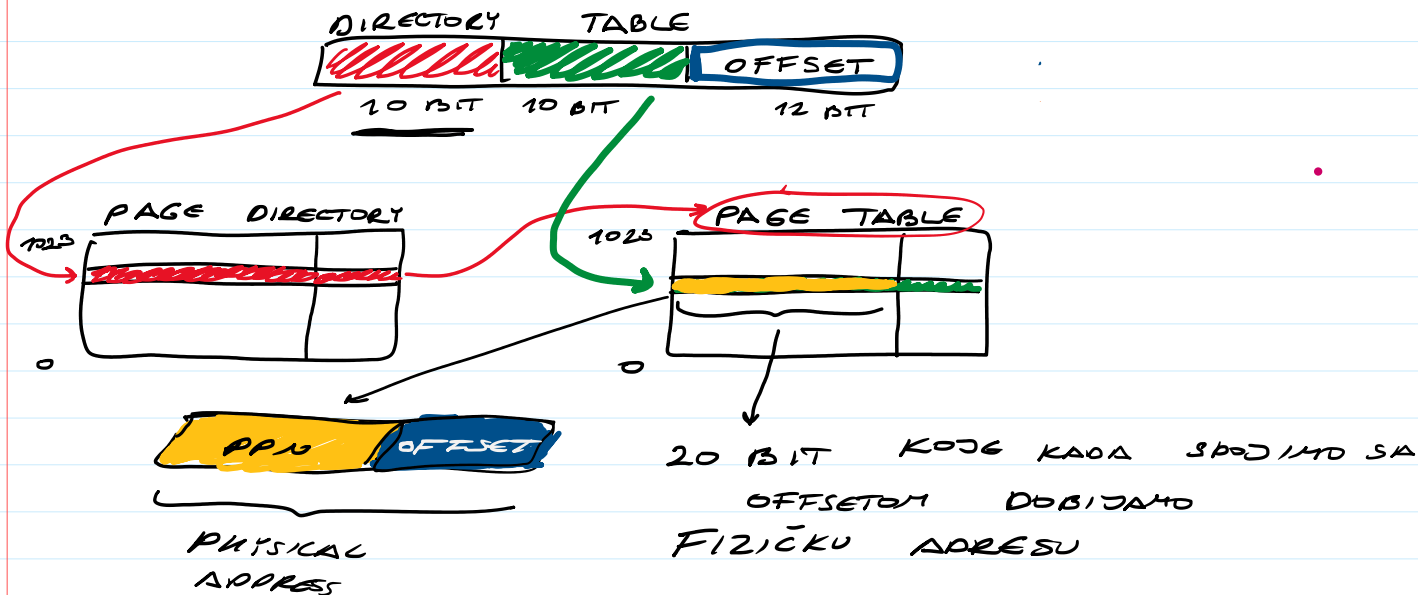


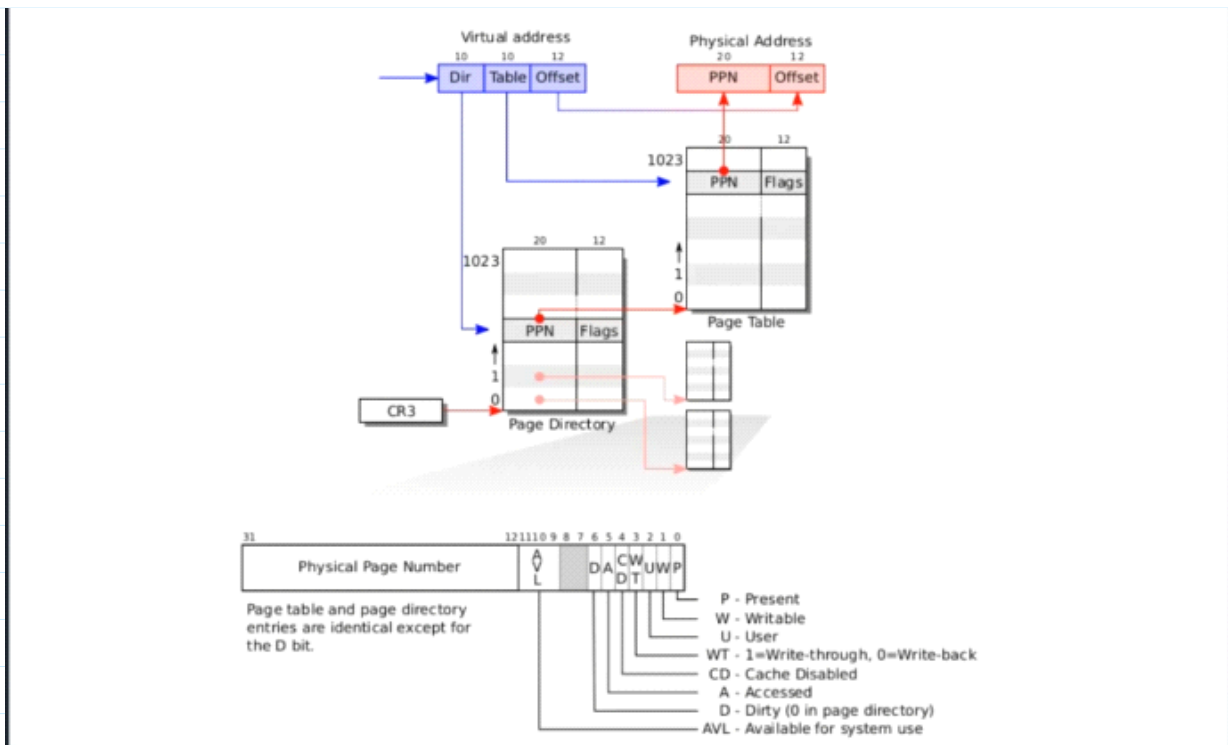
STAVKA JE / POKAZUJE NA PRAVU
FIZIČKU ADRESU



REGISTAR U KOME SE NALAZI POKAZIVAČ
NA DIREKTORIJUM STRANICA TRENUTNO
AKTIVNOG PROCESA

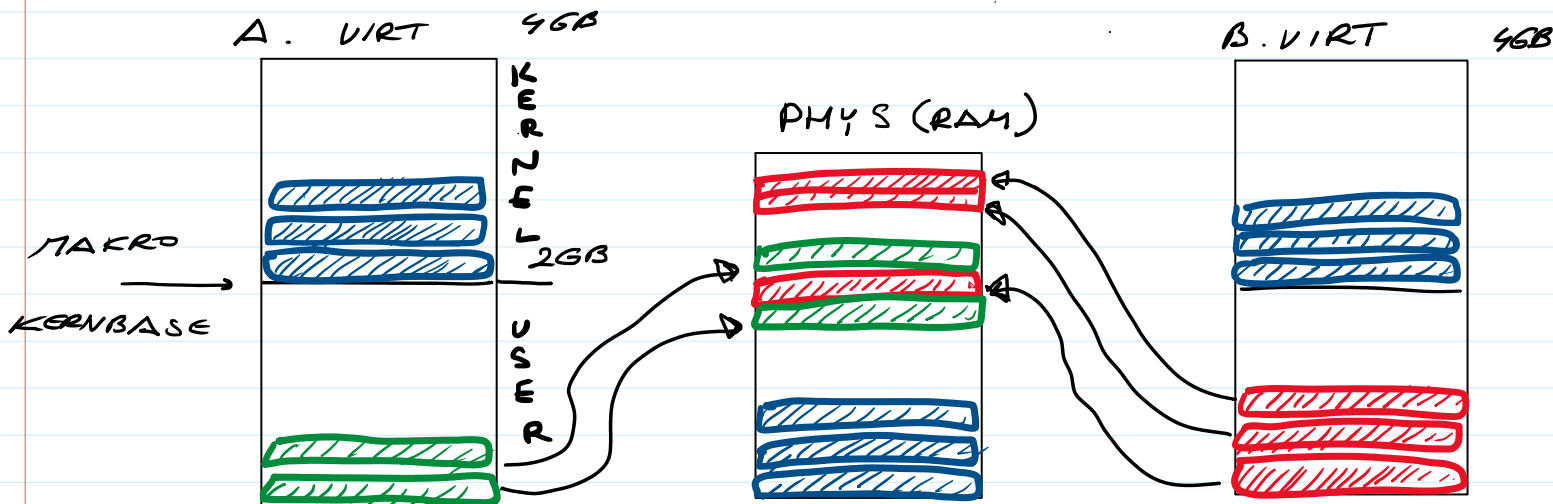
- 12 LINEARNE ADRESE TRENUTNO AKTIVNOG PROCESA





$$2^{10} \cdot 2^{10} \cdot 2^{10} \cdot 4B = 4GB$$

$$2^{10} \cdot 2^{10} \cdot 2^{10} \cdot 2^{32} = 4GB$$



BITNI FADLOVI: PROC.C
 UM.C
 MEMLAYOUT.H
 MMU.H
 TYPES.H

```

STRUCT SHARED {
    char* lme
    void* adresa
    int vecina
}

PROCES {
    .
    .
    .
    PDT - PARENT PGDIR;
    SHARE NIZSHARED [10];
    .
}

```

DALE:

```

STRUCT INFORMACIJE {
    RIDEK_NADOUZA
    RIDEK_NADKRAJA
    MAX_NADOUZA
    MAX_NADKRAJA
    KOMANDNI BROJ
}

```

