

### Switch Inputs

Switch 1: From SW1\_POS PTD7 (J2 pin 19) to ground (J2 pin 14). Uses internal pull-up resistor.

Switch 2: From SW2\_POS PTD6 (J2 pin 17) to ground (J2 pin 14). Uses internal pull-up resistor.

### Debug Signals

For logic analyzer – supported in code versions >= V2

Task	Debug Signal	Port & Bit
Task_ReadSwitches	DBG_1	PTB1
Task_Flash	DBG_2	PTB2
Task_RGB	DBG_3	PTB3