TABLE 2-4. MVME110bug Command and Option Summary

COMMAND	DESCRIPTION
BD [<device>][,<controller>]	Boot dump
BF <address1> <address2> <pattern>	Block of memory fill
BH [<device>][,<controller>]	Bootstrap halt
BI <address1> <address2>	Block of memory initialize
BM <address1> <address2> <address3>	Block of memory move
BO [<device>][,<controller>][,<string>]	Bootstrap operating system
[NO]BR [<address>[;<count>]] [<address>[;<count>]] . . .	Breakpoint set (and remove)
BS <address1> <address2> <data> [<mask>][;<option>]	Block of memory search; options B, W, L
BT <address1> <address2>	Block of memory test
DC <expression>	Data conversion
DF	Display formatted registers
DU[<port number>] <address1> <address2> [<text>]	Dump memory (S-records)
GD [<address>]	Go direct execute program
G[0] [<address>]	Go execute program
GT <temporary breakpoint address>	Go until breakpoint
HE	Help
IOC	I/O direct command
IOP	I/O physical to disk
IOT [<device>][,<controller>]	I/O "teach" to disk
LO[<port number>] [;[<options>]=<text>]	Load (S-records); options X, -C
MD[<port number>] <address> [<count>][;DI]	Memory display/disassembly
M[M] <address>[;<options>]	Memory modify; options W, L, O, V, N, DI
MS <address> <data>	Memory set
OF	Display offsets
[NO]PA[<port number>]	Printer attach (and detach)
PF[<port number>]	Port format
TM[<port number>][<exit character>[<trailing character>]]	Transparent mode
T[R] [<count>]	Trace
TT <breakpoint address>	Temporary breakpoint trace
VE[<port number>][;[<options>]=<text>]	Verify (S-records); options X, -C
VI	Vector initialize
.A0 - .A7 [<expression>]	Display/set address register
.D0 - .D7 [<expression>]	Display/set data register
.R0 - .R6 [<expression>]	Display/set relative offset register
.PC [<expression>]	Display/set program counter
.SR [<expression>]	Display/set status register
.SSP [<expression>]	Display/set supervisor stack pointer
.USP [<expression>]	Display/set user stack pointer