Time	Delta	/two_level_gate_tb/a	/two_level_gate_tb/b	/two_level_gate_tb/c	/two_level_gate_tb/s1	/two...
0 ps	0	U	U	U	U	U
0 ps	1	0	0	0	0	U
0 ps	3	0	0	0	0	0
20000 ps	1	0	0	1	0	X
20000 ps	2	0	0	1	0	1
40000 ps	1	0	1	0	0	X
40000 ps	2	0	1	0	0	0
60000 ps	1	0	1	1	0	X
60000 ps	2	0	1	1	0	1
80000 ps	1	1	0	0	0	X
80000 ps	2	1	0	0	0	0
100000 ...	1	1	0	1	0	X
100000 ...	2	1	0	1	0	1
120000 ...	1	1	1	0	1	1
120000 ...	2	1	1	0	1	X
120000 ...	3	1	1	0	1	1
140000 ...	1	1	1	1	1	1

Console

initialization done.
° # Allocation: Simulator
allocated 6464 kB
(elbread=427 elab2=5897
kernel=139 sdf=0)
° # KERNEL: ASDB file was
created in location
F:\ESE382-Lab\Lab7\lab7\two_le
vel_gate_circuit\src\wave.asdb
° # 9:09 AM, Wednesday, March
20, 2024
° # Simulation has been
initialized

>

Console

Processes

Label	Hierarchy path	Status
verify	/two_level_gate_tb	Ready
u0	/two_level_gate_tb/UUT	Ready
u1	/two_level_gate_tb/UUT	Ready