COS10004 – Computer Systems

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LAB 9

Kernel7.asm:

Initializing the base, calling the timer function, setting function to GPIO 18, setting value to GPIO 18 to switch on or off, and calling factorial from a different file are all necessary steps.

factorialj.asm

It takes r1 and r0 form the kernel7.asm file then subtract #1 from r1, and compare r1 with #1. Whether it is true, it will stop. The value of I r0 will then be stored once r0 and r1 have been multiplied. When r1 reaches 1, this will continue and we will have a value factorial of 4.

TIMER.asm

Set up timer function, get r2 from kernel, subtract 1 from r2, then compared r2 with 0. If r2 isn't equal to 0, it will loop back until r2 becomes 0

FASARM Code:

Kernel7.ASM

```
;Calculate

mov r1,#4 ;input

mov sp,$1000  ;make room on the stack

mov r0,r1

bl FACTORIAL

mov r7,r0 ;store answer

BASE = $3F000000 ;RP2 and RP3 ;GPIO_SETUP


mov r0,BASE

bl SETUP_LED


mov r0,BASE

mov r1,r7
```

```
bl FLASH


wait:
b wait
include "TIMER.asm"
include "factorialj.asm"
include "GPIO.asm"
```

factorialj.asm:

```
FACTORIAL:
sub r1,r1,#1
cmp r1,#1
beq EXIT
mul r0,r0,r1
push {r1,lr}
 ;push onto the stack without changing the stack pointer
bl FACTORIAL      ;call FACTORIAL
EXIT:
pop {r1,lr}  ;pop off the stack
bx lr   ;RETURN
```

TIMER.asm

```
;TIMER - dumb timer
;r2=number of loops
TIMER:
  wait1$:
    sub r2,#1
    cmp r2,#0
    bne wait1$
```

```
bx lr
```

GPIO.asm:

```
SETUP_LED:
 GPIO_OFFSET = $200000
 orr r0,GPIO_OFFSET
 mov r1,#1
 lsl r1,#24
 str r1,[r0,#4]
bx lr


FLASH:
mov r2,r0
orr r0,GPIO_OFFSET
mov r7,r1
loop$:
  mov r1,#1
  lsl r1,#18
  str r1,[r0,#28]

 mov r1,#1
 lsl r1,#18
 str r1,[r0,#40]

  push {r0,r1,r7,lr}
  mov r0,BASE
  mov r1,$0F0000
  bl TIMER
```

```
 pop {r0,r1,r7,lr}


 sub r7,#1

 cmp r7,#0

 bne loop$


bx lr
```

TIMER2.asm:

```
Delay: ;this function has 2 parameters
TIMER_OFFSET=$3000
mov r3,r0 ;BASE - depends on Pi model
orr r3,TIMER_OFFSET
mov r4,r1 ;$80000 passed as a parameter
ldrd r6,r7,[r3,#4]
mov r5,r6
loopt1: ;label still has to be different from one
in _start
ldrd r6,r7,[r3,#4]
sub r8,r6,r5
cmp r8,r4
bls loopt1
bx lr ;return
```