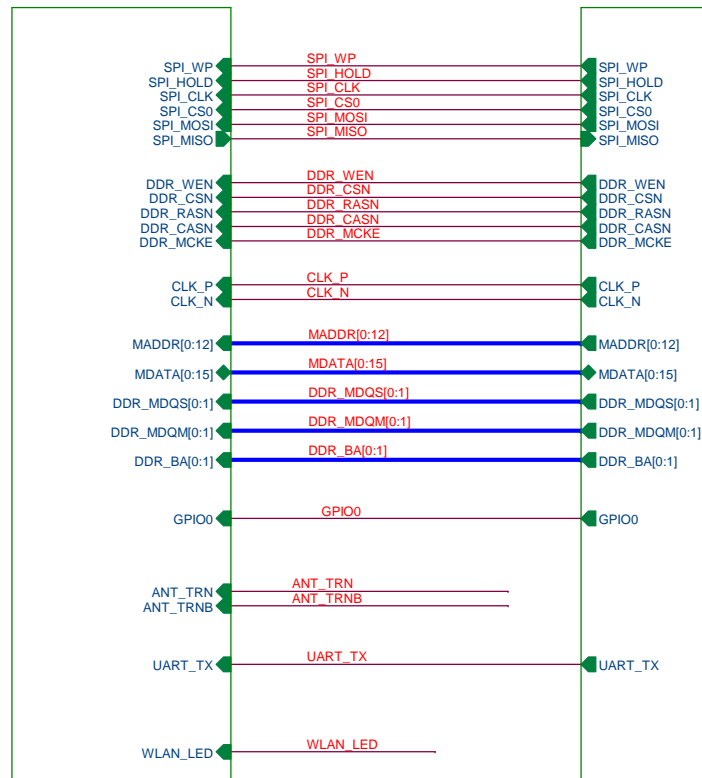
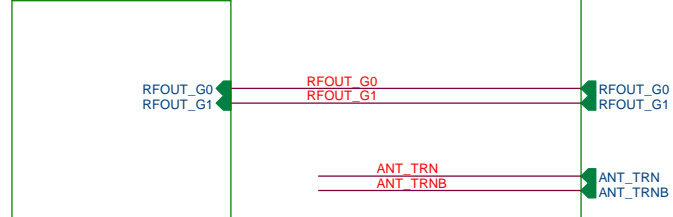


2-MEMORY



2-MEMORY

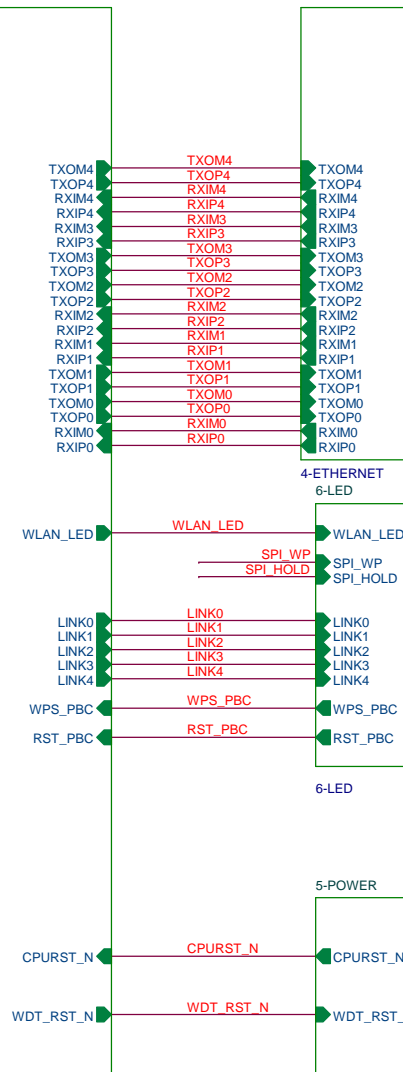
3-WLAN



3-WLAN

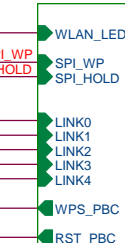
1-CPU-MT7620N

4-ETHERNET



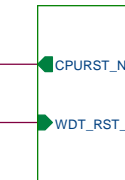
4-ETHERNET

6-LED



6-LED


5-POWER

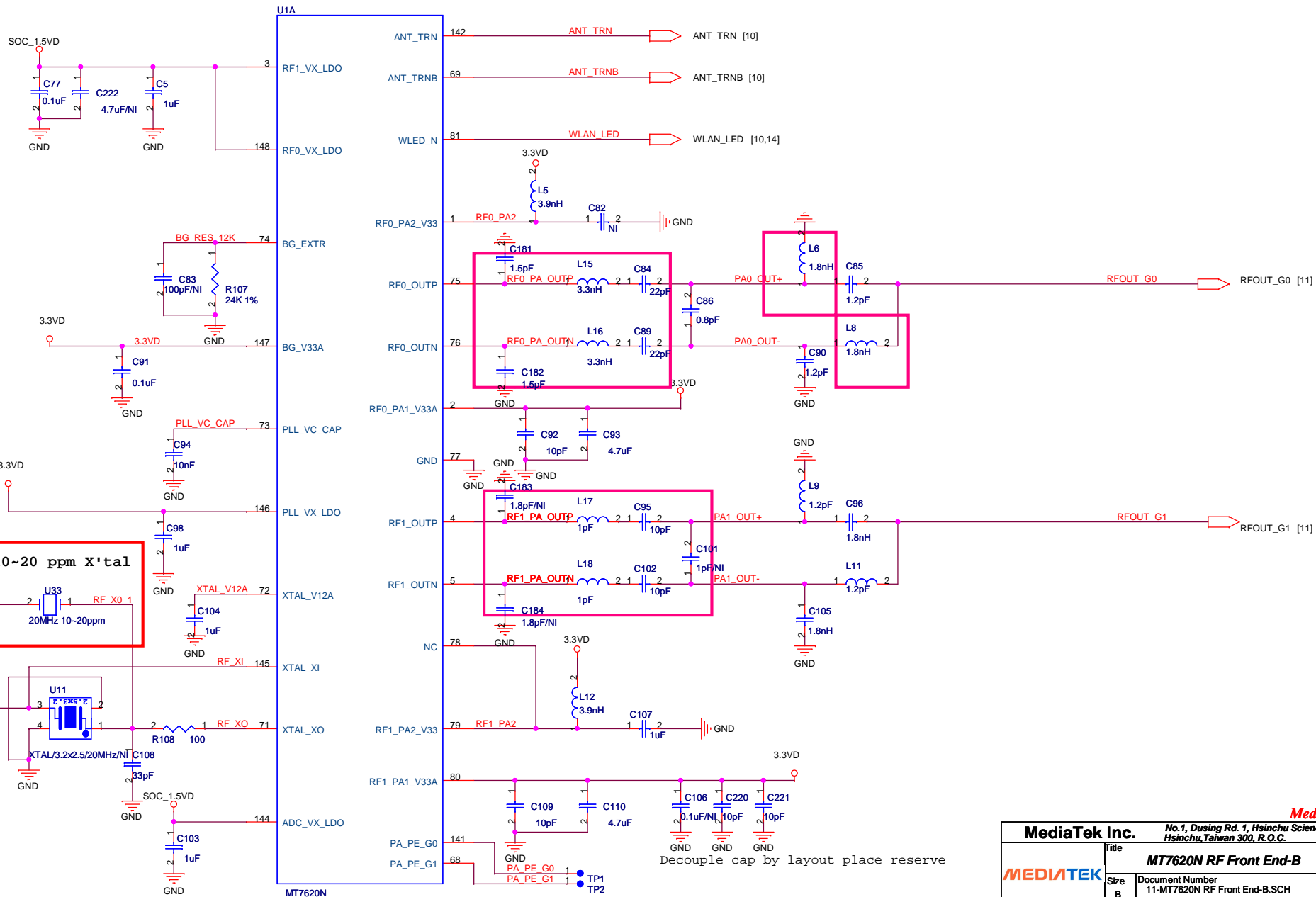


5-POWER

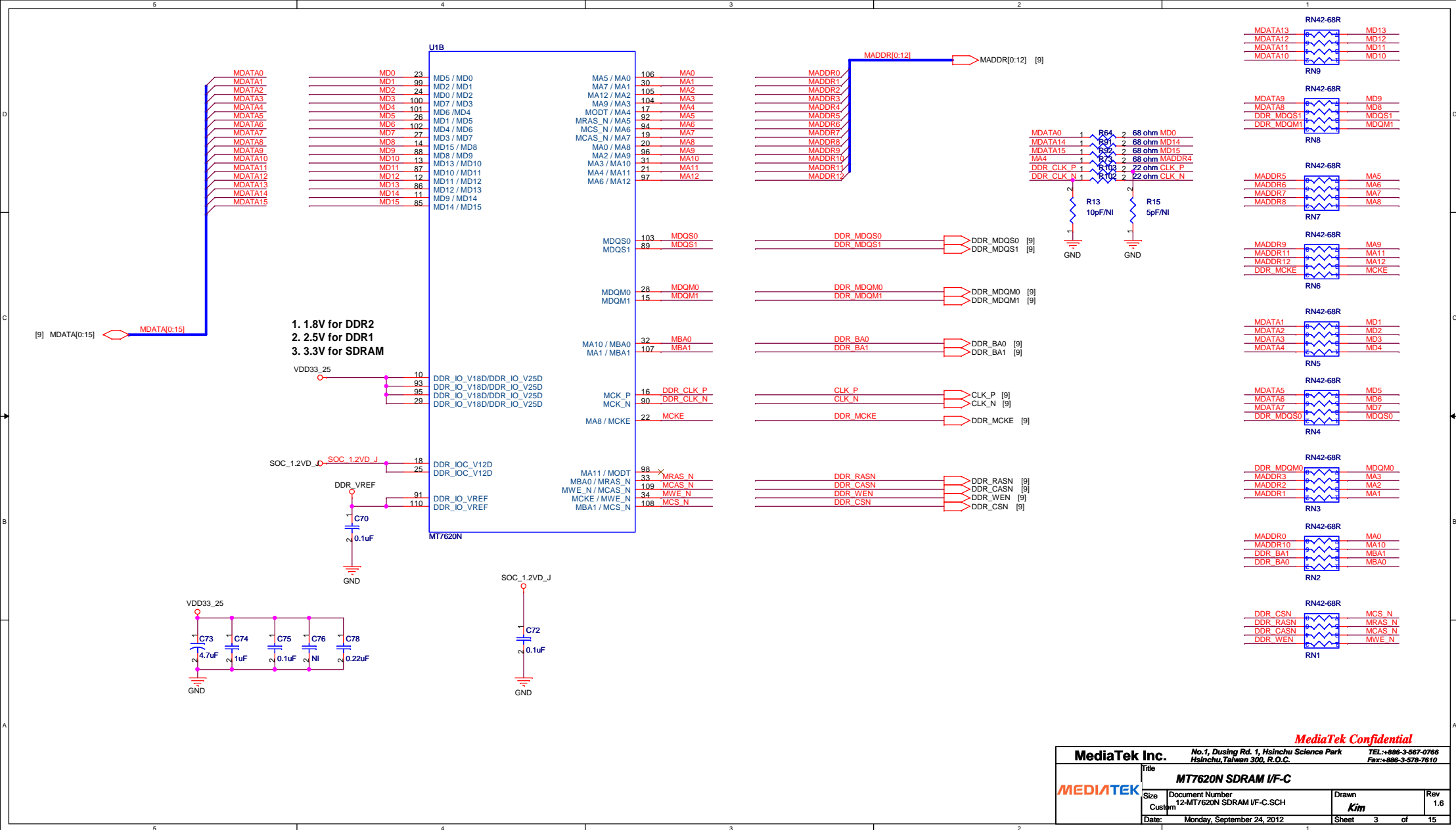
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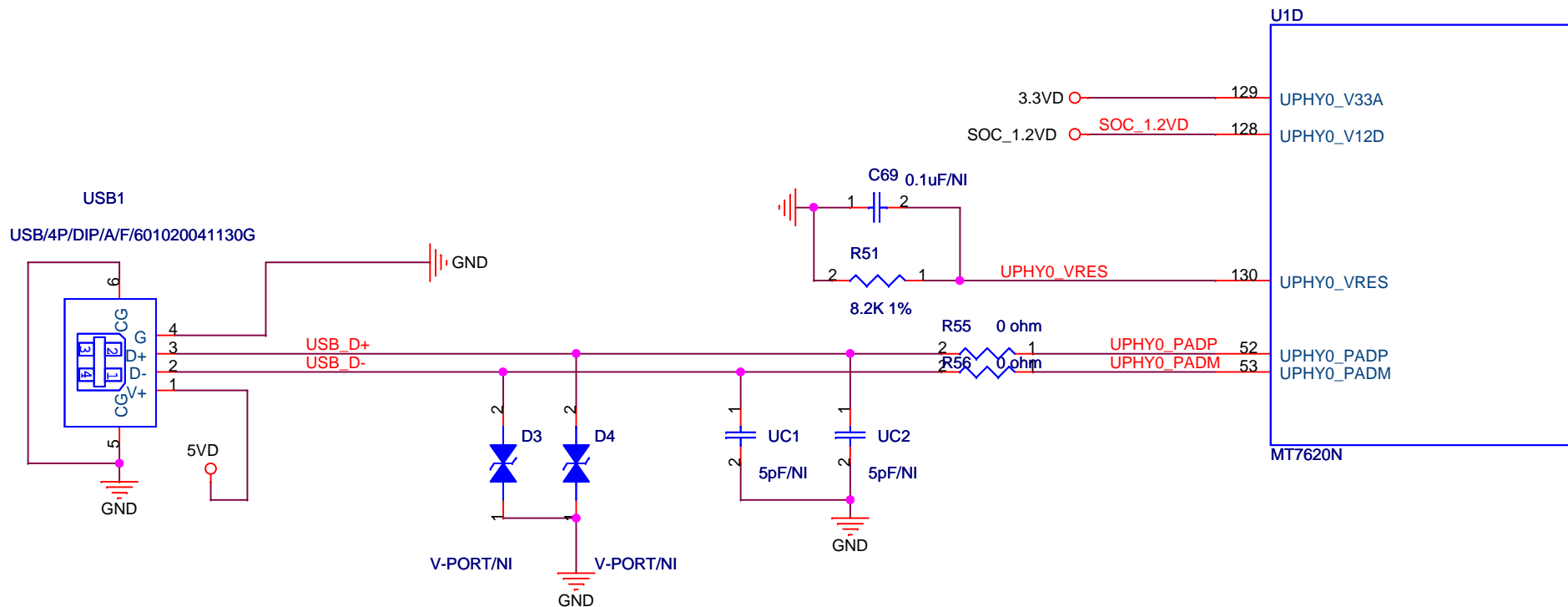
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	Title					
	11n 2.4G AP-MT7620N					
	Size	Document Number			Drawn	Rev
	B	11n 2.4G AP-MT7620N.SCH			Kim	1.6
Date:		Tuesday, November 20, 2012		Sheet	1 of 15	




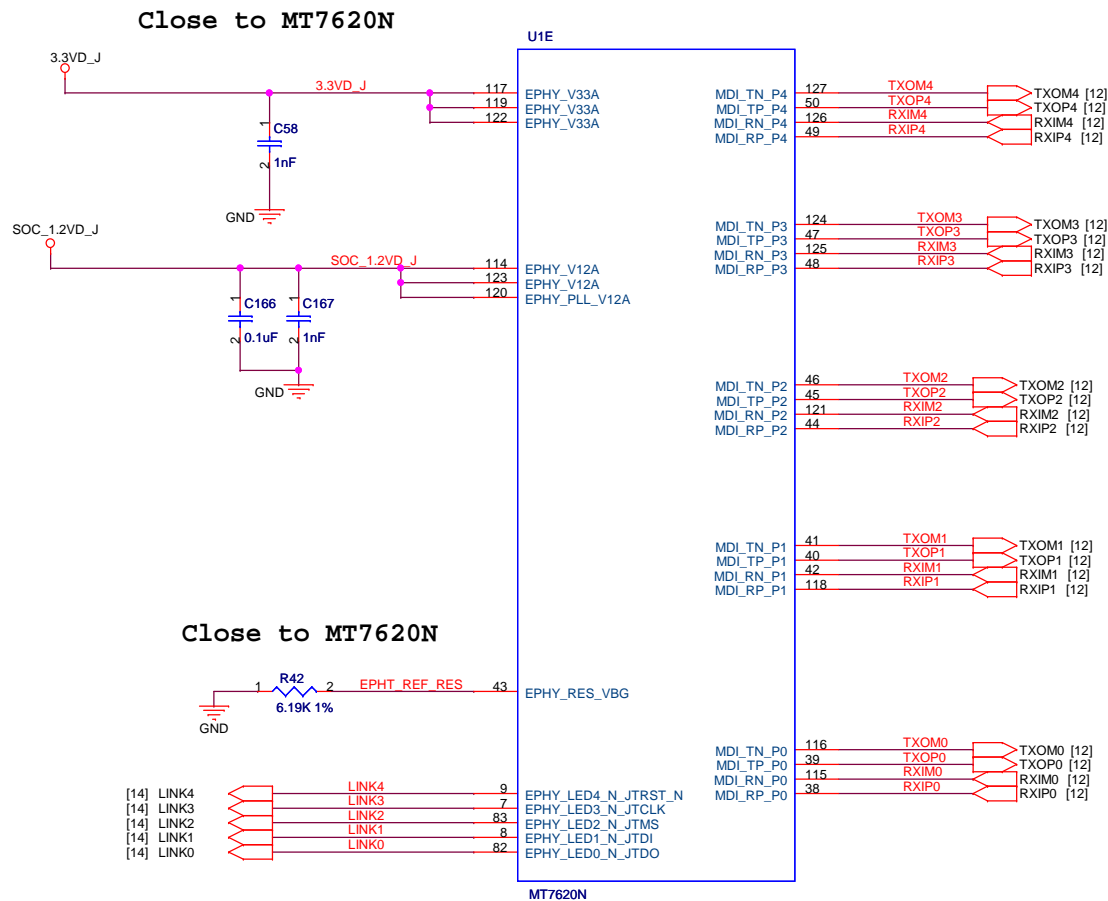
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		MT7620N RF Front End-B		
Size B	Document Number	Drawn	Rev	
	11-MT7620N RF Front End-B.SCH	Kim	1.6	
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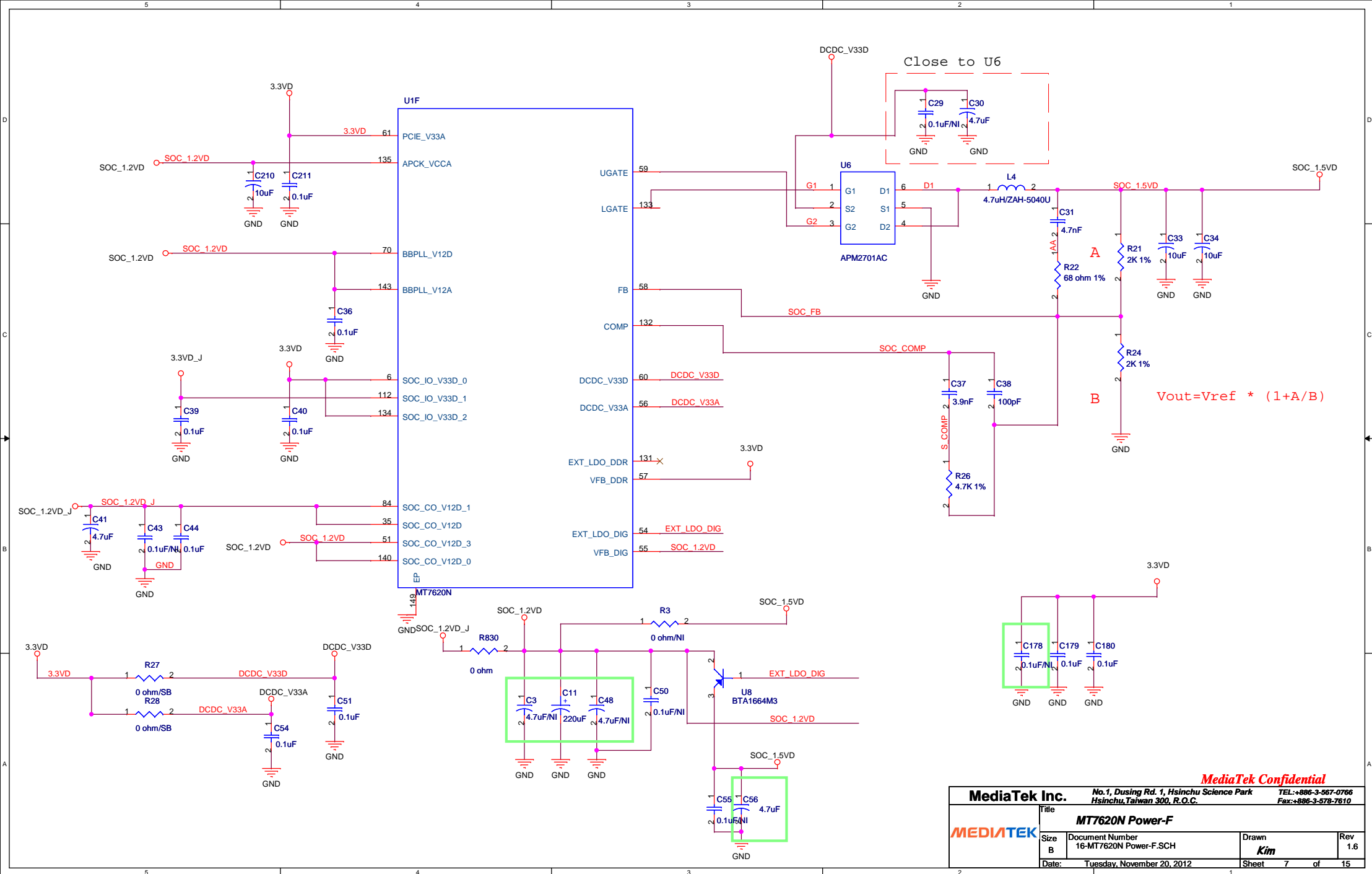


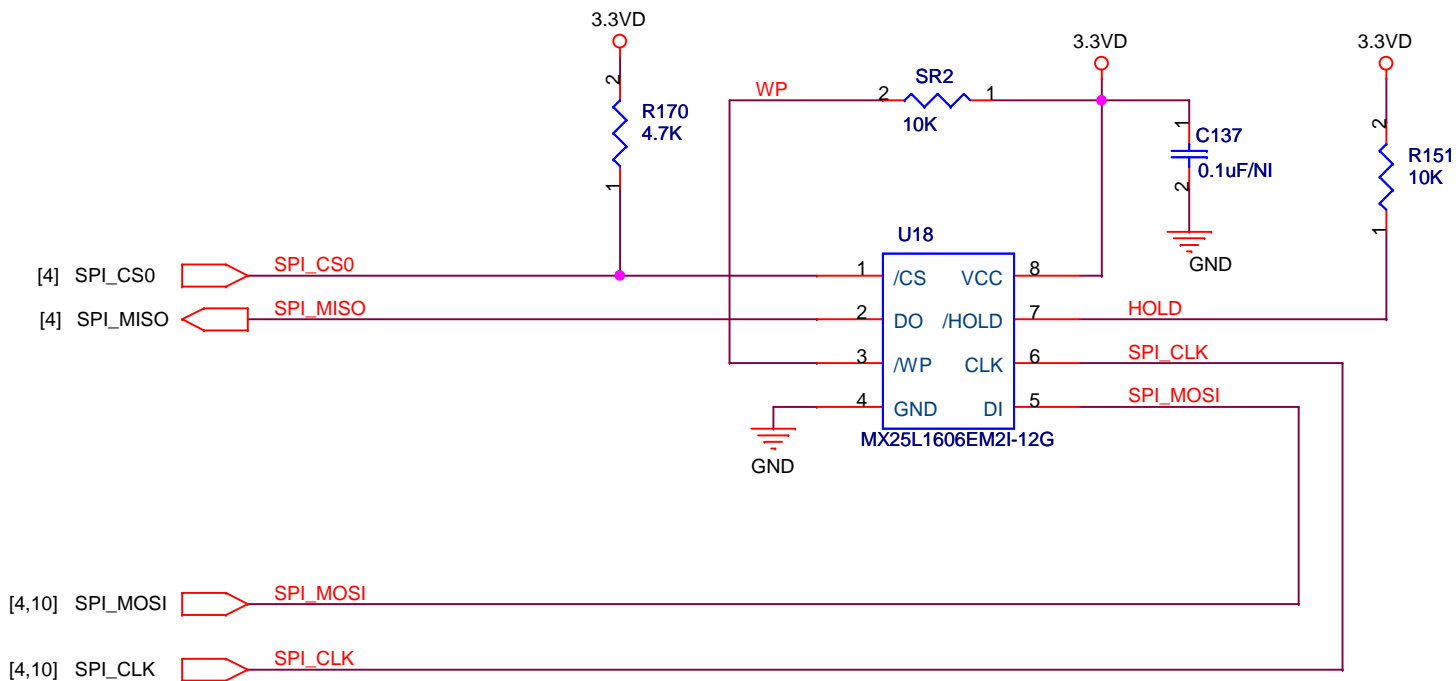
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	Title MT7620N USB-D				
	Size A	Document Number 14-MT7620N USB-D.sch		Drawn Kim	Rev 1.6
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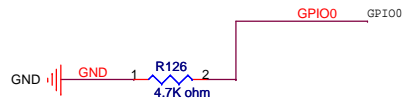
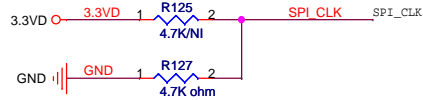
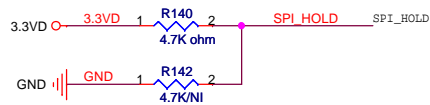
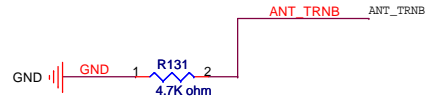
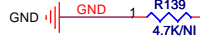
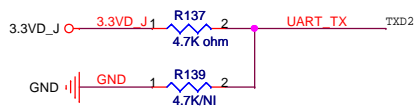
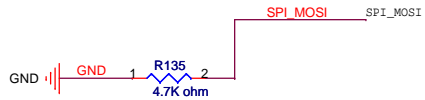
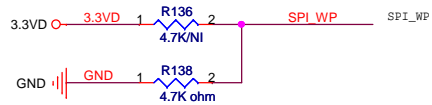
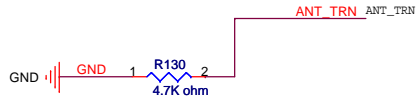
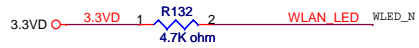
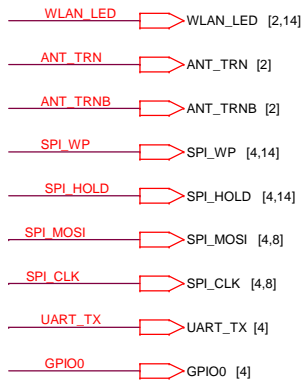
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		Title MT7620N Ethernet-E		
Size B	Document Number 15-MT7620N Ethernet-E.SCH		Drawn Kim	Rev 1.6
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	Title				
	SPI Flash				
	Size A	Document Number 21-SPI Flash.SCH		Drawn Kim	Rev 1.6
Date:		Tuesday, September 04, 2012		Sheet 8	of 15

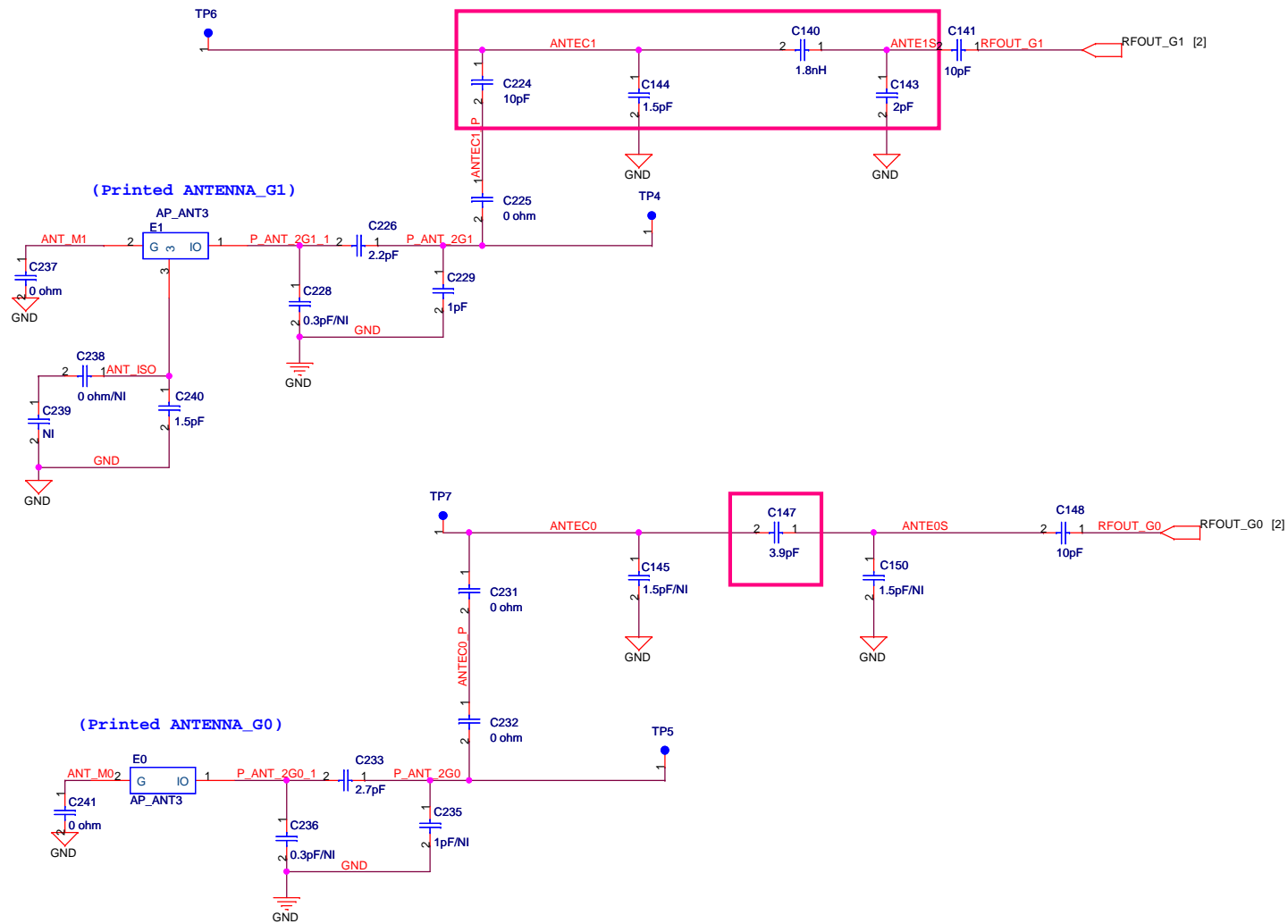


MT7620N Boot Up Strapping

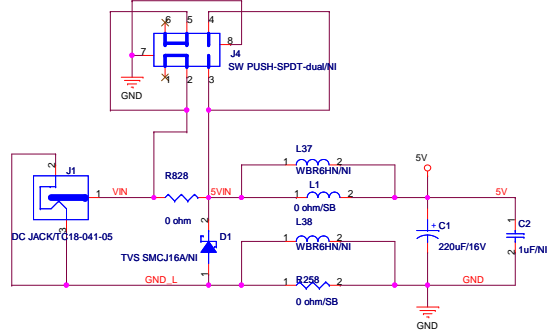
Pin Name	Description	Value
WLED_N	DRAM_FROM_EE For non scan mode:	0: DRAM configuration from EEPROM 1: DRAM configuration from Auto Detect
ANT_TRN	DBG_JTAG_MODE	0: EPHY_LED 1: JTAG MODE
ANT_TRNB	XTAL_FREQ_SEL	0: 20MHz 1: 40MHz
{SPI_WP, SPI_HOLD}	DRAM_TYPE	01: DDR1 (CPU/3) TSOP Package 10: DDR2 (CPU/3) FBGA Package 11: SDRAM (CPU/5) (LVTTL 3.3 V) TSOP Package
{SPI_MOSI SPI_CLK, TXD2, GPIO0 }	CHIP_MODE[3:0]	A vector to set chip function/test/debug modes. In non-test/debug operation, 0001: Normal mode (boot from ROM+NAND flash 4 cycle address) 0010: Normal mode (boot from SPI 3Byte Addr) 0011: Normal mode (boot from SPI 4Byte Addr) 0100 :iNIC RGMII mode(boot from ROM) 0101: iNIC MII mode(boot from ROM) 0110: iNIC RVMII mode(boot from ROM) 0111: iNIC PHY mode(boot from ROM) 1000: iNIC USB mode(boot from ROM) 1001: iNIC PCIe mode(boot from ROM)

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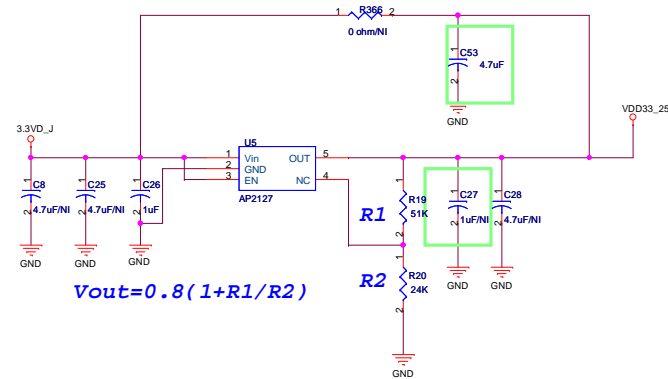
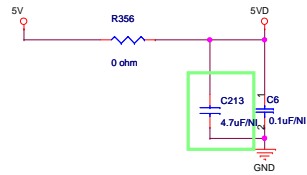
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	Title				
	Config				
	Size B	Document Number 23-Config.SCH		Drawn <i>Kim</i>	Rev 1.6
	Date:	Tuesday, November 20, 2012		Sheet 10	of 15



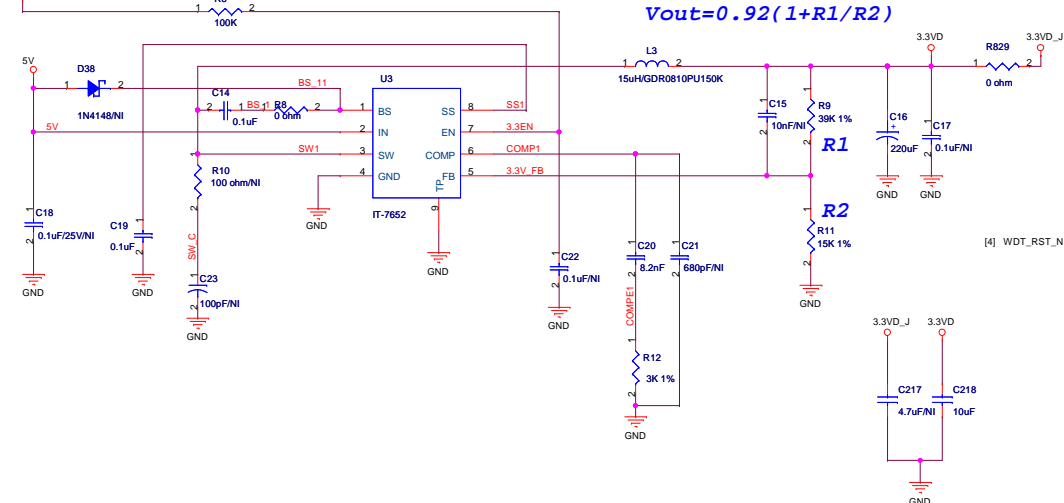
5V DC ADAPTER



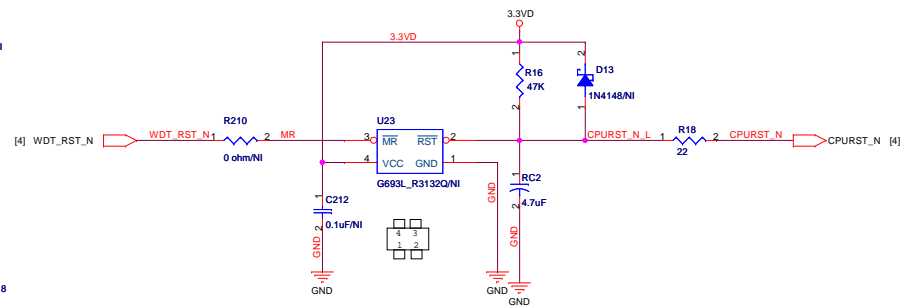
USB 5V Power




5V-->3.3V/3A

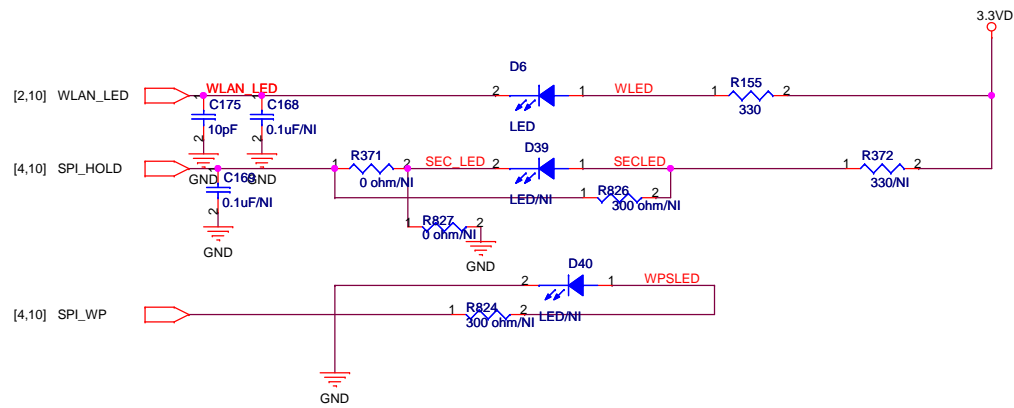


For ESD reserve, due to the 3.3V trace has to across the bottom of Ethernet transformer.



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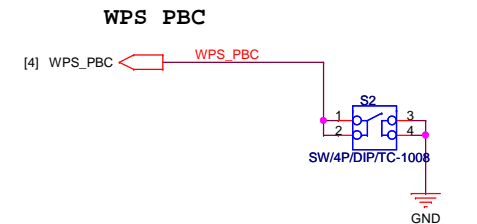
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		Title			
		Power & Reset			
		Size	Document Number	Drawn	Rev
		Customer	5-Power - Reset.SCH	Kim	1.6
		Date:	Tuesday, November 20, 2012	Sheet	13 of 15



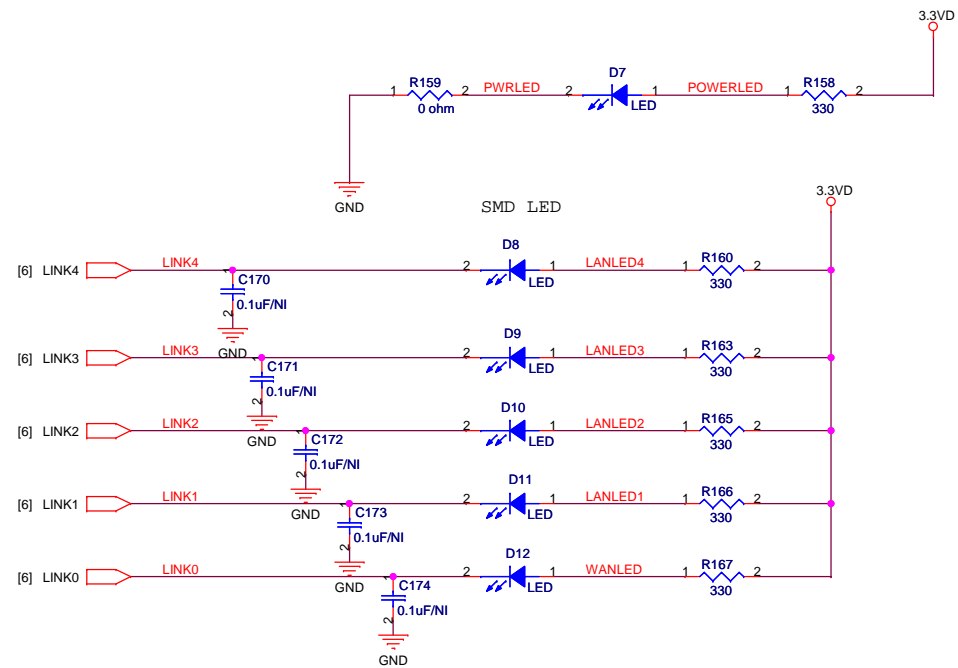
Wireless ACT LED

Security LED

WPS LED



WPS PBC



System/Power LED

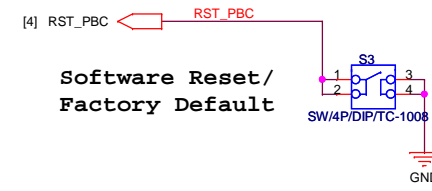
LAN4 Port LED

LAN3 Port LED

LAN2 Port LED

LAN1 Port LED

WAN Port LED



Software Reset/
Factory Default

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MEDIATEK	Title LED				
	Size B	Document Number 6-LED.SCH		Drawn Kim	Rev 1.6
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