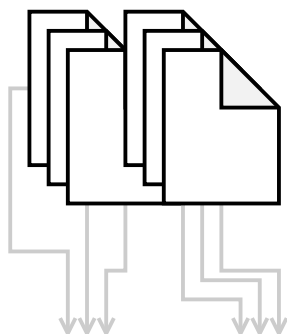


Input
Testvectors (xN)



FifoDriver (xN)

.....

input_spybuffers

.....
Spy+FIFO (xN)

.....

N Inputs
.....

LOGIC BLOCK
(DESIGN UNDER TEST)

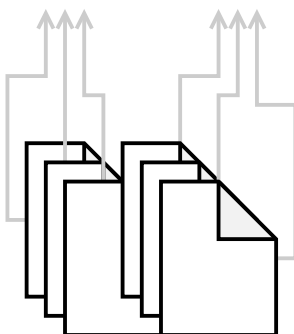
.....
M Outputs

output_spybuffers

.....
Spy+FIFO (xM)

.....

.....
FifoMonitor (xM)



Output
Testvectors (xM)

HDL

python & CocoTB