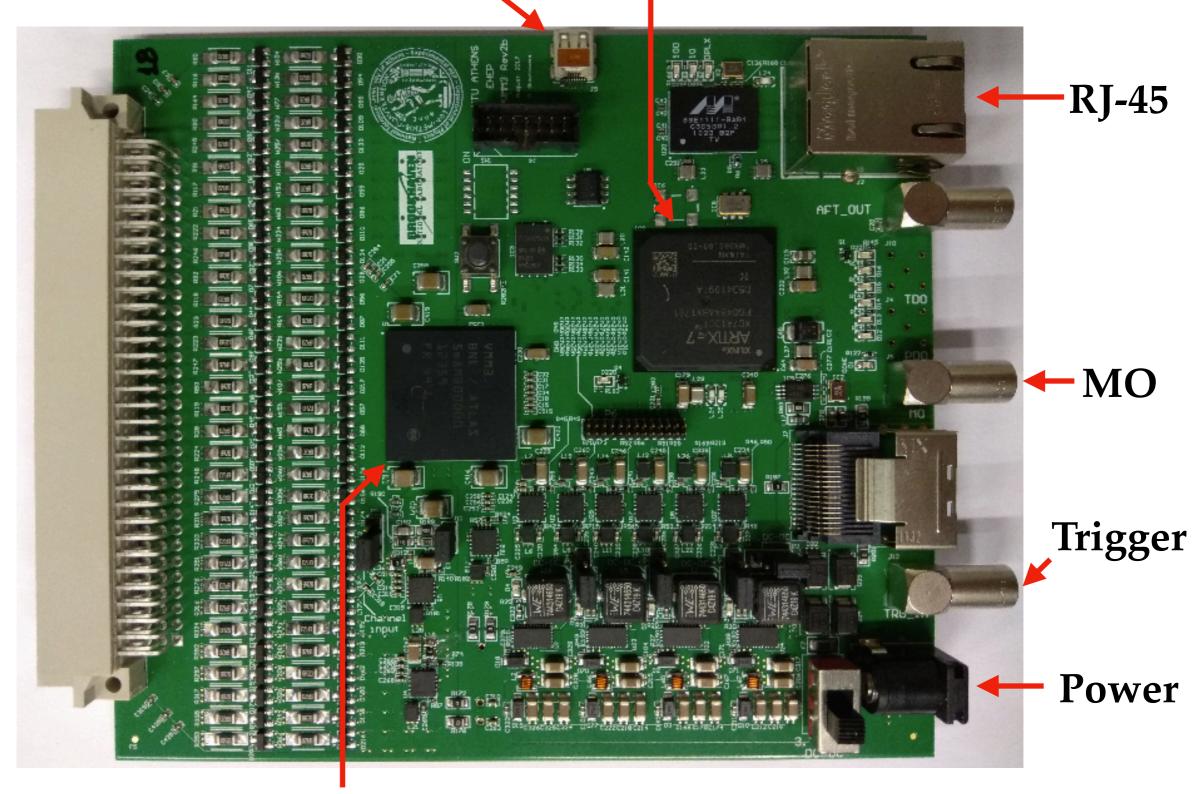
## μHDMI FPGA



**VMM**