



Connect relay feedback signals
FB_TX and FB_RX to ground if not used.

Solder JPx to/iff

JP1 - disable power monitoring
JP2 - make TX signal INH
JP3 - PTT active high
JP4 - reserved

Pull Stop to ground to stop all operation

*P = PMOS with low Rdson that can handle 20V between gate-source
E.g. PMV65XP, Si2347DS

*N = NMOS with low Rdson that can handle 40V between drain-source
E.g. PMV130ENEA, Si2318DS

Sequence, PTT from deactivated to activated:

1. RX on -> off
2. Relay off (RX) -> on (TX)
- If relay taking power and FB_TX low:
3. PA off -> on
4. TX off -> on (INH on -> off)

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