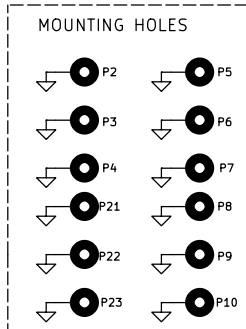
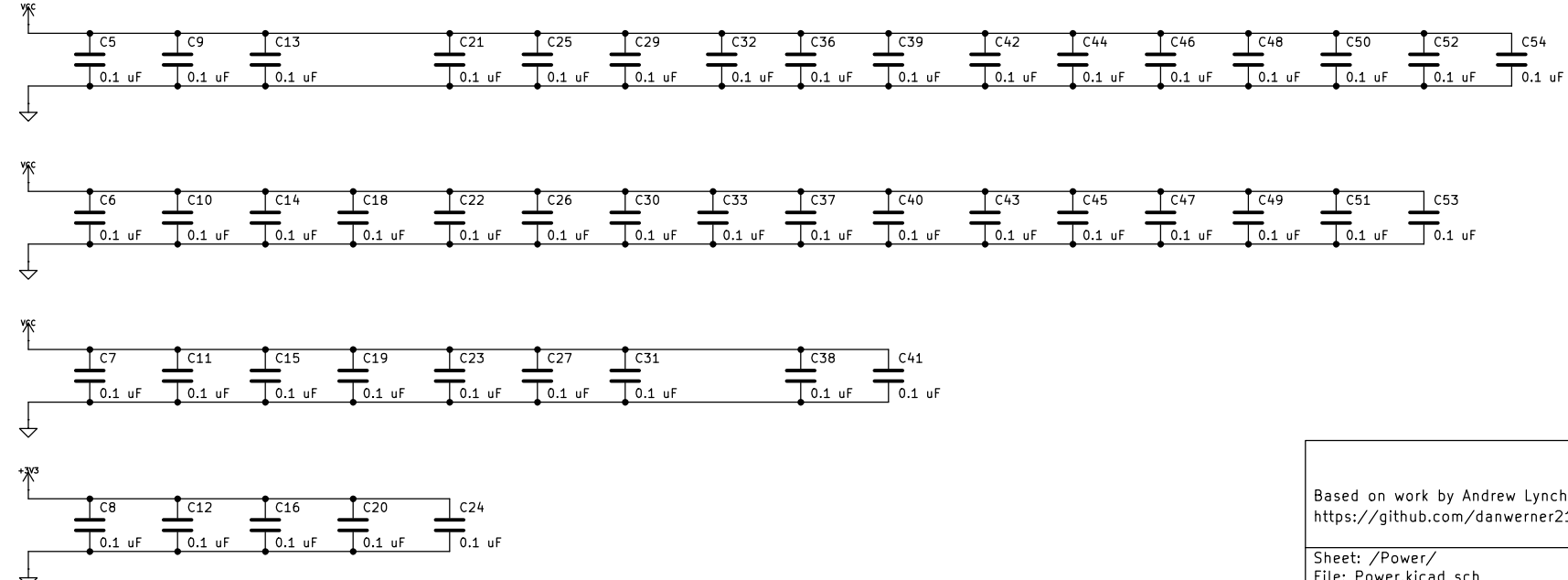
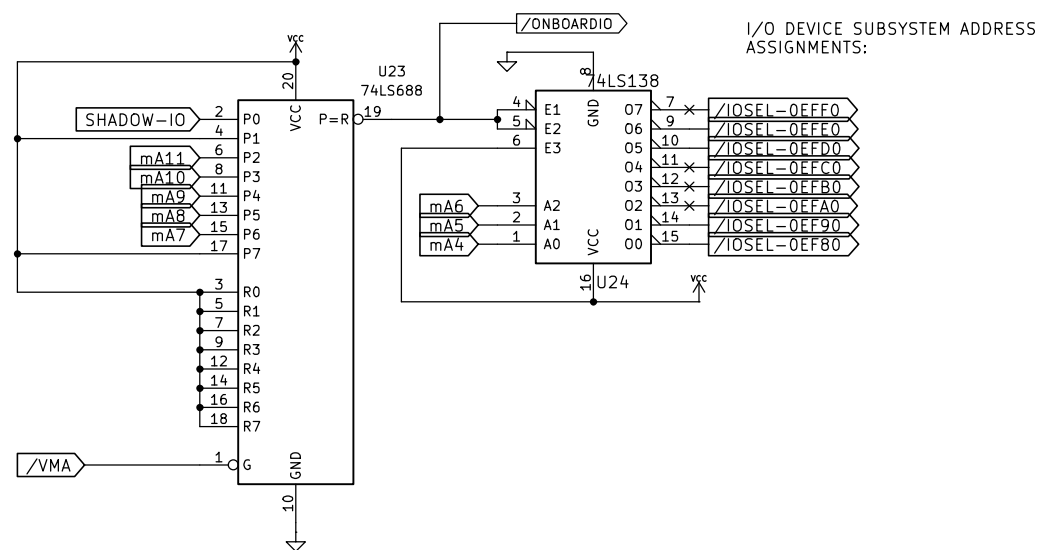
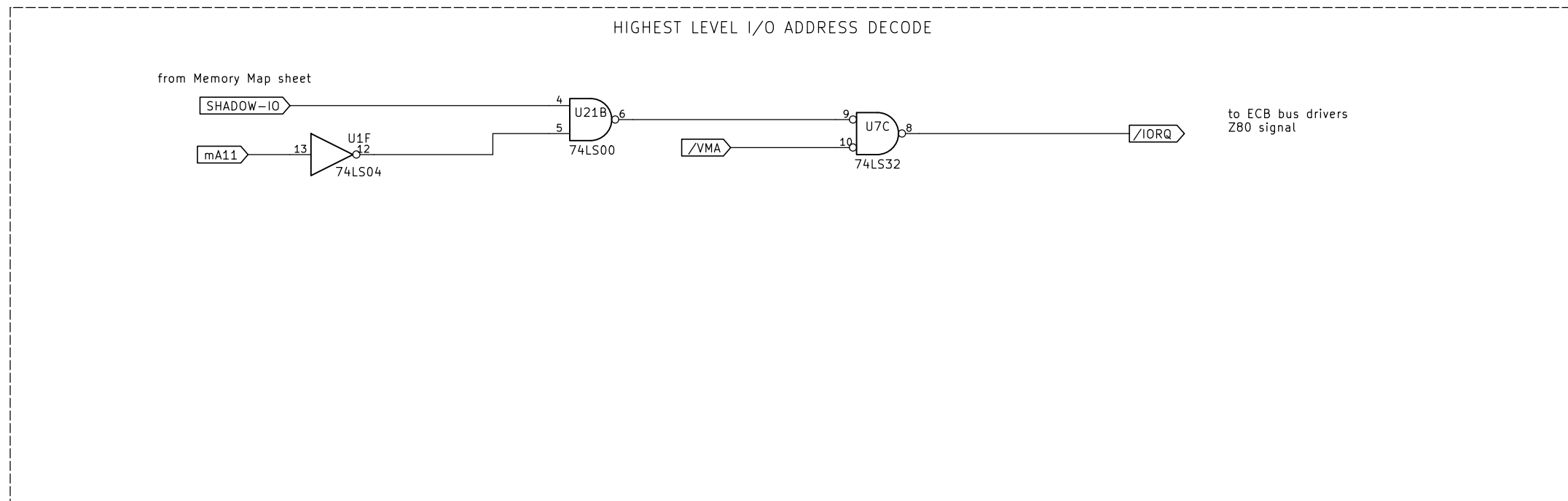
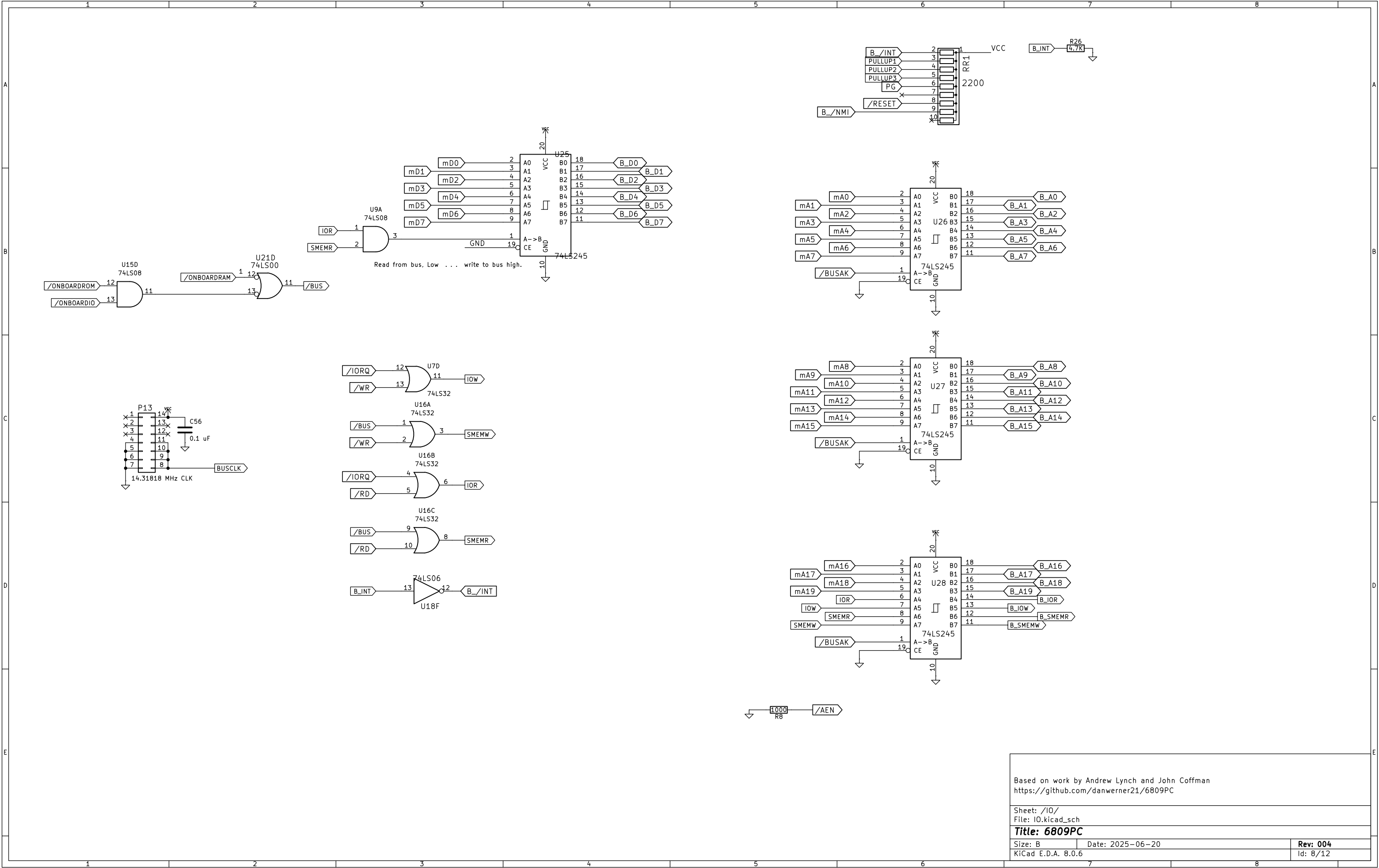


BYPASS CAPACITORS





\$E000 - \$E0FF	available
\$E100 - \$E1FF	available
\$E200 - \$E2FF	available
\$E300 - \$E3FF	available
\$E400 - \$E4FF	available
\$E500 - \$E5FF	available
\$E600 - \$E6FF	available
\$E700 - \$E7FF	available
\$E800 - \$E8FF	available
\$E900 - \$E9FF	available
\$EA00 - \$EAFF	available
\$EB00 - \$EBFF	available
\$EC00 - \$ECFF	available
\$ED00 - \$EDFF	available
\$EE00 - \$EEFF	available
\$EF00 - \$EFFF	available
\$F000 - \$F0FF	MMU Registers (3)
\$F100 - \$F1FF	MMU Task Map (16)
\$F200 - \$F2FF	available
\$F300 - \$F3FF	available
\$F400 - \$F4FF	available
\$F500 - \$F5FF	available
\$F600 - \$F6FF	available
\$F700 - \$F7FF	available
\$F800 - \$F8FF	available
\$F900 - \$F9FF	RTC (16)
\$FA00 - \$FAFF	ACIA (4)



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/6809PC>

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File: IO.kicad_sch

Title: 6809PC

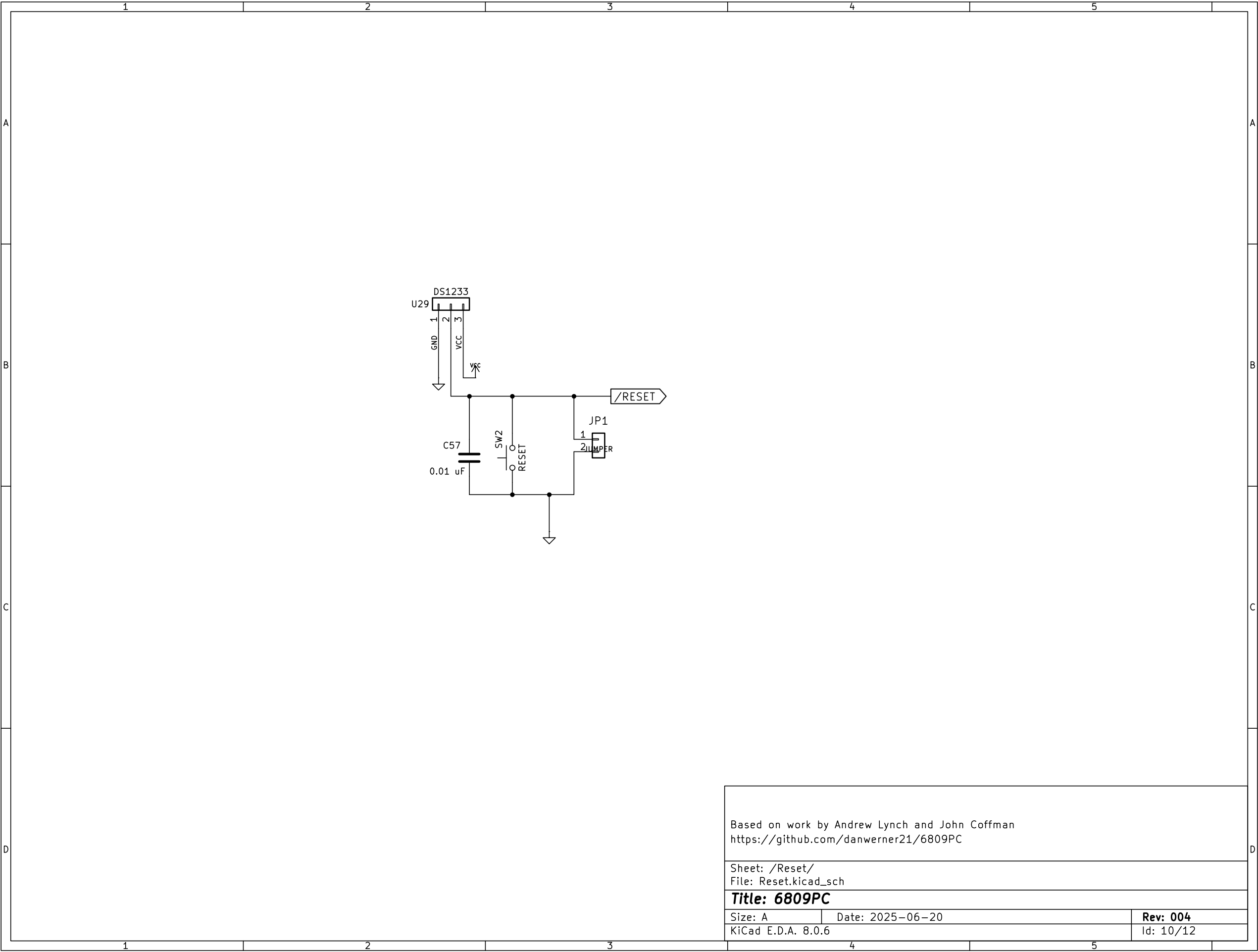
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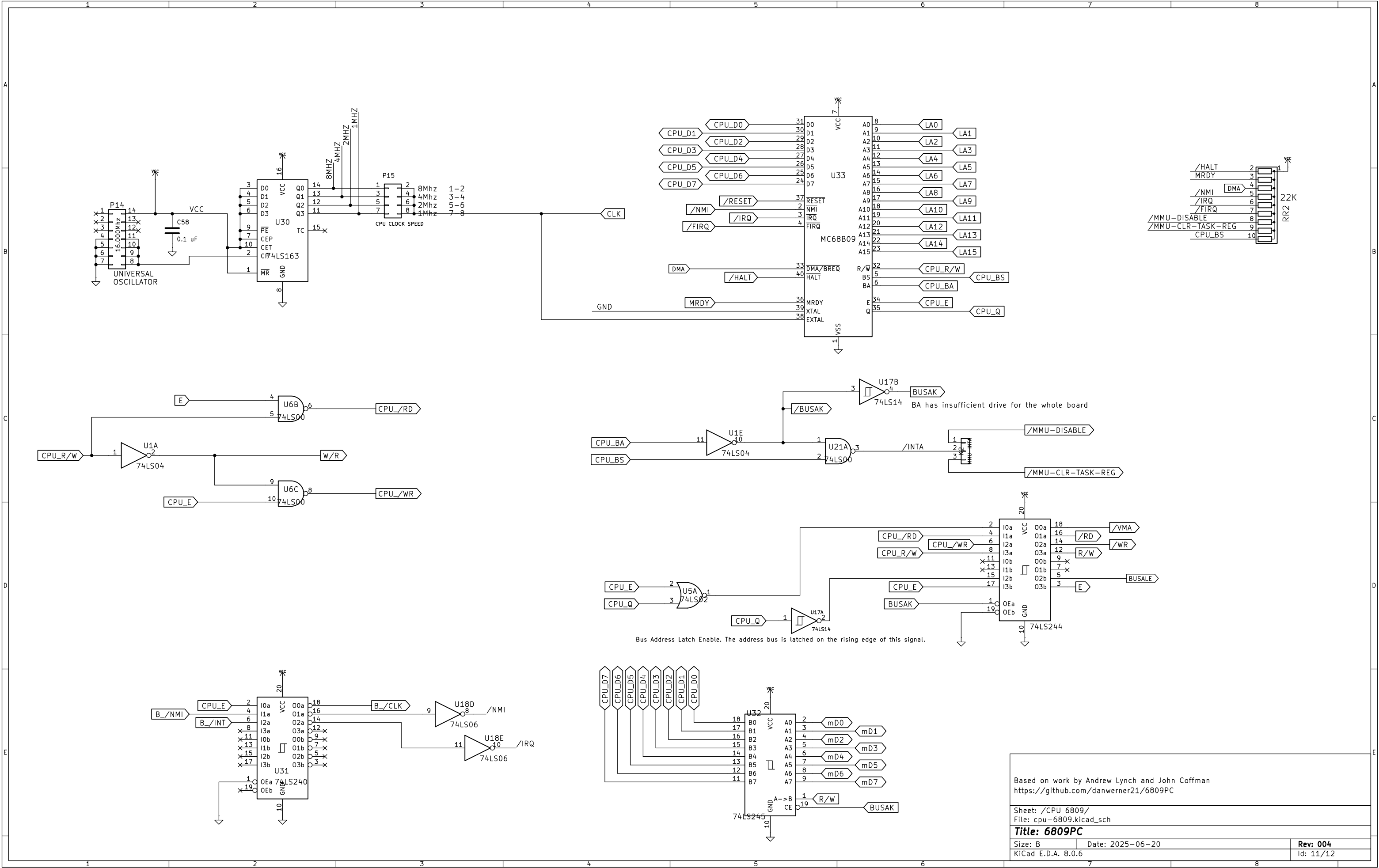
Rev: 004

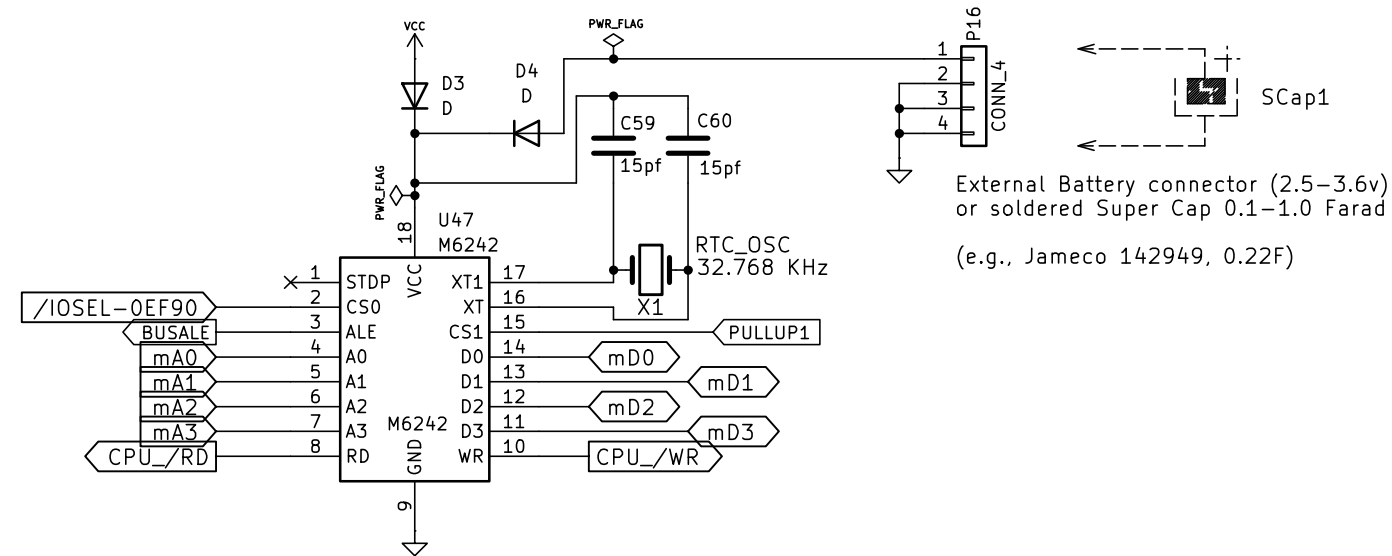
KiCad E.D.A. 8.0.6

Id: 8/12

Id: 9/12







Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/6809PC>

Sheet: /RTC/
File: RTC.kicad_sch

Title: 6809PC

Size: A Date: 2025-06-20

Rev: 004

KiCad E.D.A. 8.0.6

Id: 13/12

