

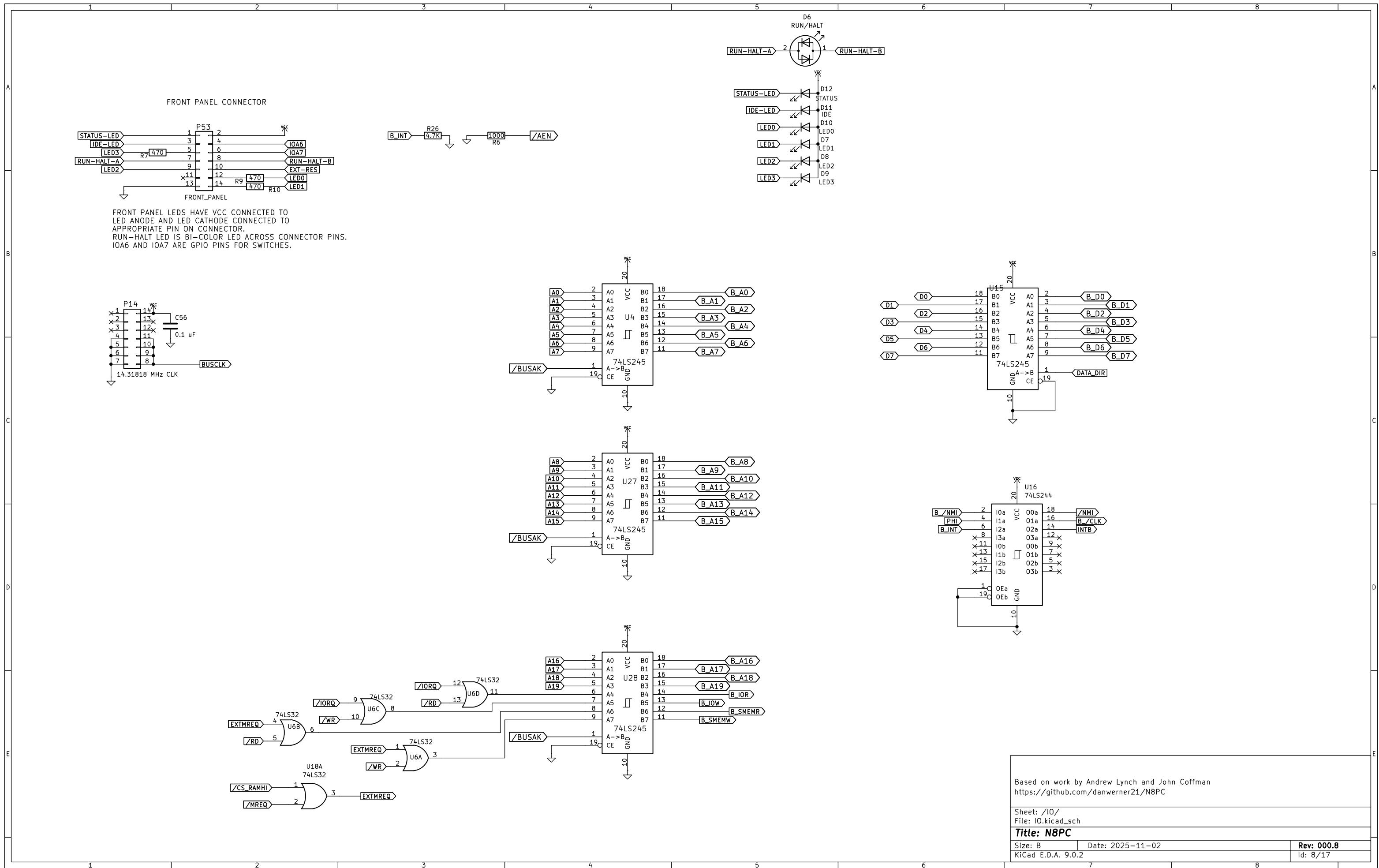
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

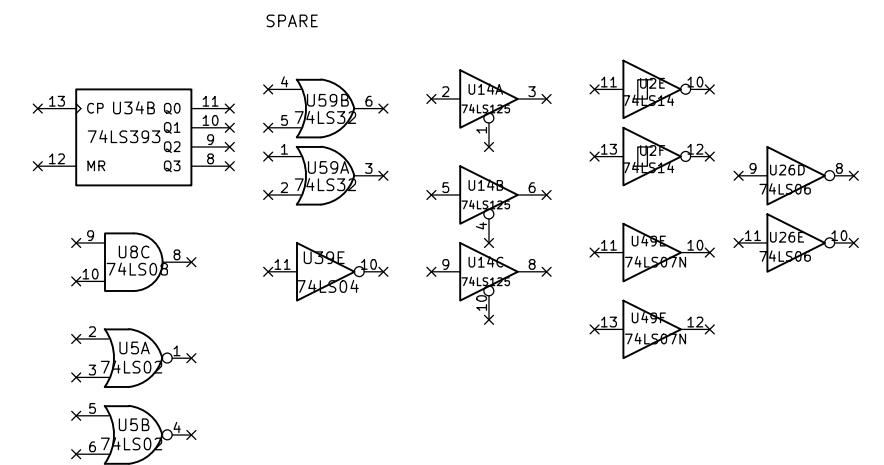
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Title: N8PC

Size: B Date: 2025-11-02
KiCad E.D.A. 9.0.2

Rev: 000.8
Id: 7/17





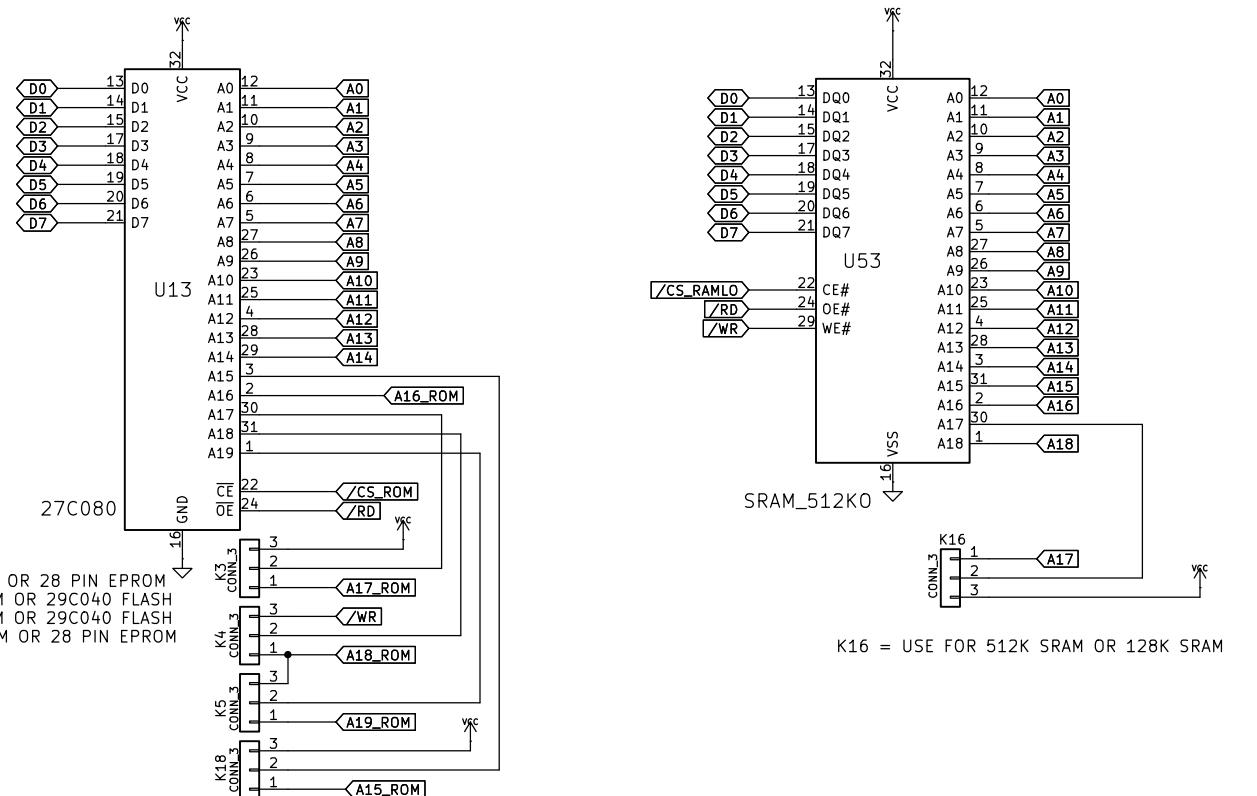
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /Spare/
File: Spare.kicad_sch

Title: N8PC

Size: B Date: 2025-11-02
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K3 = USE FOR 32 PIN EPROM OR 28 PIN EPROM
K4 = USE FOR 27C080 EPROM OR 29C040 FLASH
K5 = USE FOR 27C080 EPROM OR 29C040 FLASH
K18 = USE FOR 32 PIN EPROM OR 28 PIN EPROM

K16 = USE FOR 512K SRAM OR 128K SRAM

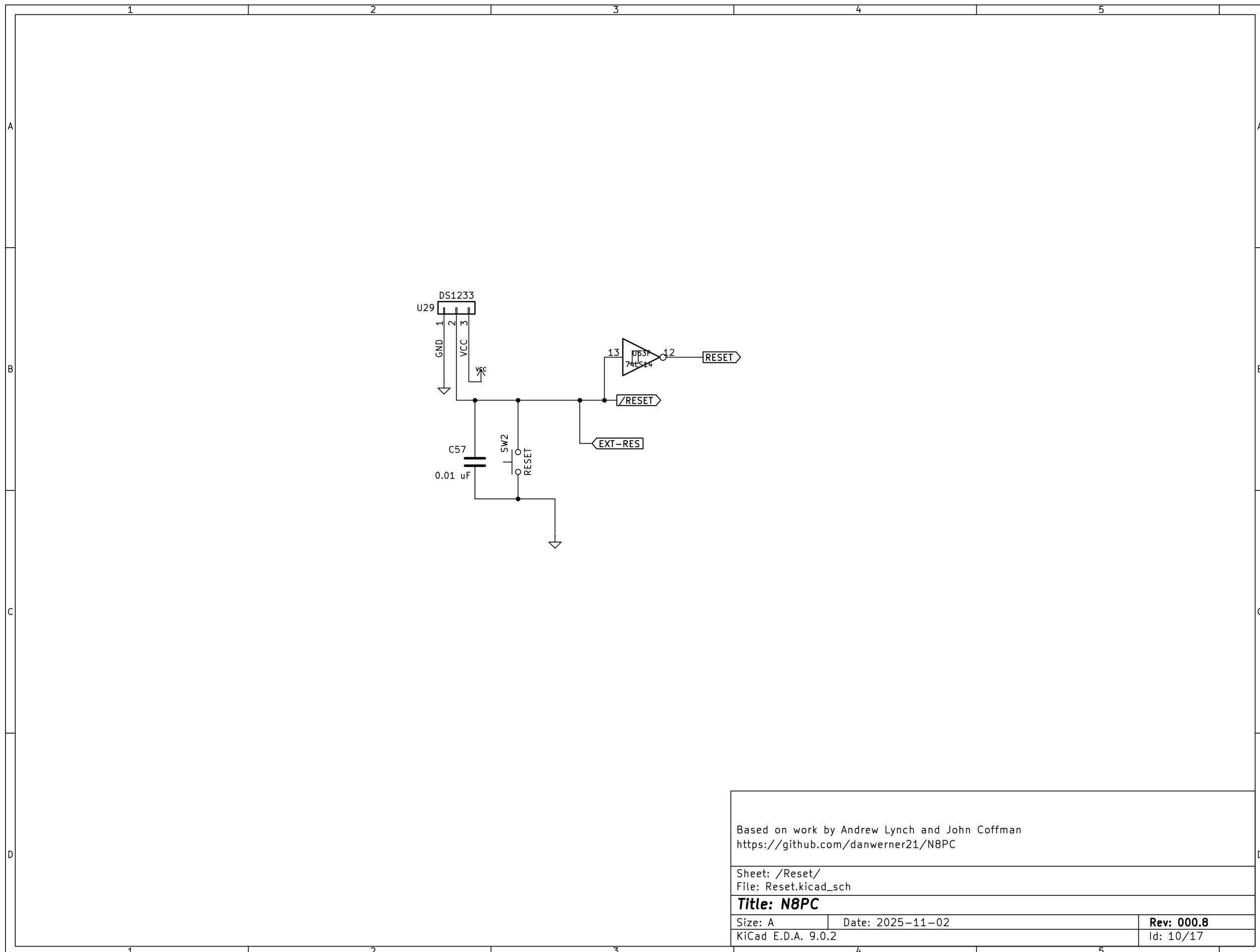
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

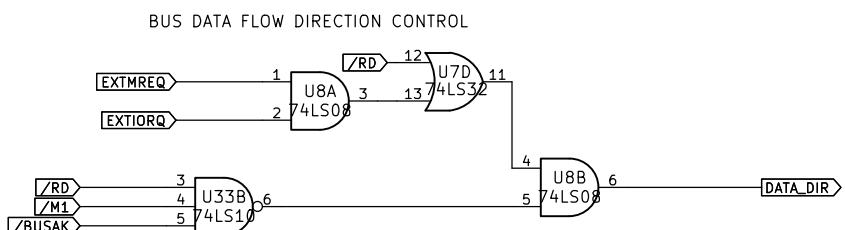
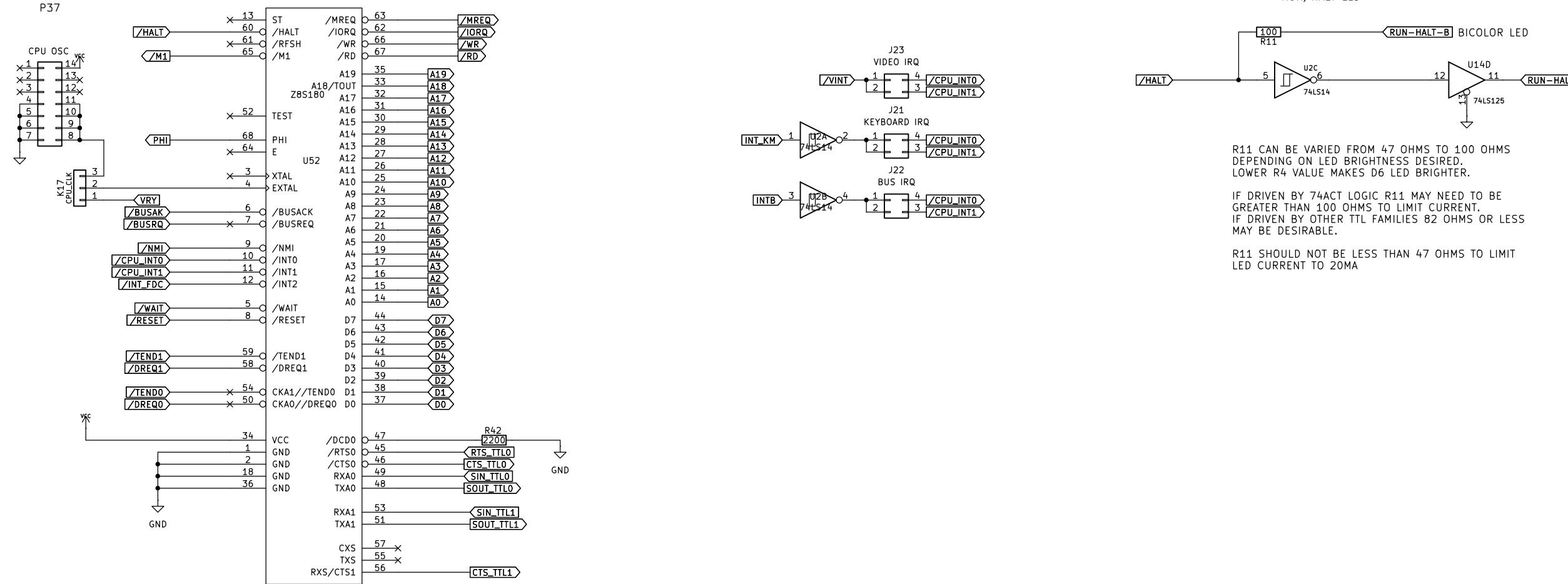
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Title: N8PC

Size: USLedger Date: 2025-11-02
KiCad E.D.A. 9.0.2

Rev: 000.8
Id: 10/17





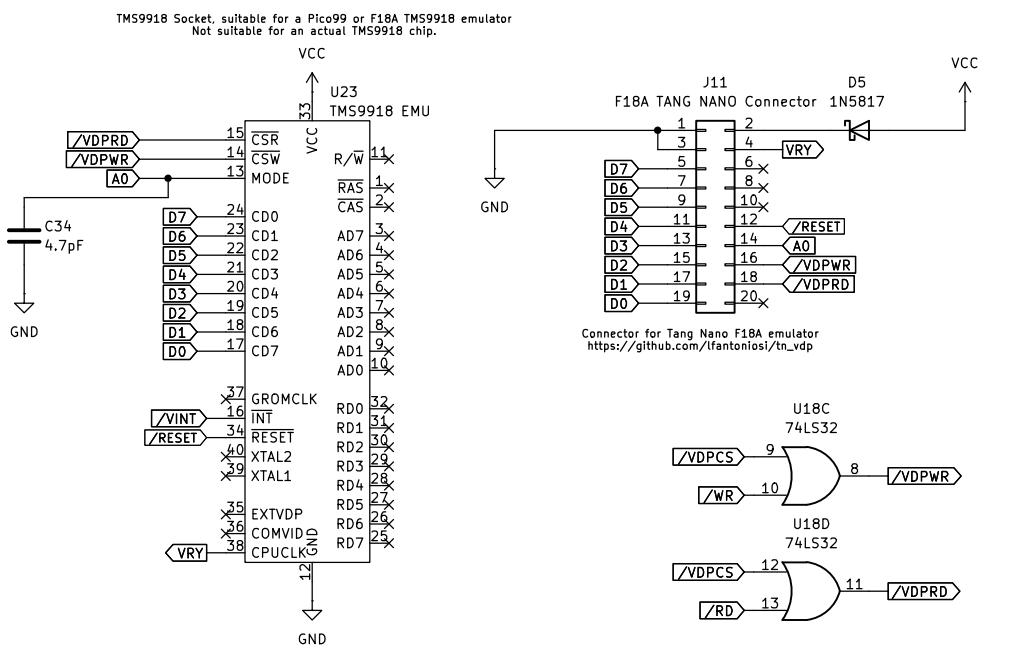
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /CPU/
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Title: N8PC

Size: B Date: 2025-11-02
KiCad E.D.A. 9.0.2

Rev: 000.8
Id: 11/17



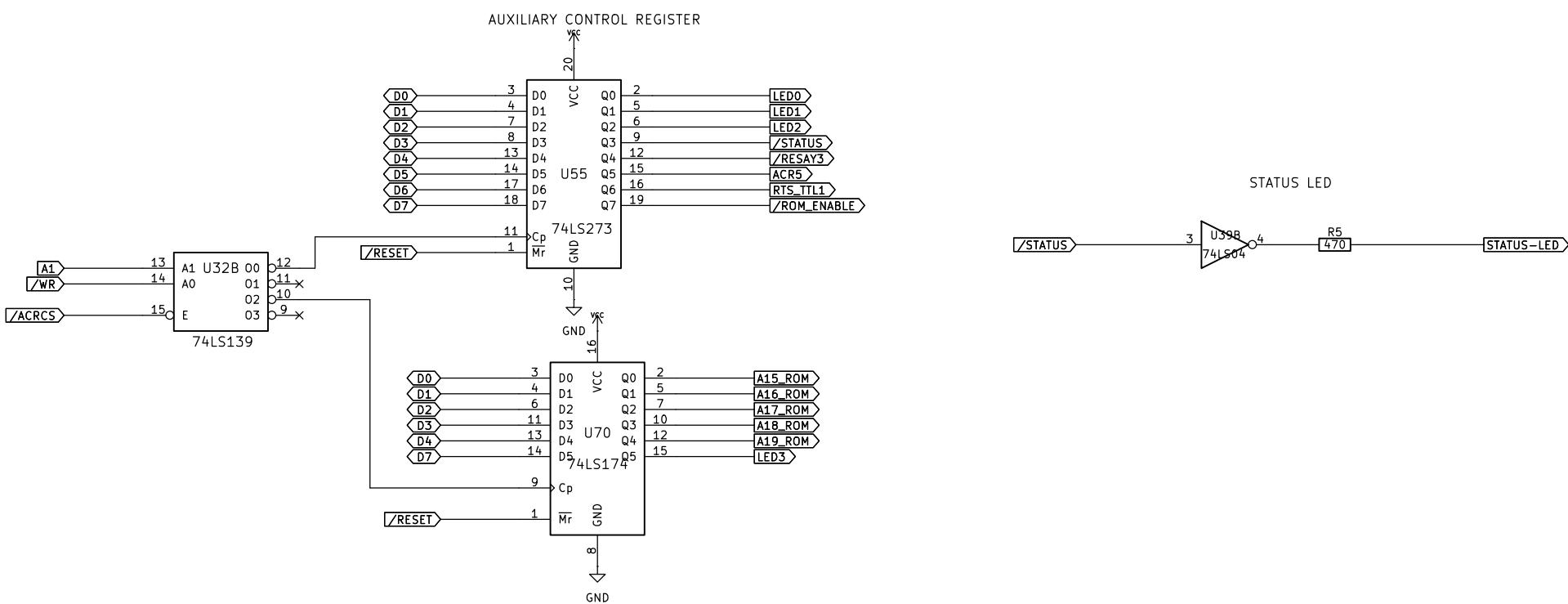
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

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Title: N8PC
Size: USLetter Date: 2025-11-02

KiCad E.D.A. 9.0.2

Id: 11/17



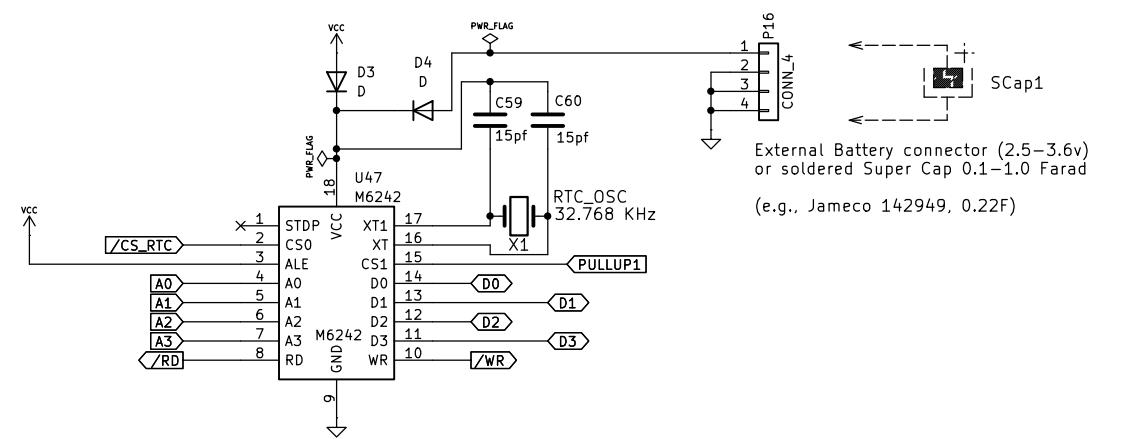
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /acr/
 File: acr.kicad_sch

Title: N8PC

Size: USLetter Date: 2025-11-02
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Rev: 000.8
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External Battery connector (2.5–3.6v)
(e.g., Jameco 142949, 0.22F)

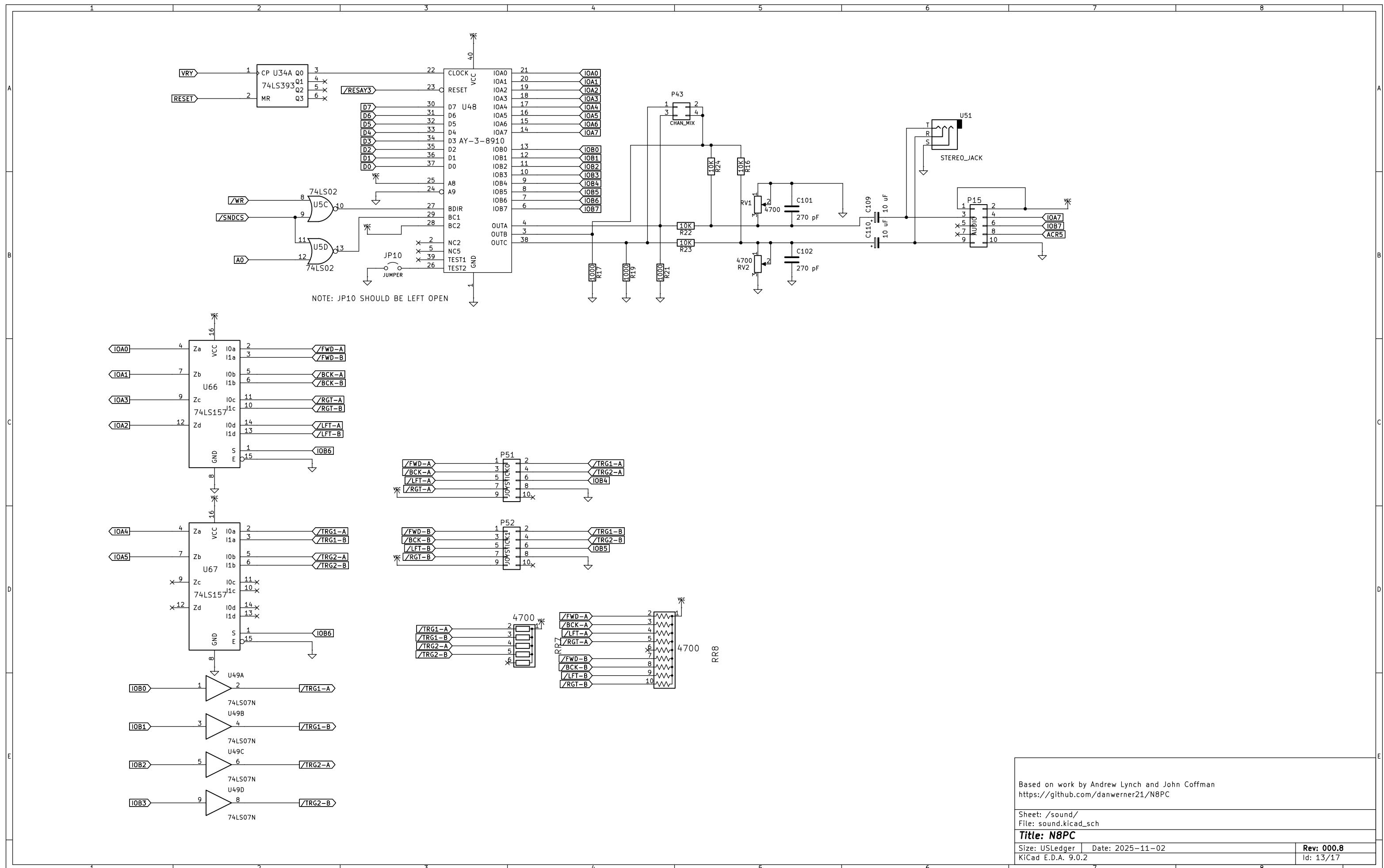
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

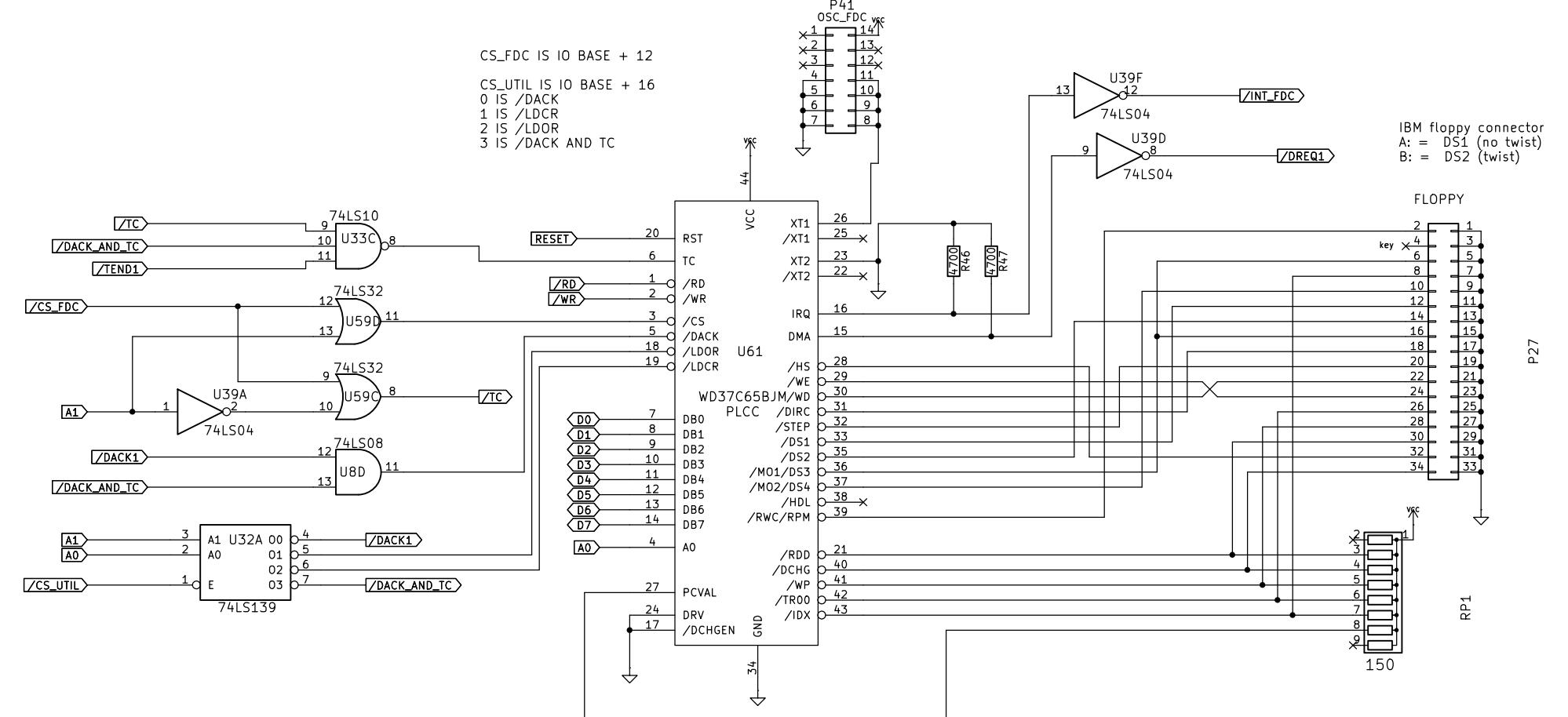
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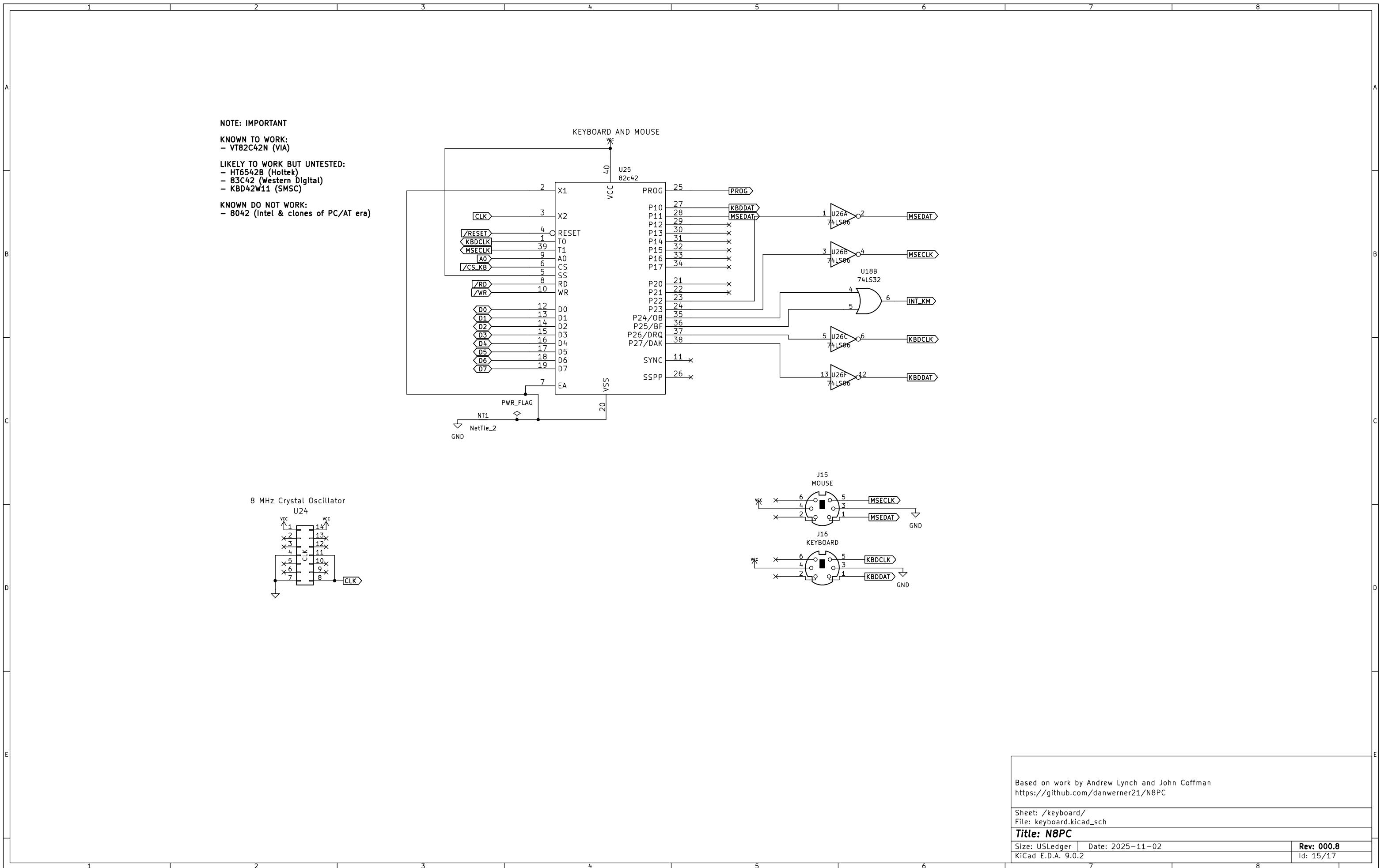
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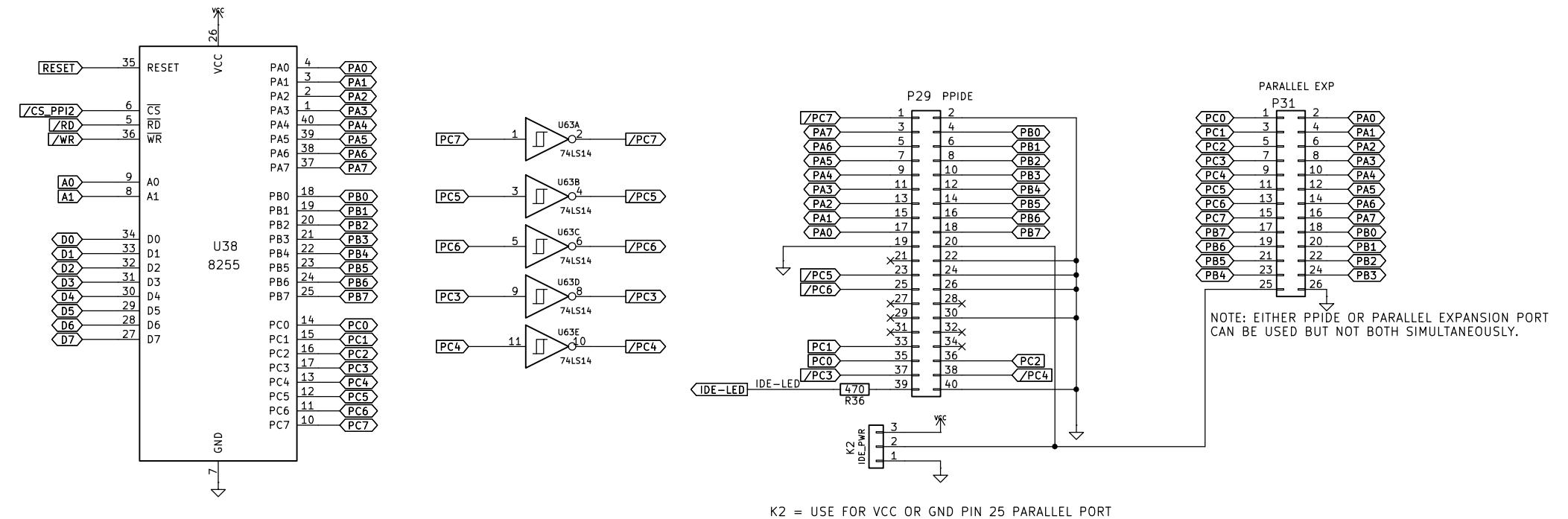
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Id: 13/17









Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /ide/
 File: ide.kicad_sch

Title: N8PC

Size: USLetter Date: 2025-11-02
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Rev: 000.8
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