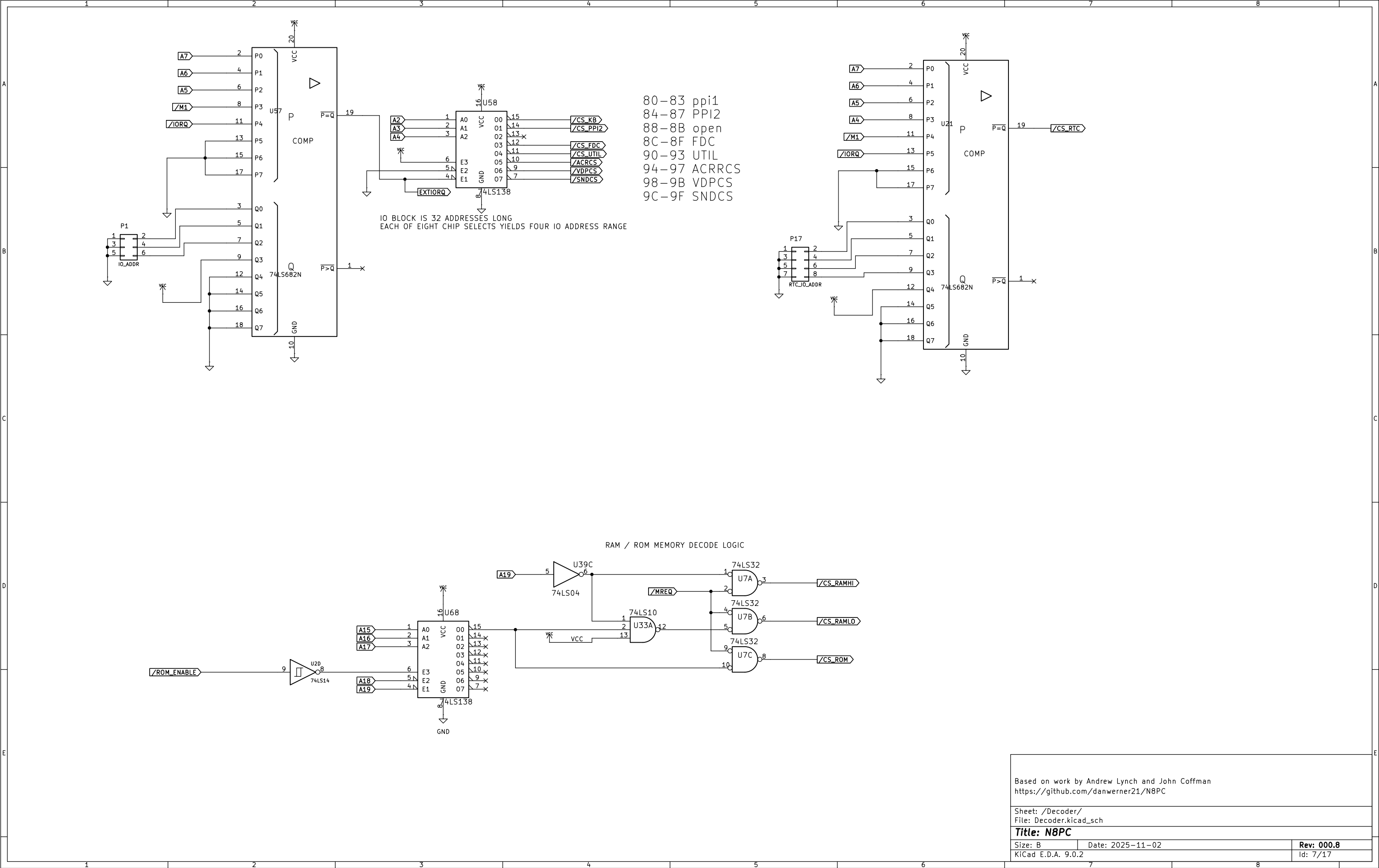


Id: 6/17



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /Decoder/
File: Decoder.kicad_sch

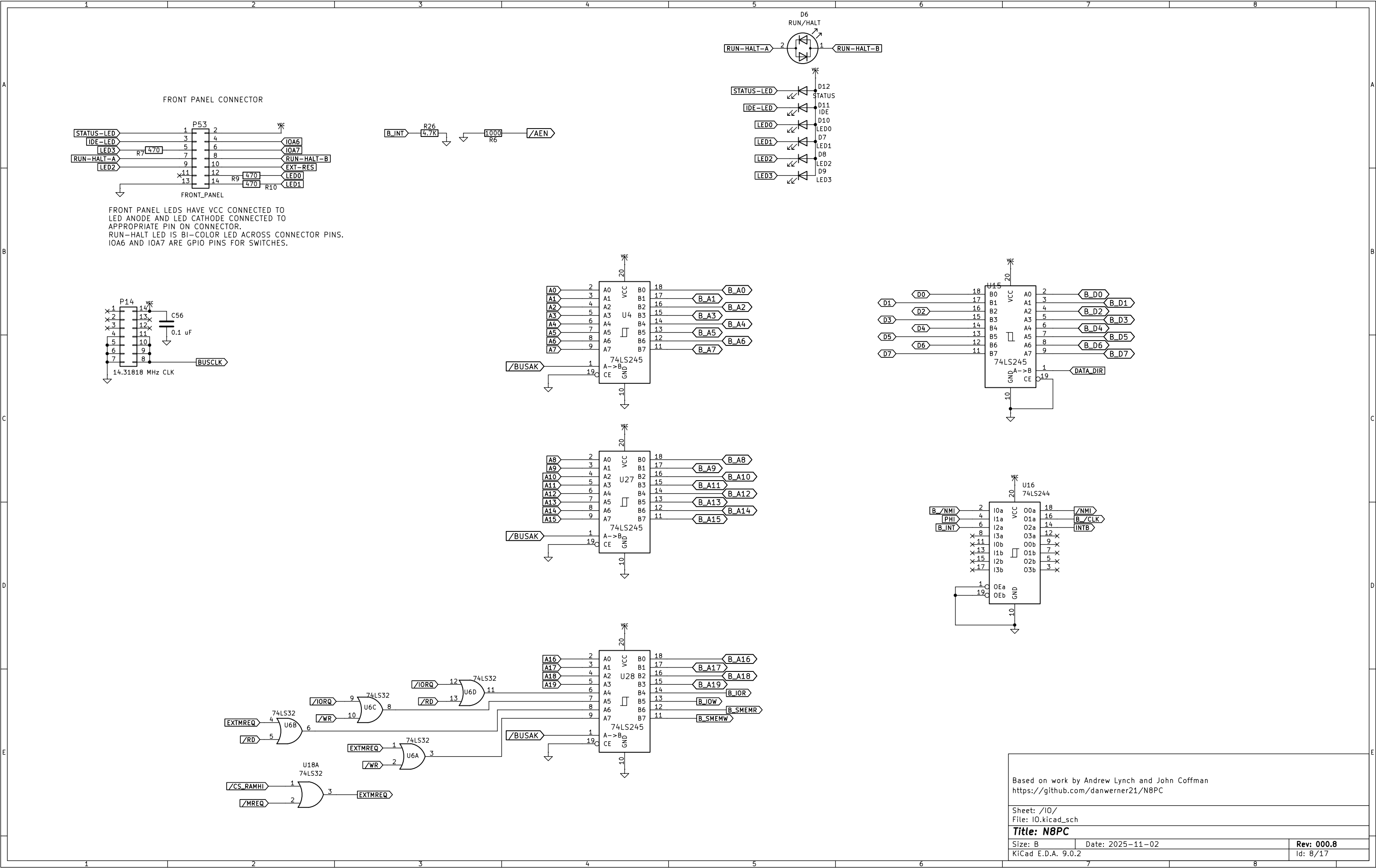
Title: N8PC

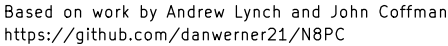
Size: B
KiCad E.D.A. 9.0.2

Date: 2025-11-02

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Sheet: /Spare/
File: Spare.kicad_sch

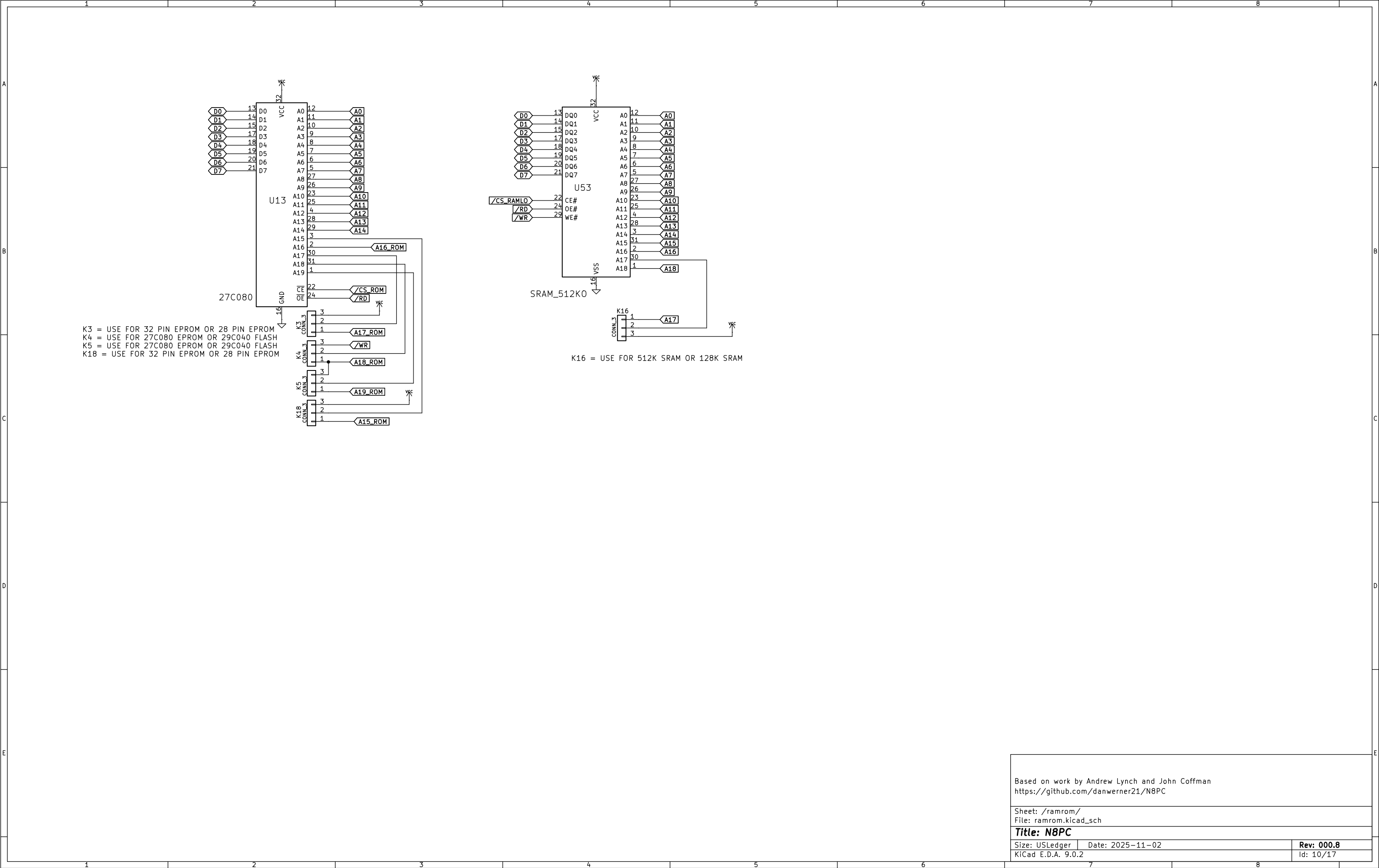
Title: N8PC

Size: B	
KiCad E.D.A. 9.0.2	

Date: 2025-11-02

Rev: 000.8

d: 9/17



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /ramrom/
File: ramrom.kicad_sch

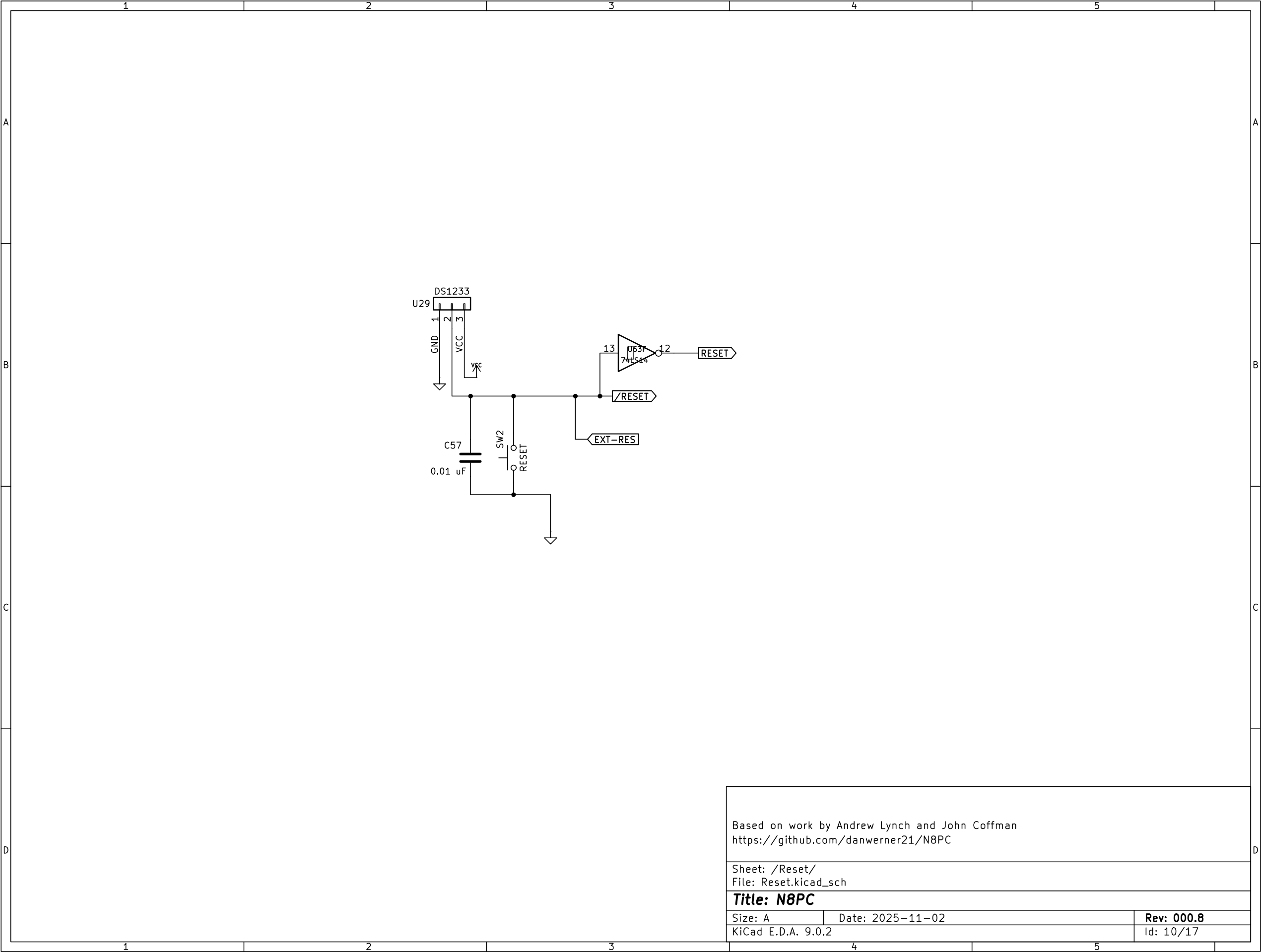
Title: N8PC

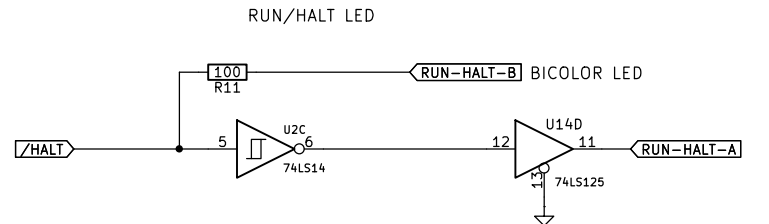
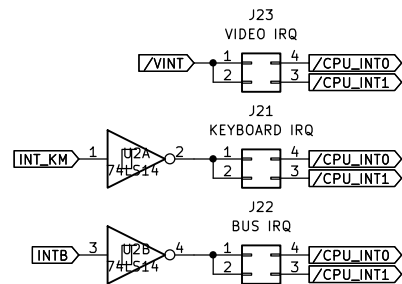
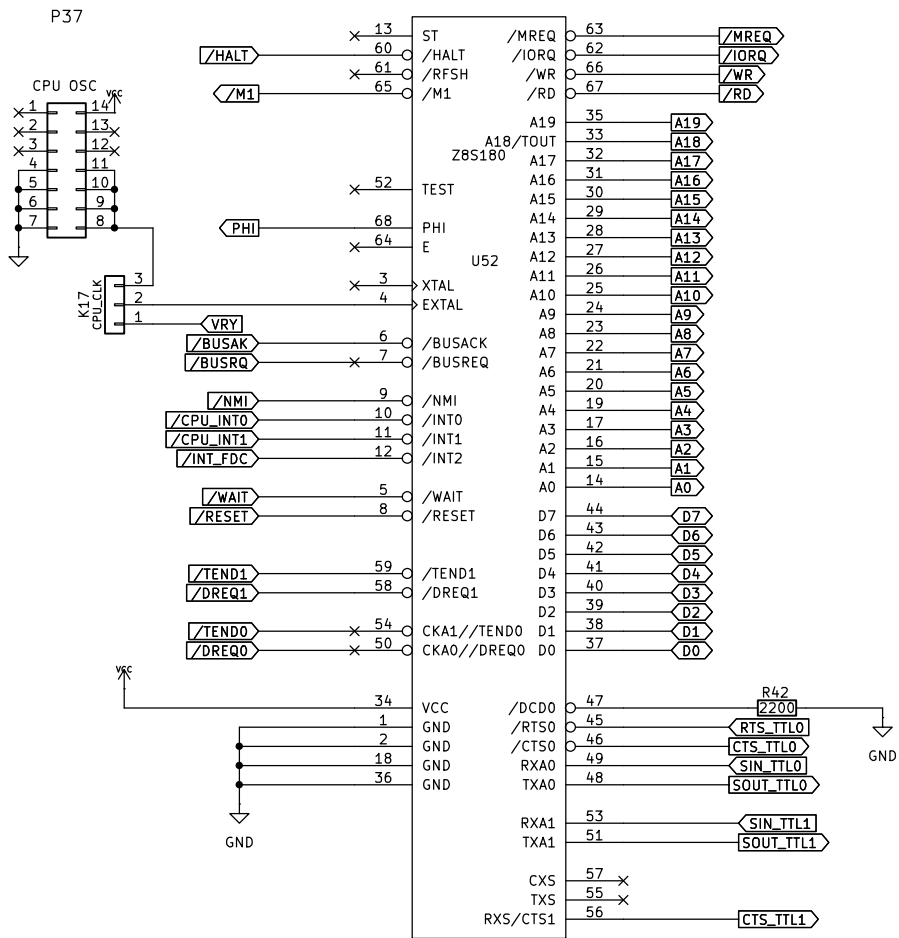
Size: USLedger Date: 2025-11-02

KiCad E.D.A. 9.0.2

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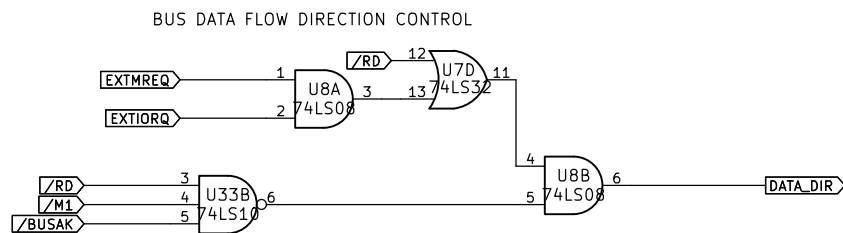




R11 CAN BE VARIED FROM 47 OHMS TO 100 OHMS DEPENDING ON LED BRIGHTNESS DESIRED. LOWER R4 VALUE MAKES D6 LED BRIGHTER.

IF DRIVEN BY 74ACT LOGIC R11 MAY NEED TO BE GREATER THAN 100 OHMS TO LIMIT CURRENT. IF DRIVEN BY OTHER TTL FAMILIES 82 OHMS OR LESS MAY BE DESIRABLE.

R11 SHOULD NOT BE LESS THAN 47 OHMS TO LIMIT LED CURRENT TO 20MA



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /CPU/
File: cpu.kicad_sch

Title: N8PC

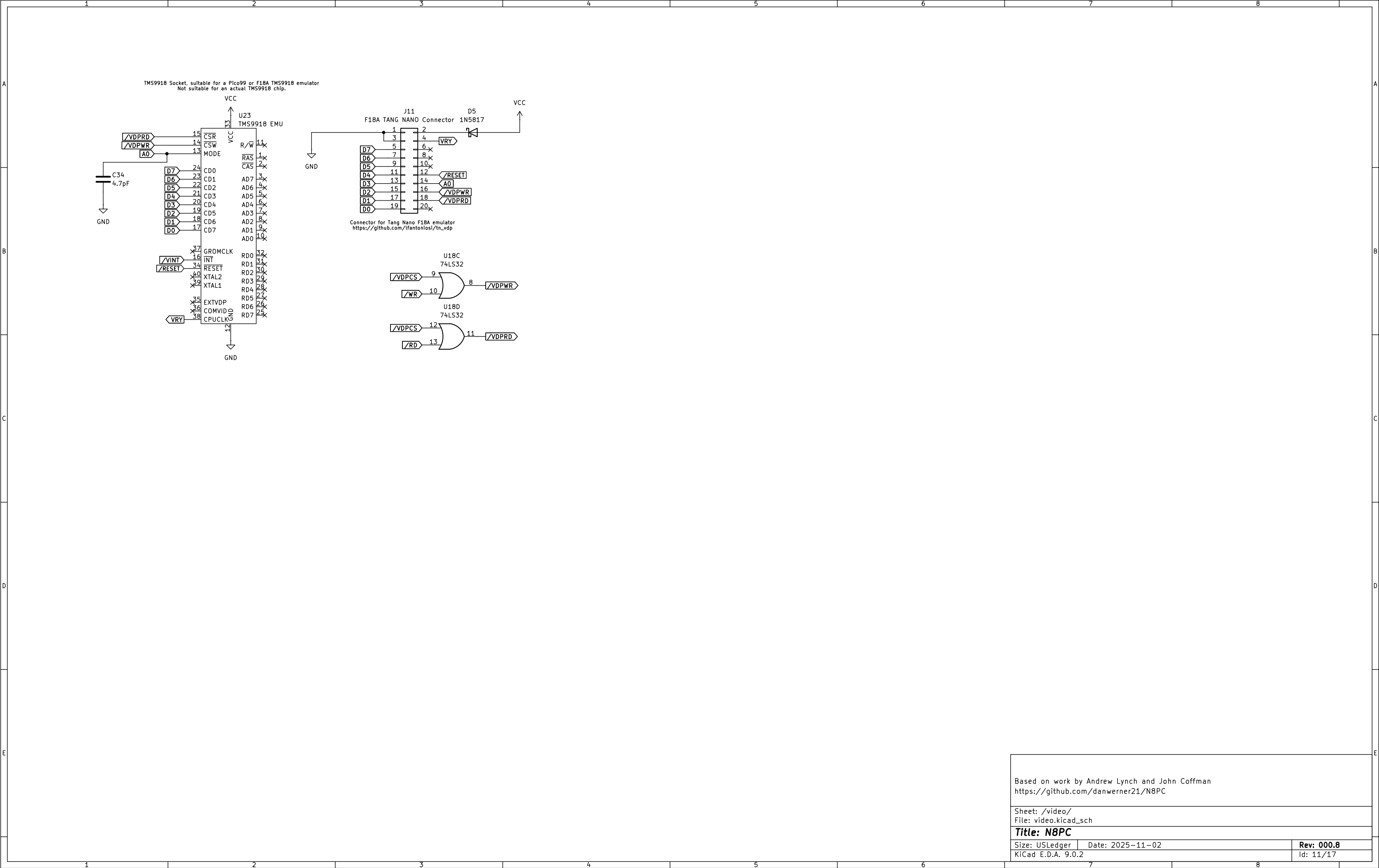
Size: B

Date: 2025-11-02

Rev: 000.8

KiCad E.D.A. 9.0.2

Id: 11/17



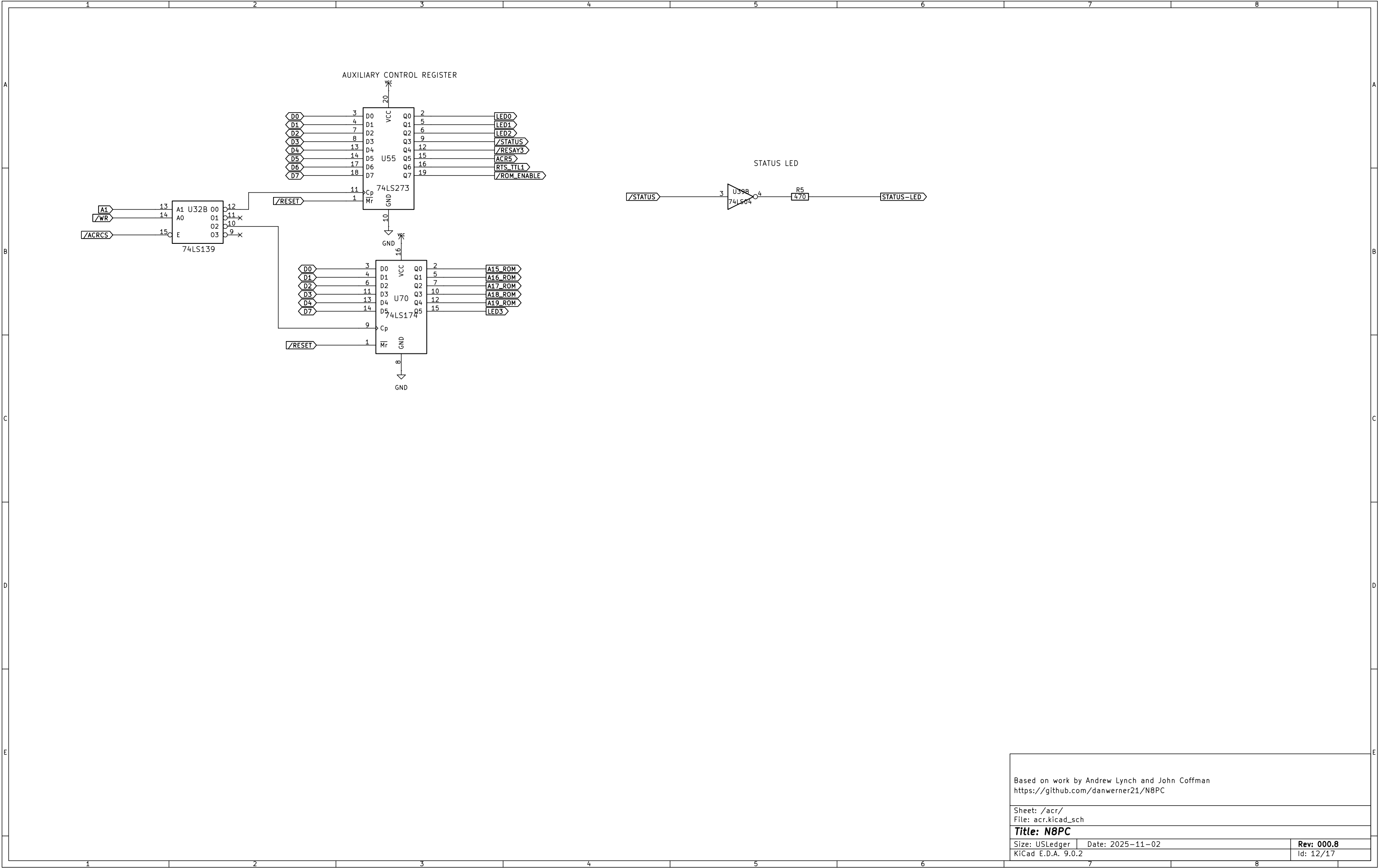
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /video/
File: video.kicad_sch

Title: N8PC

Size: USLedger Date: 2025-11-02
KiCad E.D.A. 9.0.2

Rev: 000.8
Id: 11/17



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /acr/
File: acr.kicad_sch

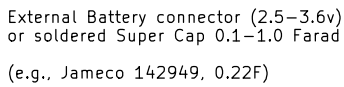
Title: N8PC

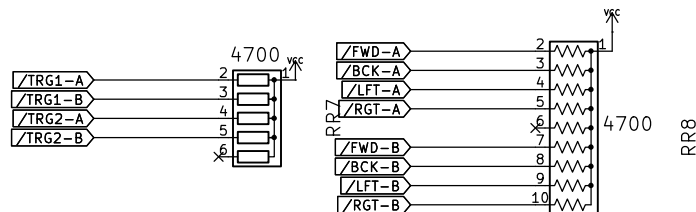
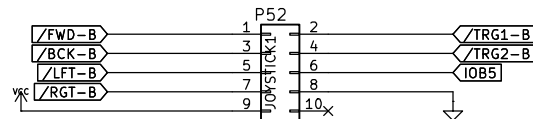
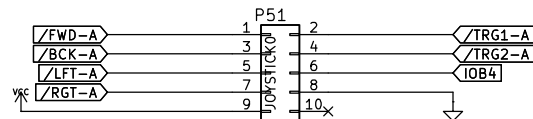
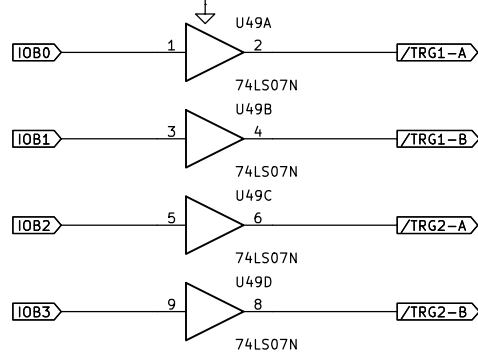
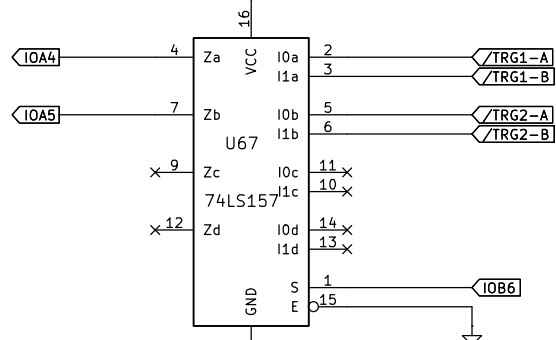
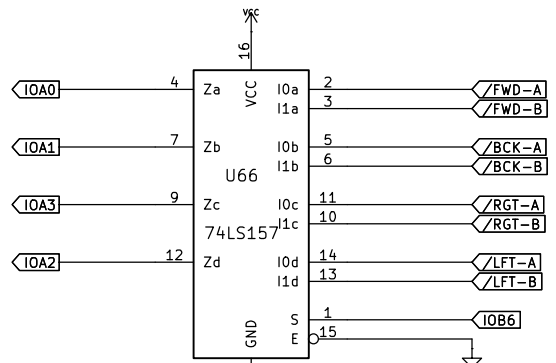
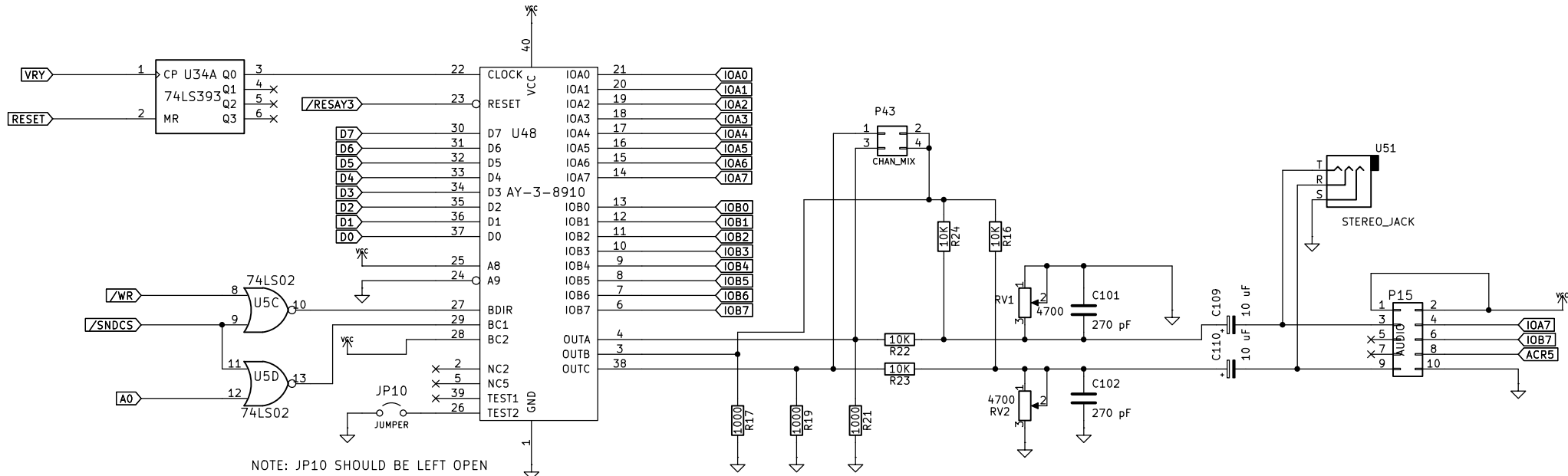
Size: USLedger Date: 2025-11-02

Rev: 000.8

KiCad E.D.A. 9.0.2

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d: 13/17



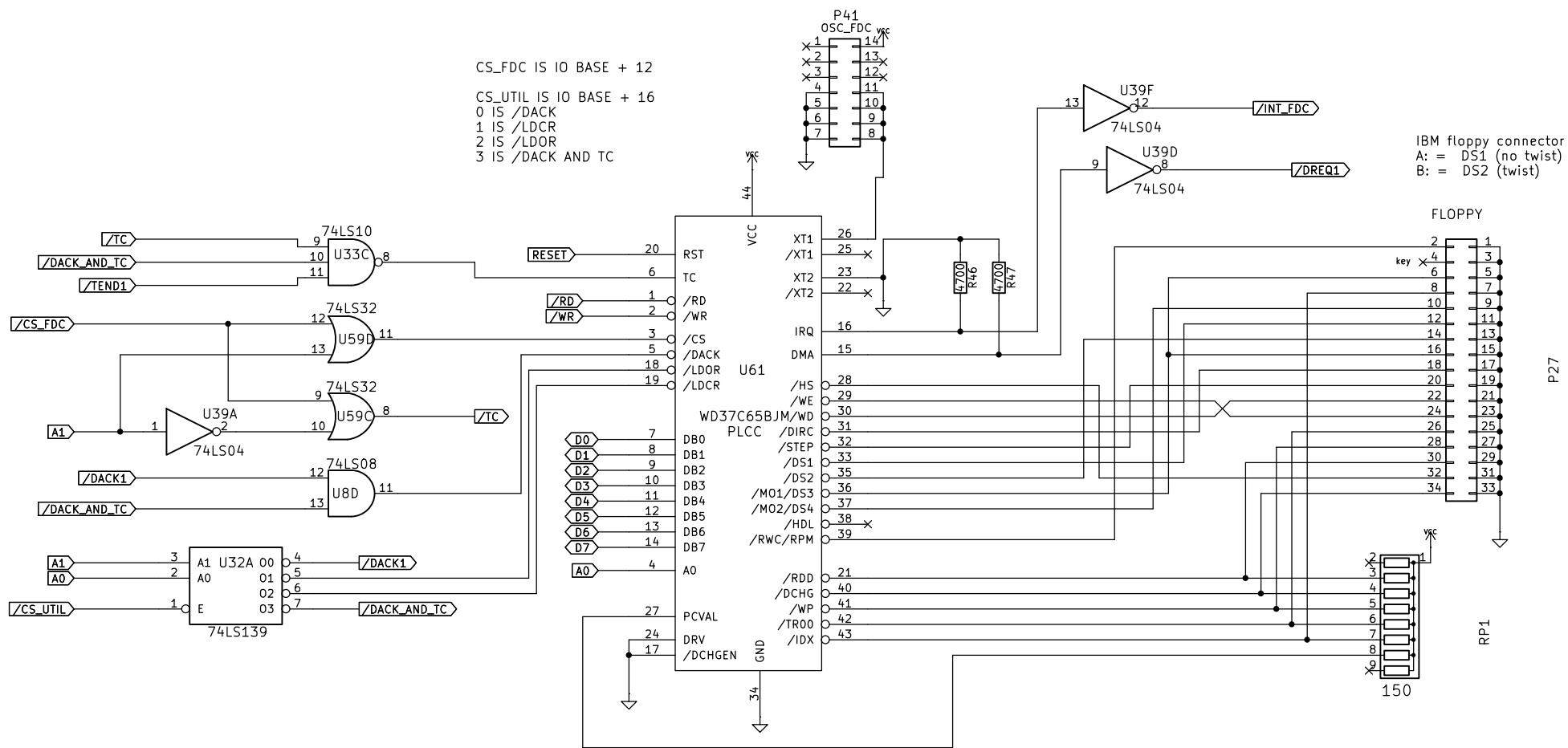
Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /sound/
 File: sound.kicad_sch

Title: N8PC

Size: USLedger Date: 2025-11-02
 KiCad E.D.A. 9.0.2

Rev: 000.8
 Id: 13/17



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /floppy/
 File: floppy.kicad_sch

Title: N8PC

Size: USLedger Date: 2025-11-02
 KiCad E.D.A. 9.0.2

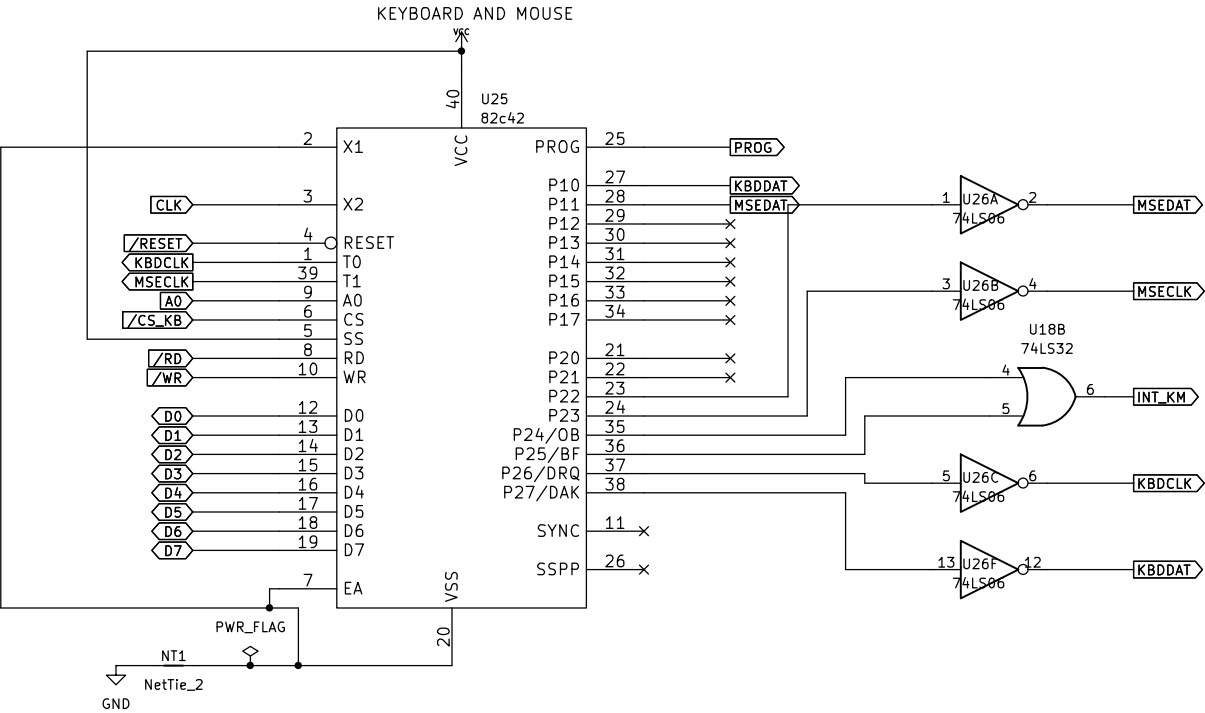
Rev: 000.8
 Id: 14/17

NOTE: IMPORTANT

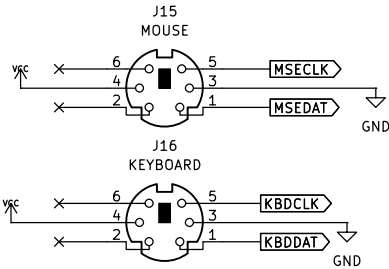
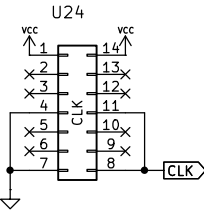
KNOWN TO WORK:
- VT82C42N (VIA)

LIKELY TO WORK BUT UNTESTED:
- HT6542B (Holtek)
- 83C42 (Western Digital)
- KBD42W11 (SMSC)

KNOWN DO NOT WORK:
- 8042 (Intel & clones of PC/AT era)



8 MHz Crystal Oscillator



Based on work by Andrew Lynch and John Coffman
<https://github.com/danwerner21/N8PC>

Sheet: /keyboard/
File: keyboard.kicad_sch

Title: N8PC

Size: USLedger Date: 2025-11-02
KiCad E.D.A. 9.0.2

Rev: 000.8
Id: 15/17

