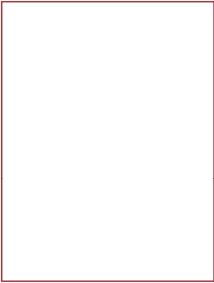


CPU



File: CPU.kicad_sch

AddressDecoding



File: AddressDecoding.kicad_sch

ROM/RAM/Sound



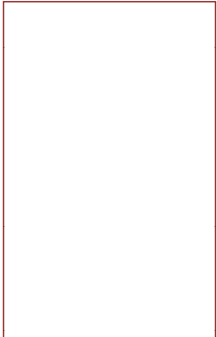
File: ROM-RAM-Sound.kicad_sch

Video



File: Video.kicad_sch

Keyboard



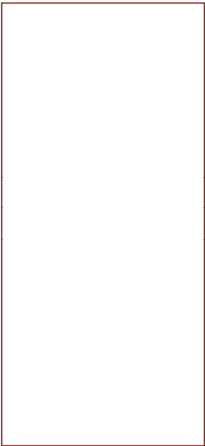
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GROM-I/O-Port



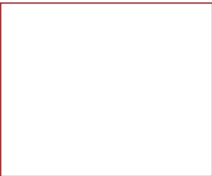
File: GROM-I/O-Port.kicad_sch

8Bit16BitSwitch



File: 8bit16bitSwitch.kicad_sch

Power

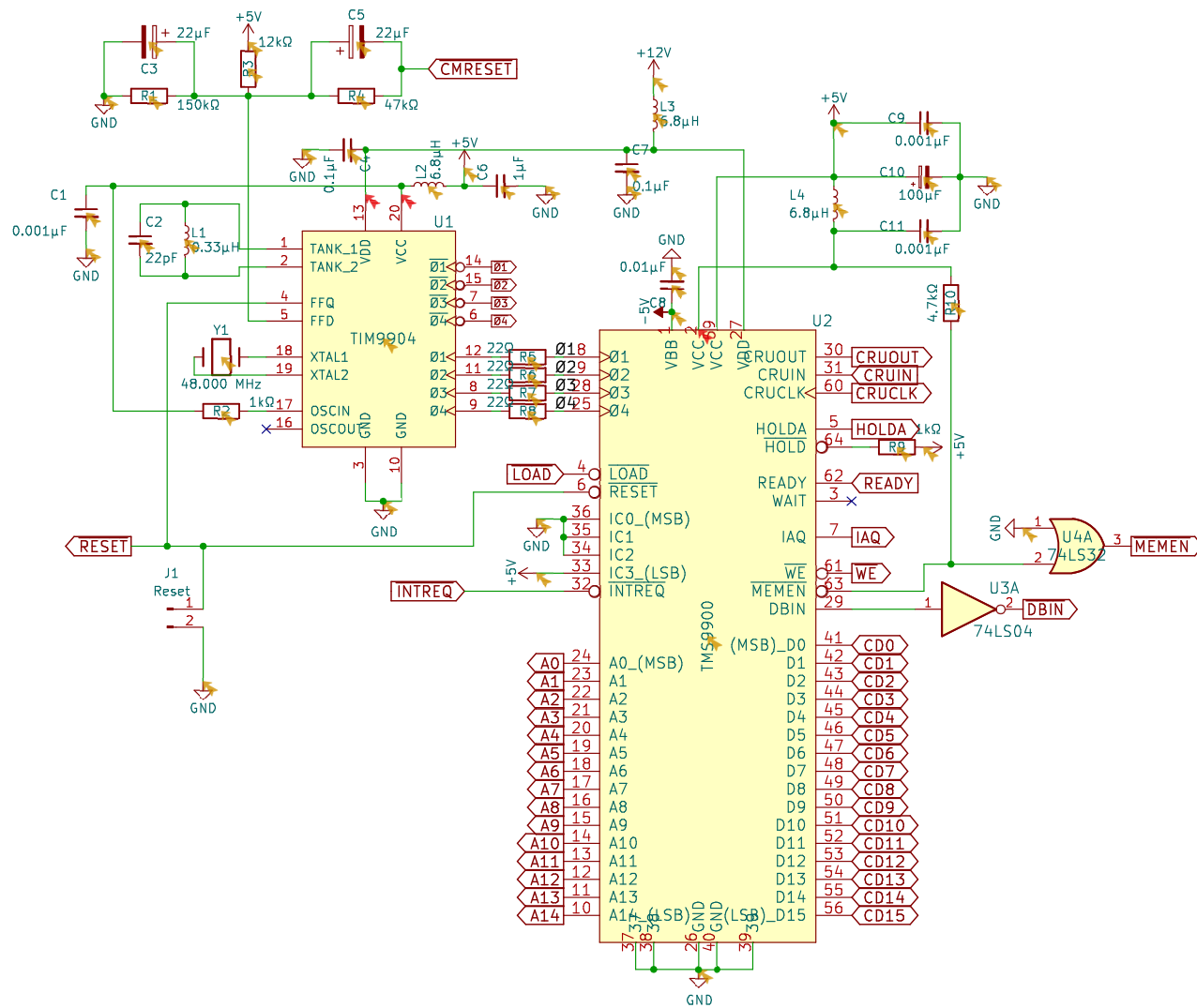


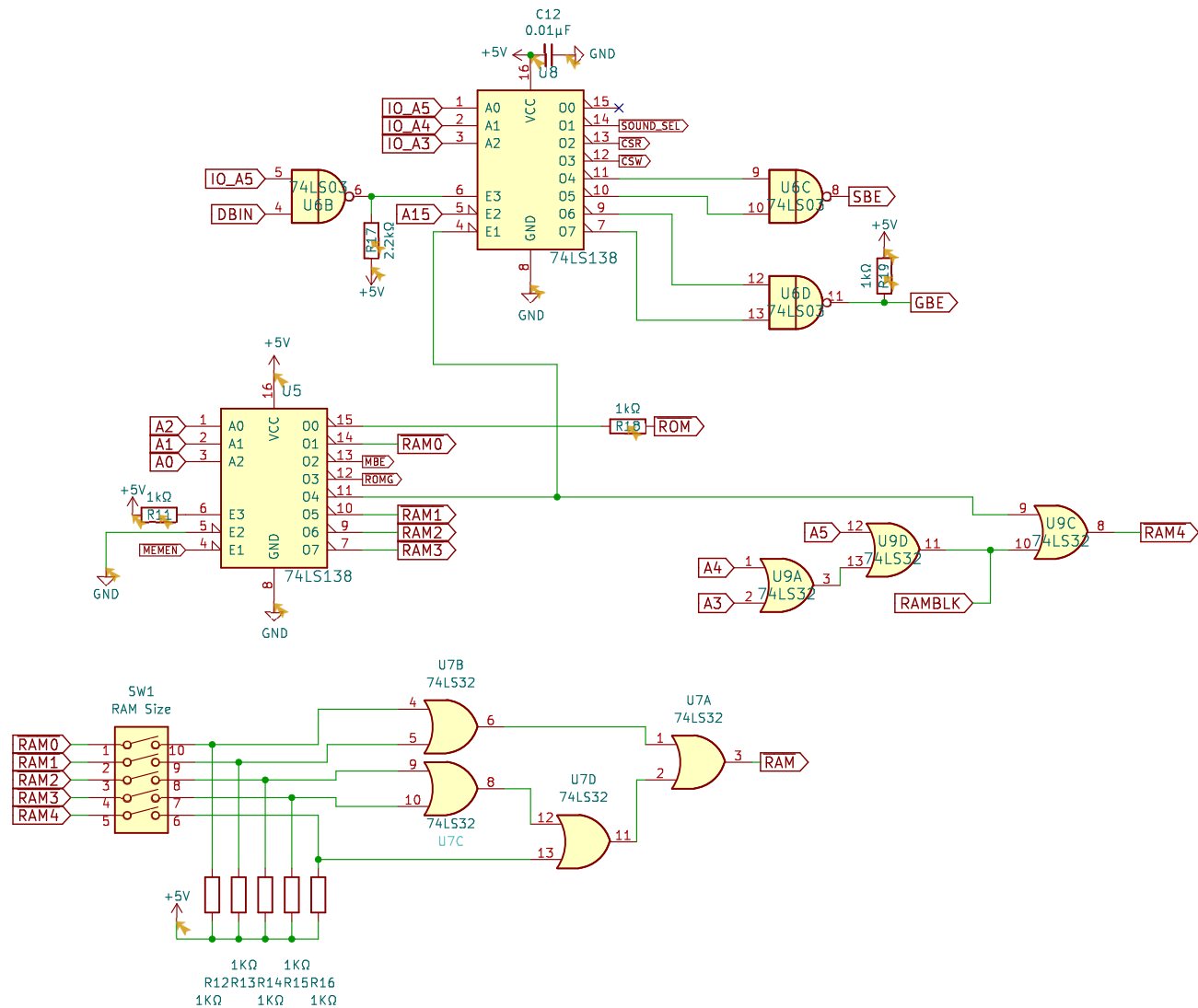
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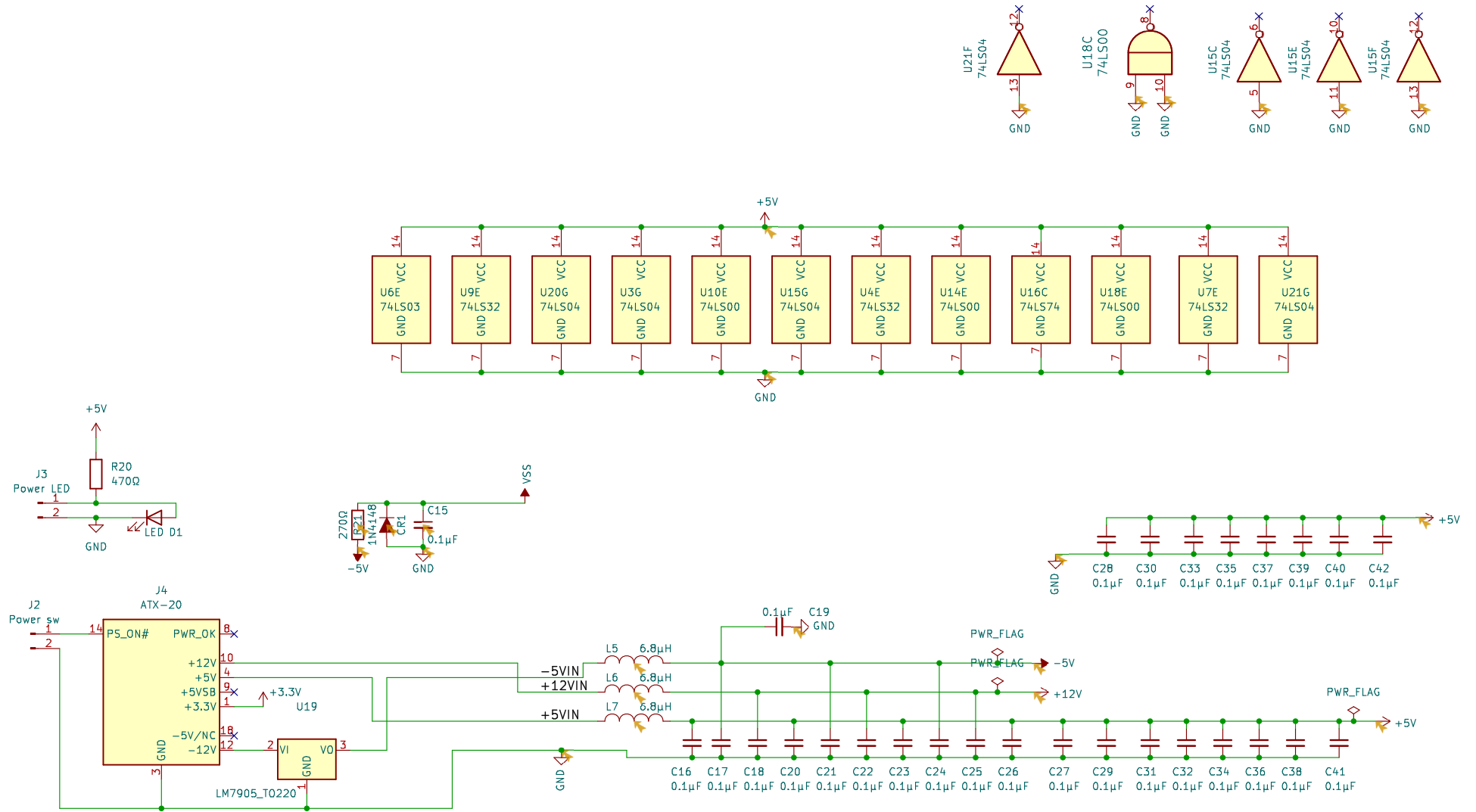
GROM-Logic

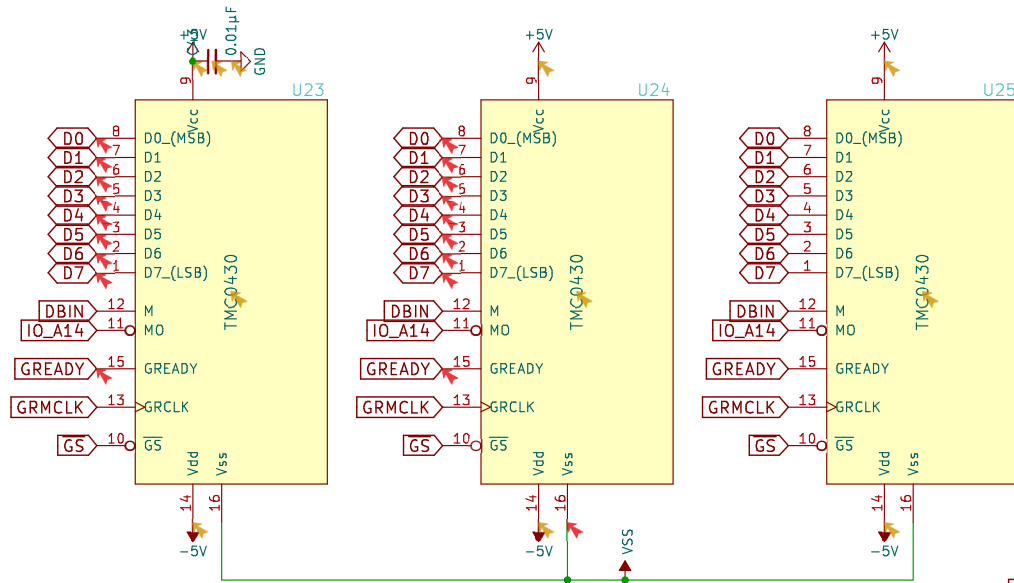


File: GROM-Logic.kicad_sch









GROMs are left in for compatibility, in case GROM chips are available for testing. The intention is to emulate the GROMs with an ESP32.

