

## Modification to run CPUxxCMI on 3MHz bus.

Replace xtal into 12MHz.

Replace C1 and C2 by 15 pF.

Place 10M 1206 smd between crystal pins.

Use the HD63C09 or MC68B02.

**You are done**, ..... but for the HD63C09 add the Q clock for a better timing.

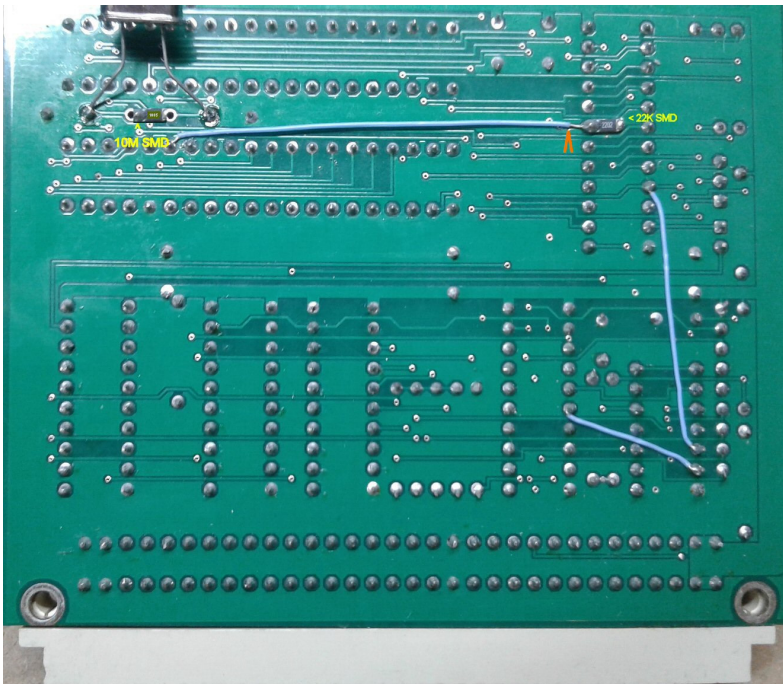
Replace GAL CMI\_1 by CMI-4\_1

Use the HD63C09 or MC68B02 with full modification.

On solder side:

Cut trace between U13-19 and via (OVMA)

Place 22K resistor between U13-19 and Gnd



To keep CPUxxCMI 6802 compatible:

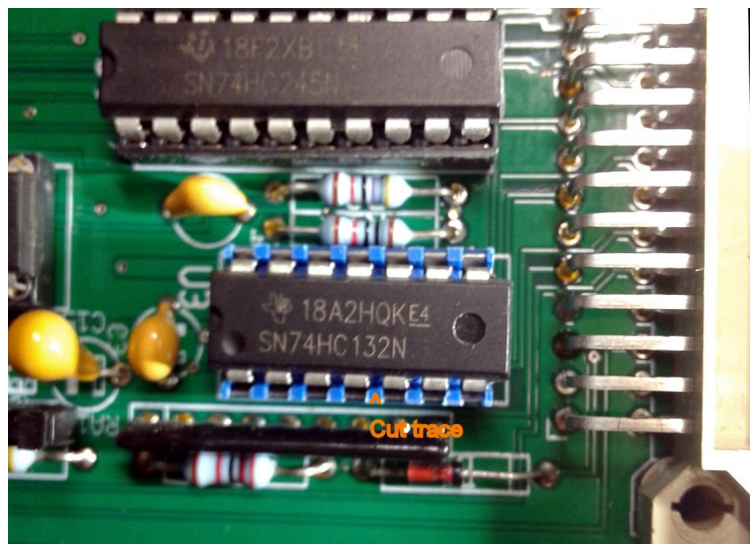
On top side:

Cut trace between U3-4 and U3-5

On solder side:

Place wire from U3-5 to U13-9

Place wire from U3-6 to U4-6



Remarks:

Works even fine with HD6350., MC6850.

Board also tested with S6802.

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