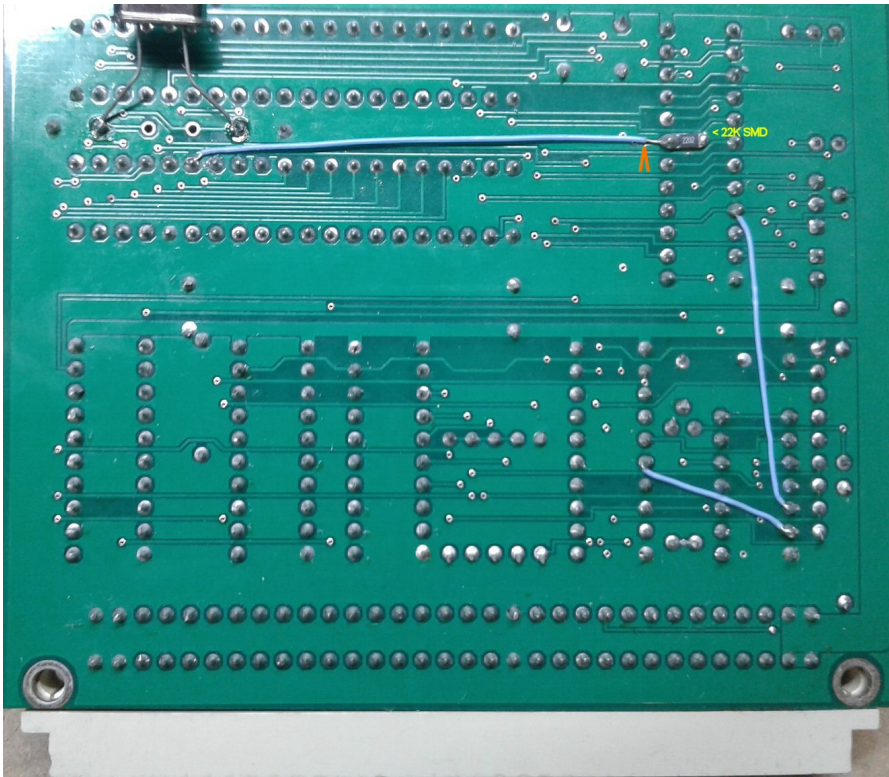# Modification to run CPU09CMI on 3MHz bus.

Replace xtal into 12MHz
Replace GAL CMI_1 by CMI-4_1

On solder side:
Cut trace between U13-19 and via (OVMA)
Place 22K resistor between U13-19 and Gnd

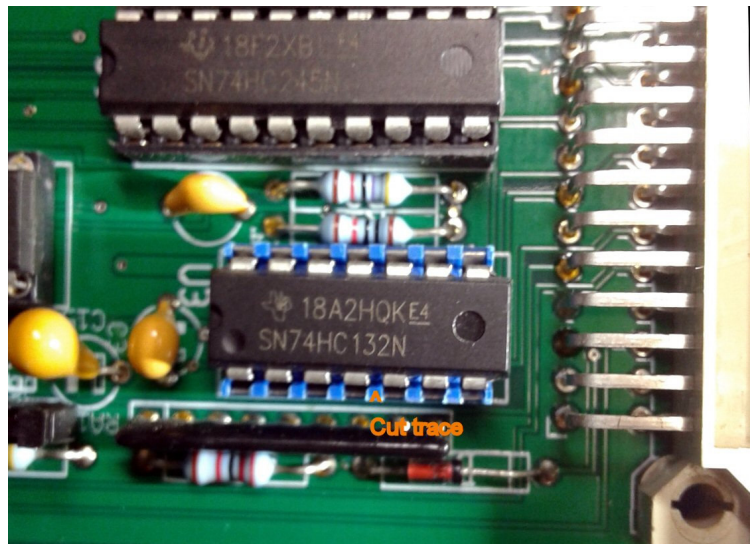To keep CPUxxCMI 6802 compatible:

On top side:
Cut trace between U3-4 and U3-5

On solder side:
Place wire from U3-5 to U13-9
Place wire from U3-6 to U4-6

Remarks:
    Still some work to do on FLEX/MON switch.
    Not tested with 6802.

CdeJ