

Modification to run CPU09CMI on 4MHz bus.

Now we need the Q clock on the 09FLP

Replace xtal into 16MHz

Replace GAL CMI_1 by CMI-4_1

On top side:

Cut trace between U4-14 and J1 C-21 (B_VMA)

For REV:0.9 print only cut trace from J1 A23 (Gnd)

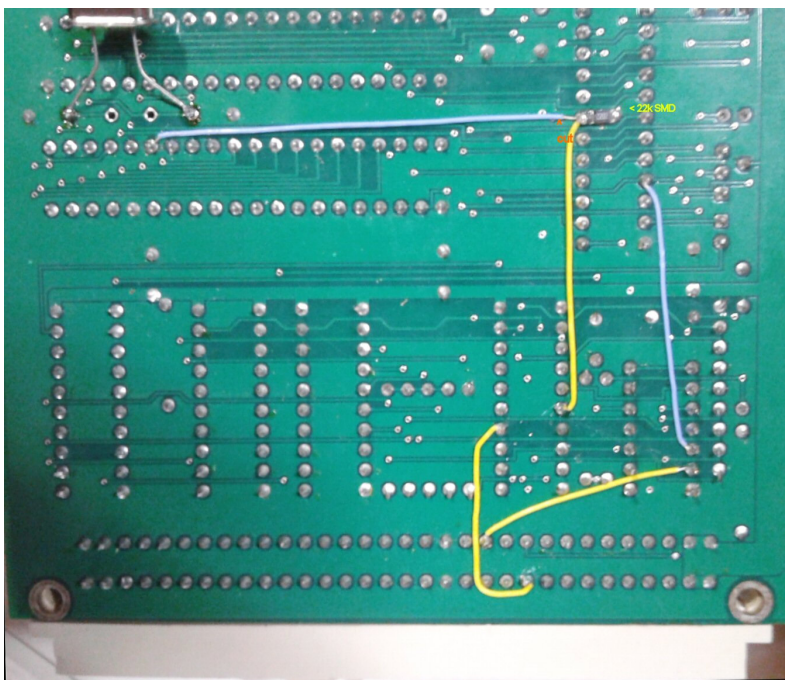
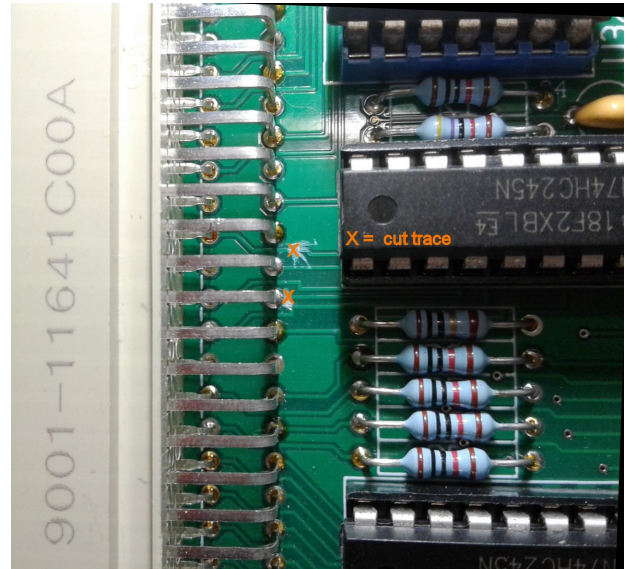
On solder side:

Cut trace between U13-19 and via (OVMA)

Place 22K resistor between U13-19 and Gnd

Place wire from U6-35 to U13-19 and U4-6 (Q)

Place wire from U4-14 to J1 A-23 (B_Q)



To keep CPUxxCMI 6802 compatible:

On top side:

Cut trace between U3-4 and U3-5

On solder side:

Place wire from U3-5 to U13-9 (VMA)

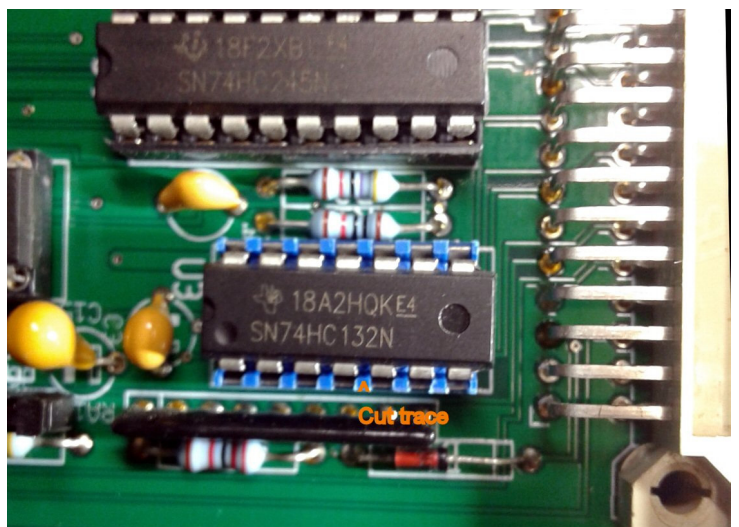
Place wire from U3-6 to J1 C-21 (B_VMA)

Remarks:

Still some work to do on FLEX/MON switch.

Not tested with 6802.

Should work on REV:0.9 and REV:1.1 print



CdeJ