Chip stress test in the CPU09CMI

chip	Bus 2 MHz		Bus 3 MHz		Bus 4 MHz	
HD6350	20	0	20	0	20	0
HD63B50	40	0	40	0	40	0
MC6850	15	0	15	0	15	0
MC68B50	13	0	13	0	13	0
HD6309						
HD63C09	40	0	20	0	20	0
MC6809						
MC68B09						
	Pcs	Fail	Pcs	Fail	Pcs	Fail

For all bus frequencies the PAL CMI-4_1 is used, which is using the Q clock.
All CPU's are tested by running FLEX.