DANYAL SIRAJ

 Github: github.com/danyalsiraj

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ABOUT

Electrical Engineer with a strong interest in hardware design and programming. Always working on new projects with passion to explore hardware technologies. My skills include, but are not limited to circuit design and simulation, strong grip in JAVA,C/C++, and MATLAB, problem solving, algorithms, and quality assurance.

EDUCATION

BASc in Electrical Engineering with Engineering Management

University of Ottawa

Sep 2012-Dec 2016

- Received Admission Scholarship
- Specialized in Systems Engineering
- Capstone: Developed windows program for cursor control via face/eye movement.

AREAS OF EXPERTISE

Circuits

- Knowledge of Basic circuit elements (resistors, capacitors, inductors).
- Experience in using oscilloscope, spectrum analyzer, current/voltage sources, multimeter, and signal generator.
- Expert in using computer-aided circuit simulation software such as PSpice, NI Multisim, and Quartus.
- Able to design, implement, and simulate circuits on the software.
- Able to design, implement, and debug circuits on computer and on breadboard with efficiency.
- Knowledge of PCBs and how to repair them.
- Soldering and desoldering.

Programming Languages

- Languages: Java, Javascript, C, C++, MATLAB, Arduino (Embedded C), VHDL
- Frameworks: React, React Native, Node, Bootstrap, Freemarker, OpenCV
- IDE: Eclipse, Sublime, Atom, Visual Studio Ultimate, Visual Studio Code, DBeaver
- Databases: PostgreSQL, MySQL, NoSQL, MongoDB
- Frontend: HTML, CSS, ReactJS.Backend: Java and Javascript
- Arduino programming and VHDL (FPGA programming).
- Experience in software application testing and web testing.
- Version control using git.

PROFESSIONAL EXPERIENCE

Full Stack Developer

Freshbe

Nov 2017-Present

- Design and develop Mobile and REST API
- Conduct requirements analysis

- Built from scratch
- Experience agile SDLC
- Project Management using github projects
- Technologies used: Javascript, React Native, Heroku, PostgreSQL, Node, Mocha, Draw.io

Data/ARTEMIS Support

Aug 2017-Present

SBE Canada Ltd

- Build reports based on client requirements
- Responsible for managing/assigning role based access in ARTEMIS
- Create locks(acceptance criteria for release) for devices
- Research into solutions to improve release processes

Function Testing (Screener)

May 2017-July 2017

SBE Canada Ltd

- Test the cell phones and tablets for functional and cosmetic faults.
- Perform function tests by using auto test codes or manually.
- Troubleshoot to resolve software issues(i.e. Hard reset).
- Assign grades to all the devices based on the cosmetic condition.
- Scan all the devices in computer and update the device information.

C++/OpenCV Developer

Jan 2016-Dec 2016

Capstone Project

- Maia - Design and Develop Cursor Control via Eye Tracking.
- Developed MATLAB simulation to validate the concept.
- Used C++ and Open CV to develop the tracking software.
- Created executable file to allow portability for windows platform.

Junior Quality Assurance Analyst

Sep 2015-Dec 2015

Innovation Science and Economic Development Canada

- Testing such as: Functional, Integration, System, Regression, and User Acceptance.
- Creating/executing test plans and test cases from use cases and/or requirements documents.
- Deploying builds in a dedicated testing environment.
- Logging/analyzing issues using a bug tracking software (JIRA).

Junior IM/IT Architecture Analyst

Jan 2015-Apr 2015

Transport Canada

- Assist with identifying appropriate IM/IT solutions to meet the requirements of the business lines.
- Researching and testing commercially available products to determine their functionality and ease of use.
- Consult with IM/IT Architecture team to determine the viability of options and recommendations.
- Testing and evaluating software and hardware to assess its suitability for use within Transport Canada.
- Assist with troubleshooting and resolving problems related to IM/IT architecture.