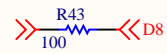
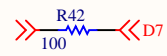
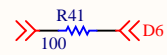
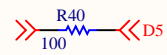


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AFR BAR		
Size	Number	Revision
B		B
Date:	7/17/2018	Sheet of
File:	C:\github\...\AFR_bar.SchDoc	Drawn By:



Title			AFR BAR TERMINATION RESISTORS		
Size		Number			Revision
A					B
Date:		7/17/2018			Sheet of
File:		C:\github\...\AFR_Bar_Termination_Resistors			Dr. Schilling

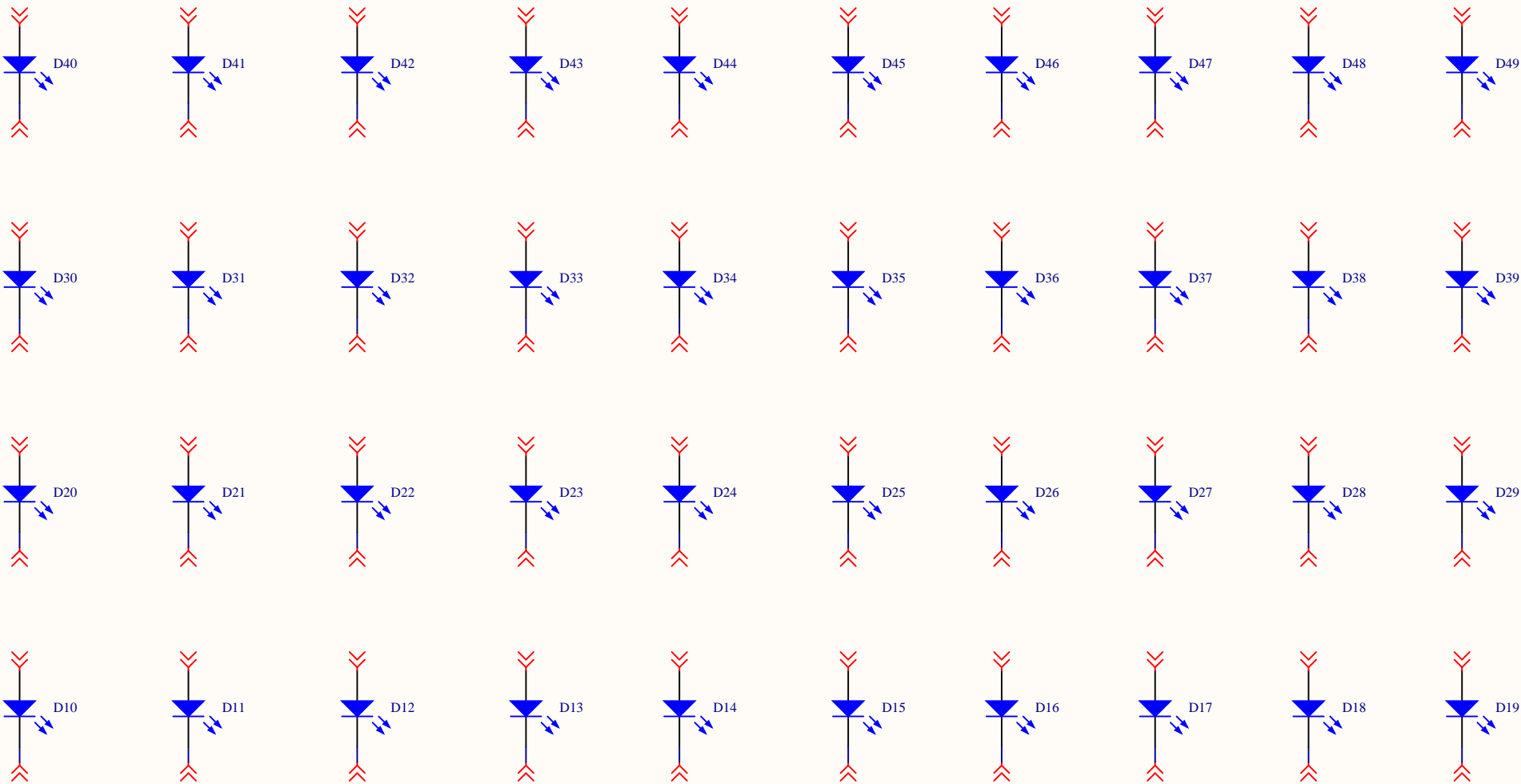
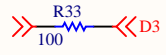
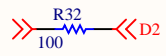
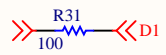
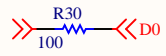
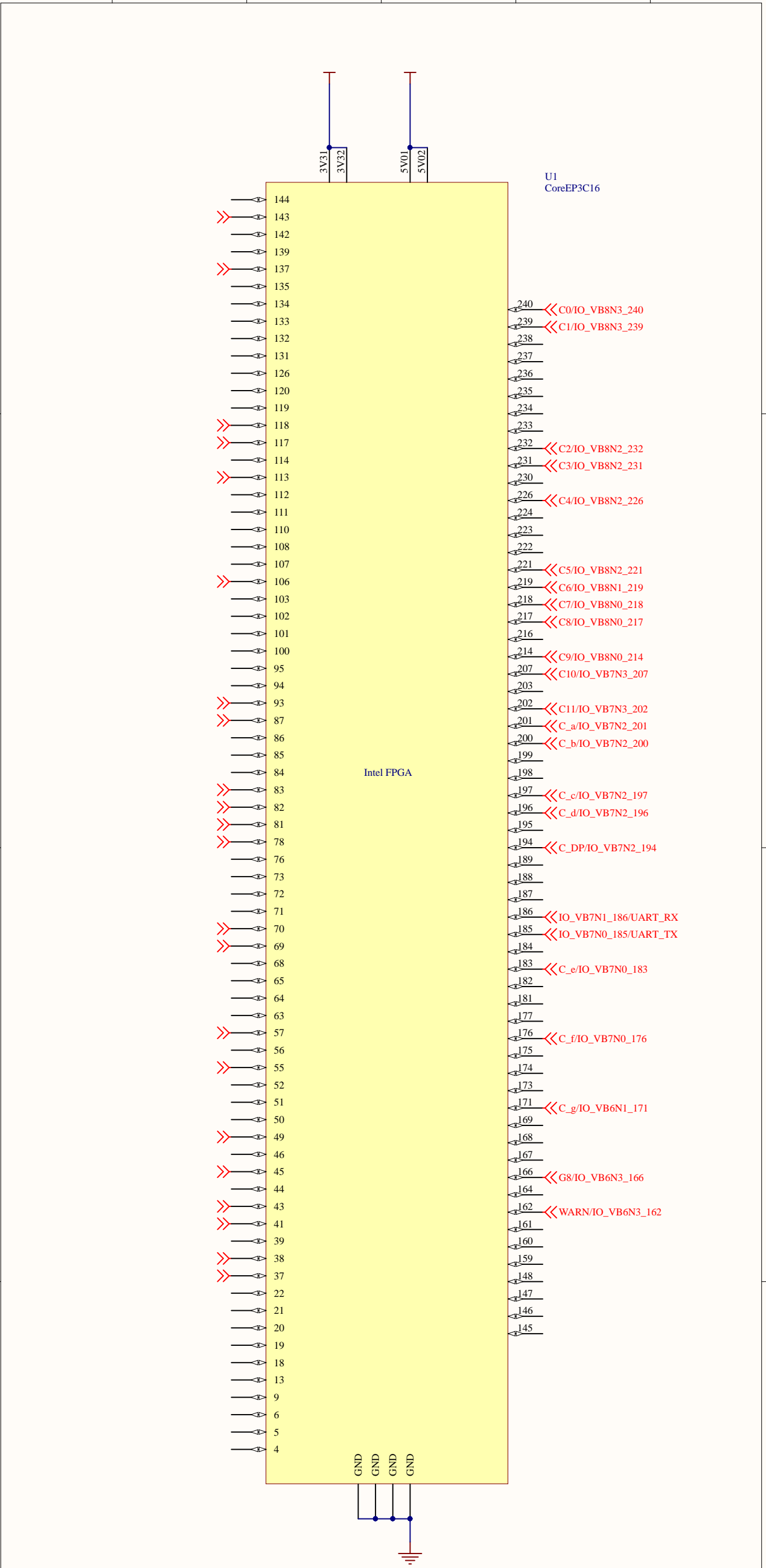


FIGURE OUT RESISTOR VALUES AFTER REVA

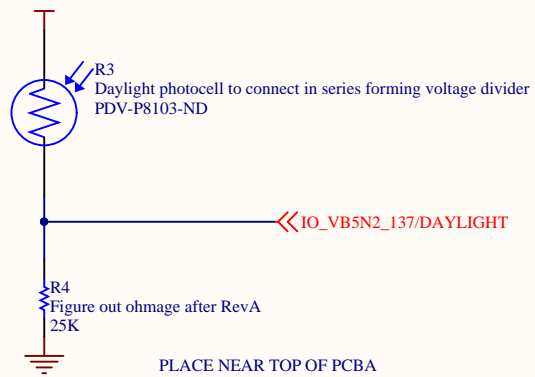
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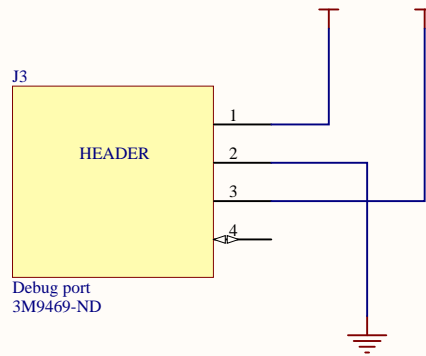
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A		B
Date:	7/17/2018	Sheet of
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Title				FPGA Core			
Size		Number			Revision		
A					A		
Date:		7/17/2018			Sheet of		
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Title			DAYLIGHT SENSOR NETWORK
Size	Number		Revision
A			A
Date:	7/17/2018	Sheet	of
File:	C:\github\...\Daylight_Sensor_Network.Sch	Drawn By:	



Pull out some references to make probing easier

Title			DEBUG PORT		
Size	Number			Revision	
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Date:	7/17/2018			Sheet	of
File:	C:\github\..\Debug.SchDoc			Drawn By:	

H0  
  
STANDOFF HOLE

Title			HOLE		
Size	Number			Revision	
A				A	
Date:	7/17/2018			Sheet	of
File:	C:\github\...\Hole1.SchDoc			Drawn By:	



H2  
  
STANDOFF HOLE

Title			HOLE		
Size	Number			Revision	
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Date:	7/17/2018			Sheet	of
File:	C:\github\...\Hole3.SchDoc			Drawn By:	

H3  
  
STANDOFF HOLE

Title			HOLE		
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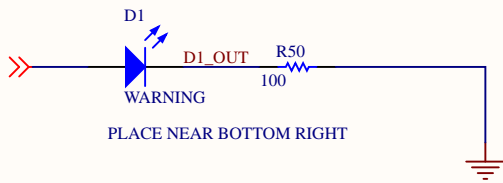
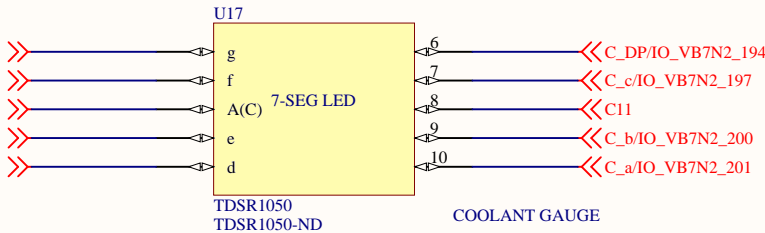
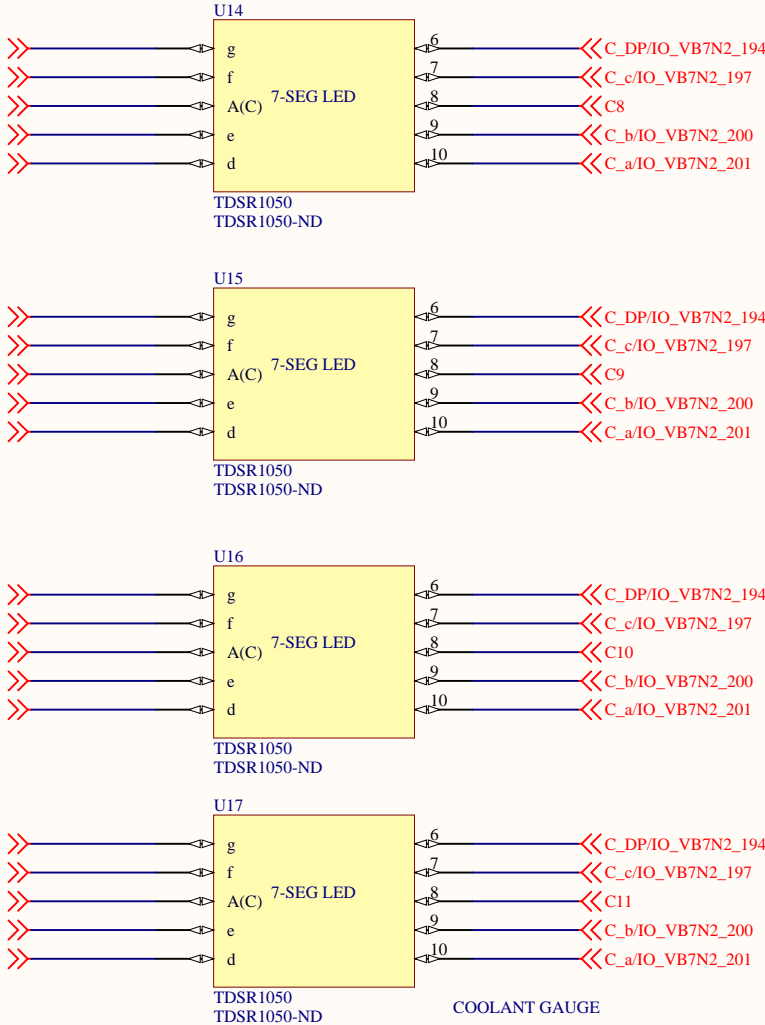
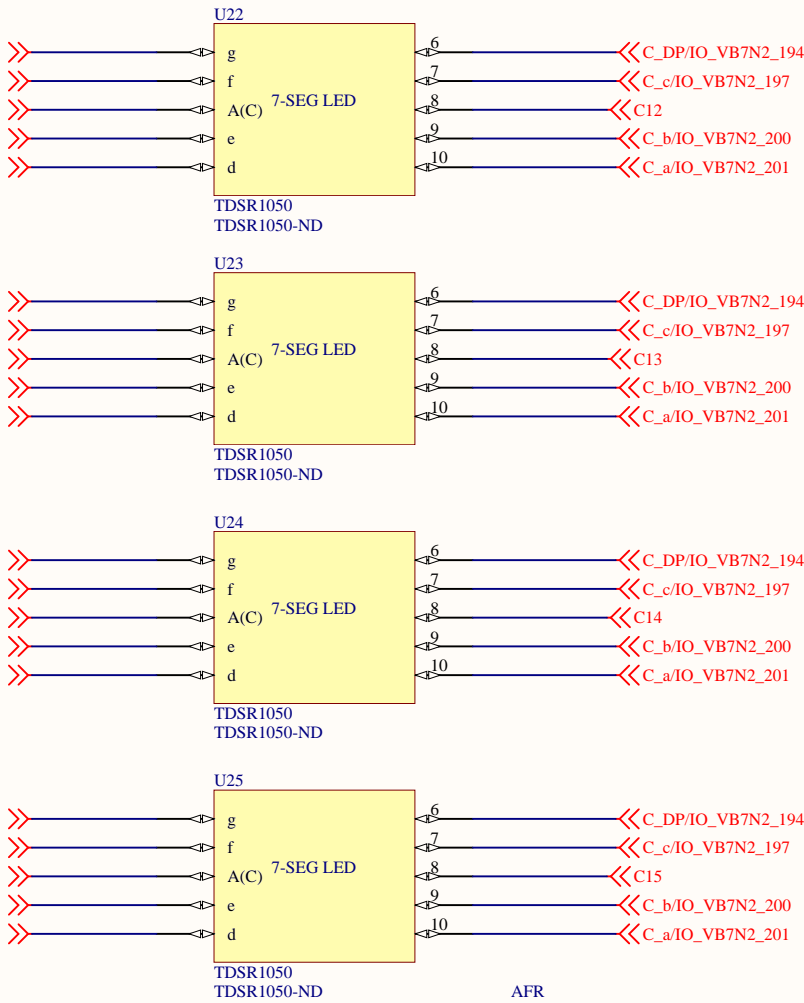
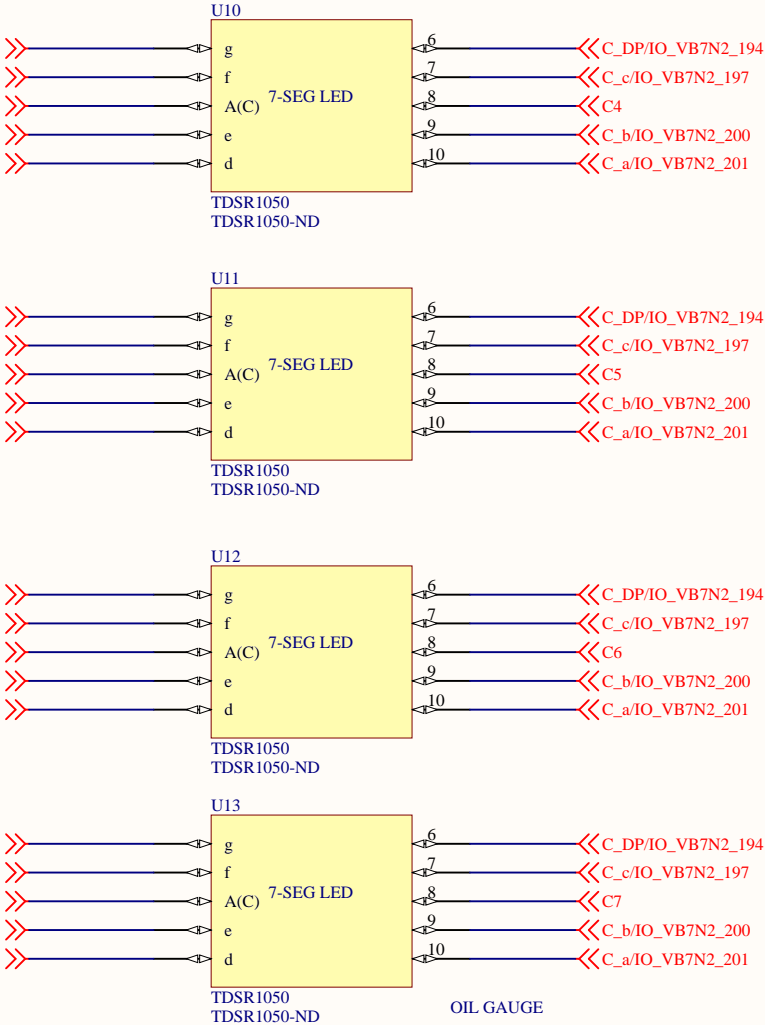
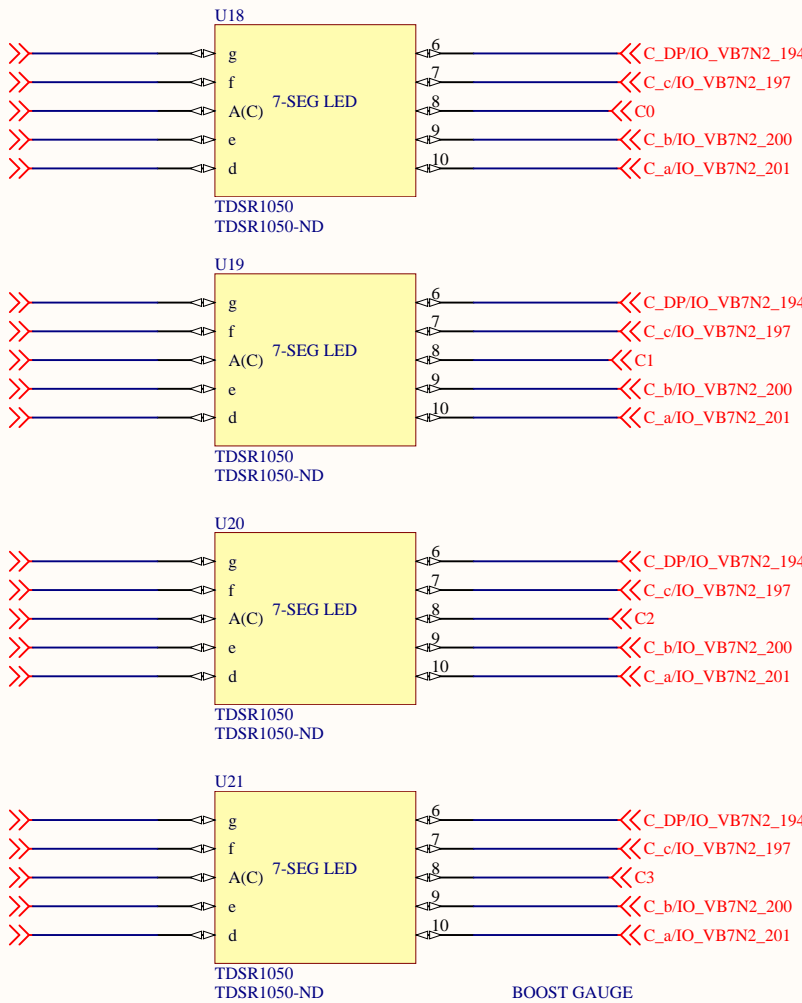
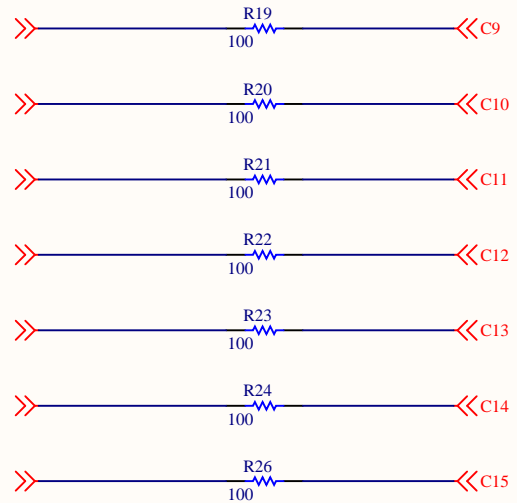
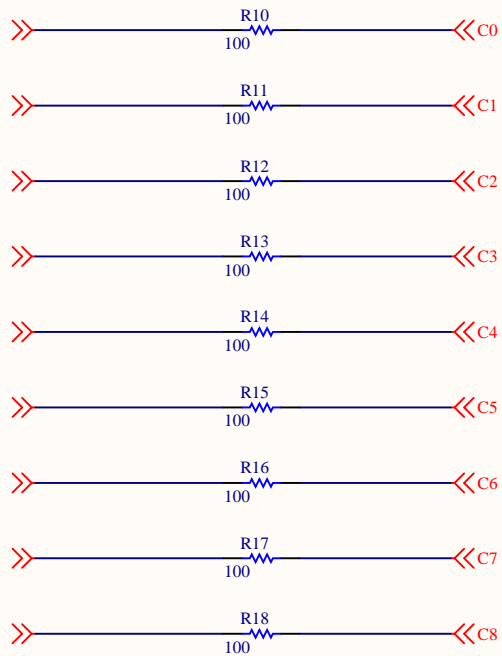
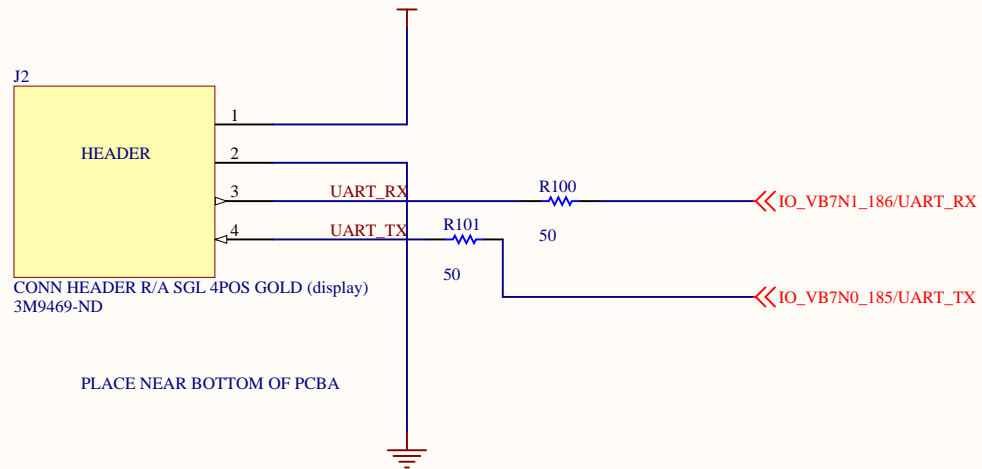


FIGURE OUT RESISTOR VALUES AFTER REVA





Title		
7 SEGMENT LED READOUT, 4 DIGITS, AND WARNING LIGHTS TERMINATION RESISTORS		
Size	Number	Revision
A		B
Date:	7/17/2018	Sheet of
File:	C:\github\LED_Readout_Termination_Resistors\BschDoc	Drawn By



Title		
UART		
Size	Number	Revision
A		B
Date:	7/17/2018	Sheet of
File:	C:\github\...\Uart.SchDoc	Drawn By:

