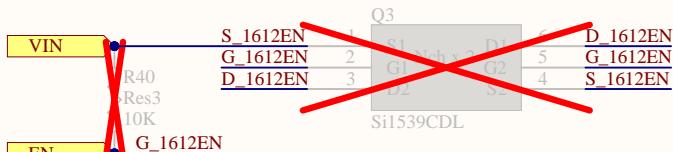
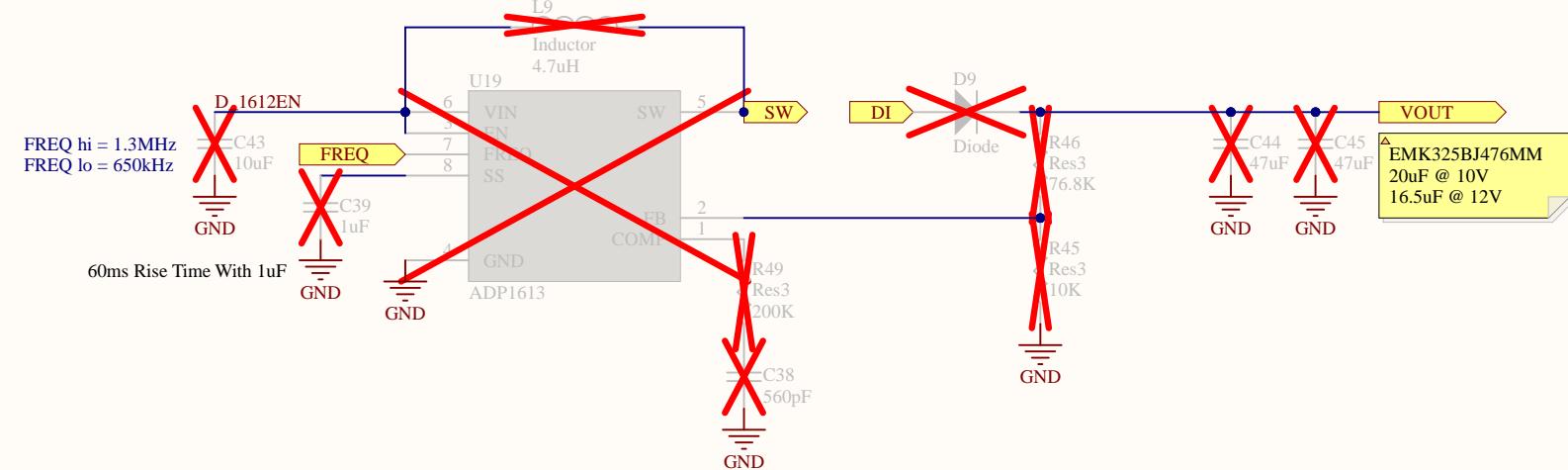


A



Boost Design: Short SW and DI.
SEPIC Design: Connect cap between SW and DI and inductor between DI and GND.



B

A

C

B

D

C

1

2

3

4

A

A

B

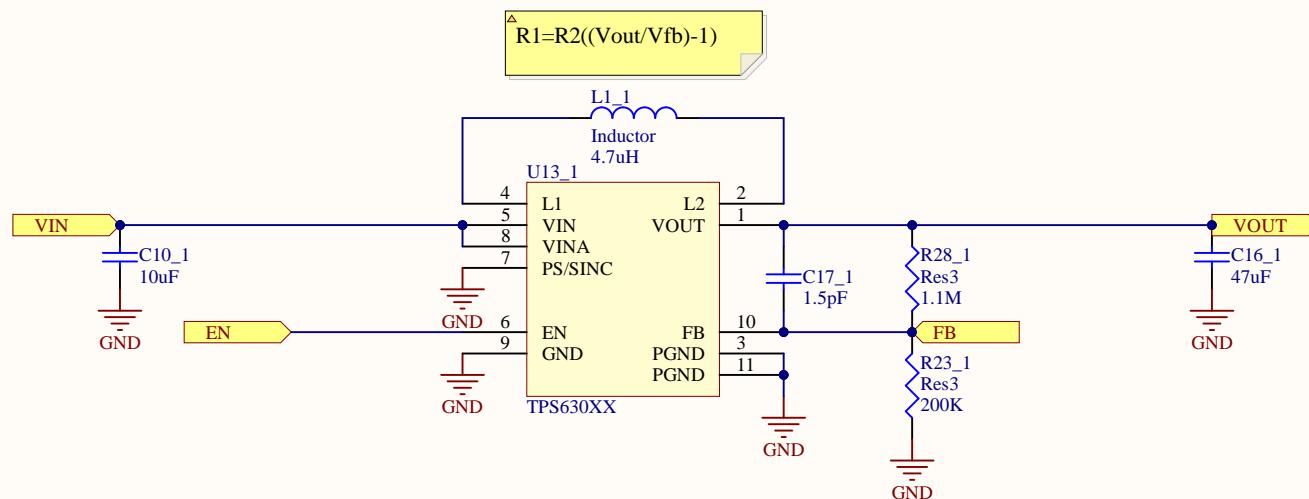
B

C

C

D

D



1

2

3

4

1

2

3

4

A

A

B

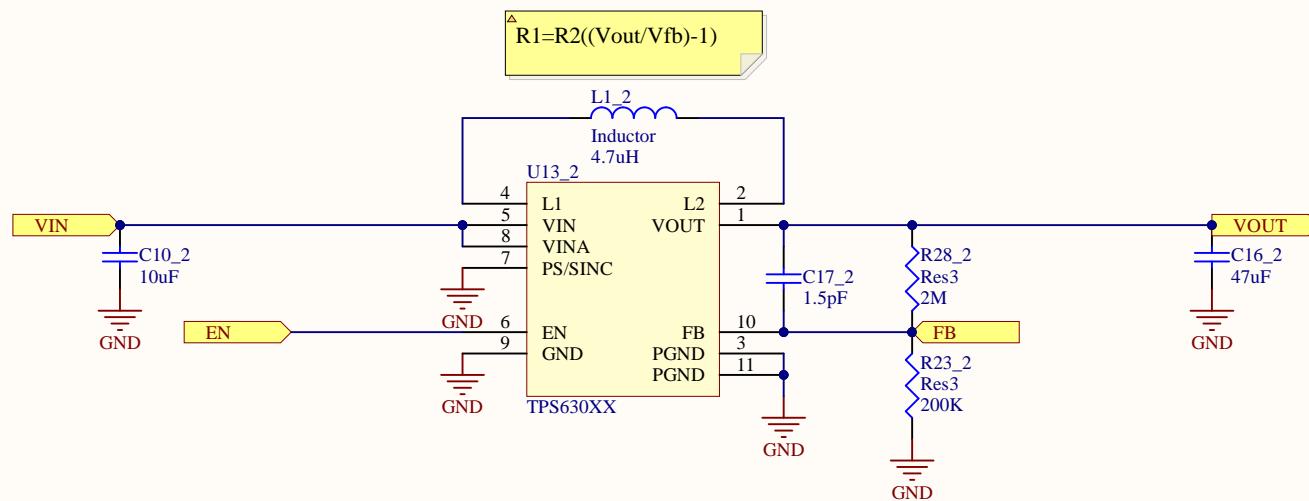
B

C

C

D

D



1

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1

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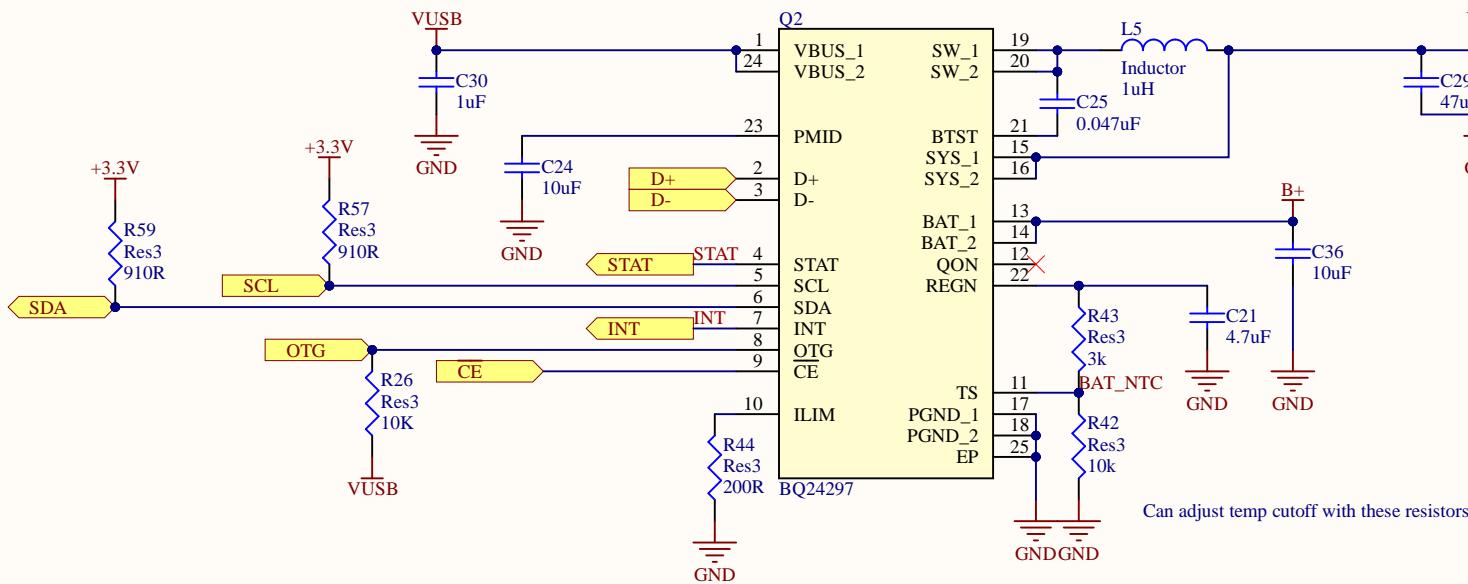
A

A



B

B



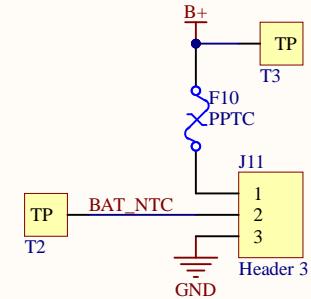
C

C



D

D



1

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1

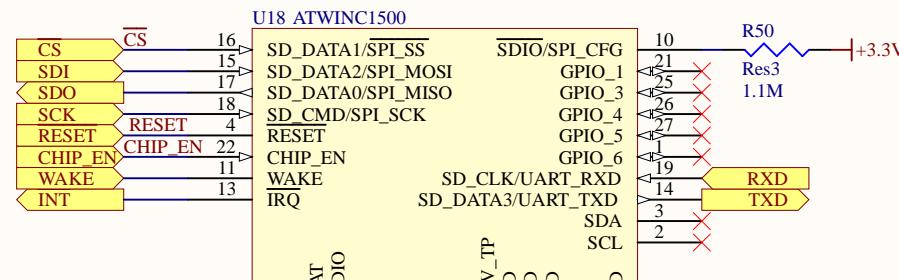
2

3

4

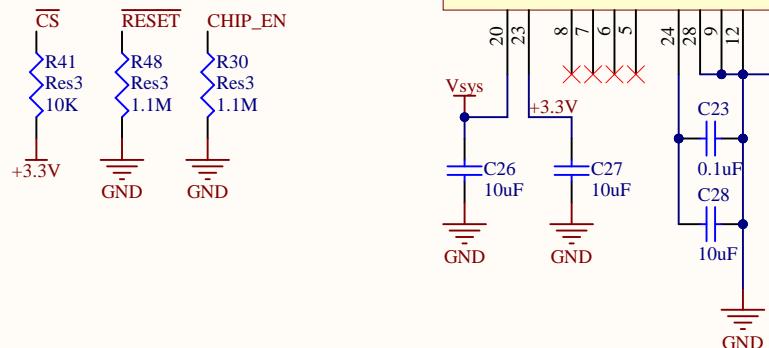
A

A



B

B



C

C

D

D

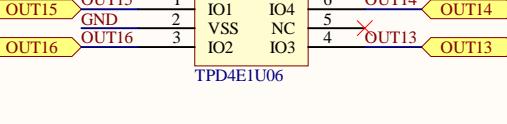
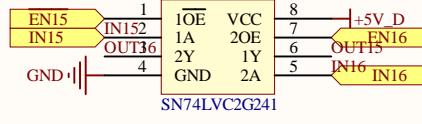
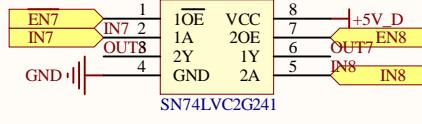
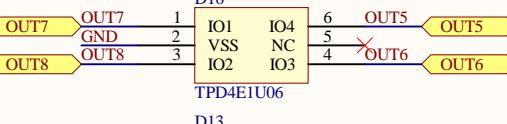
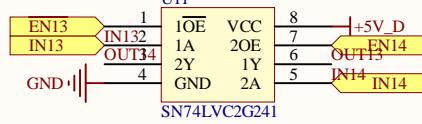
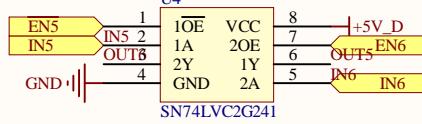
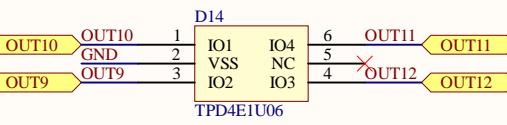
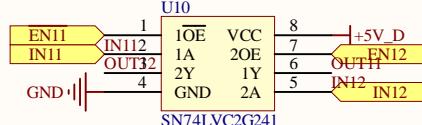
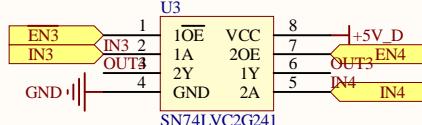
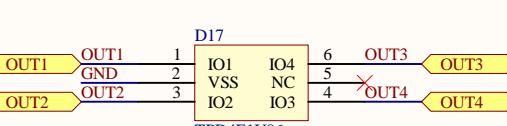
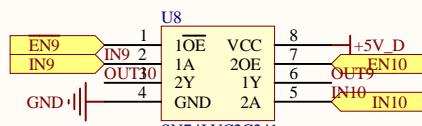
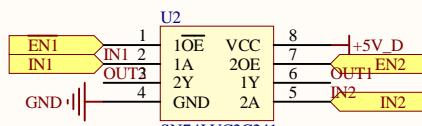
1

2

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A



B

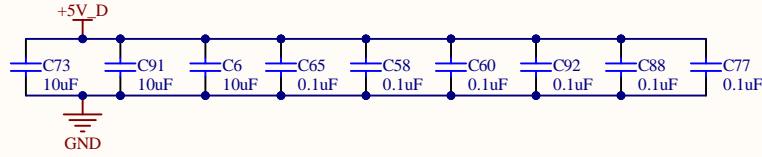
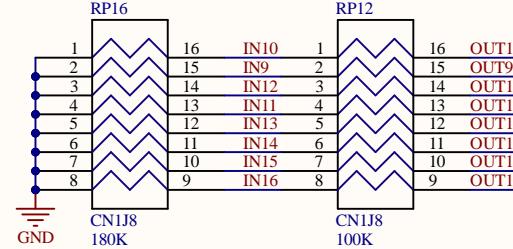
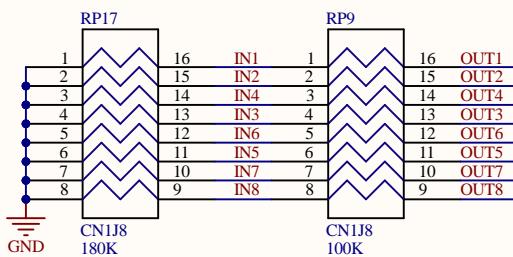
A

B

B

C

C



D

D

A

B

C

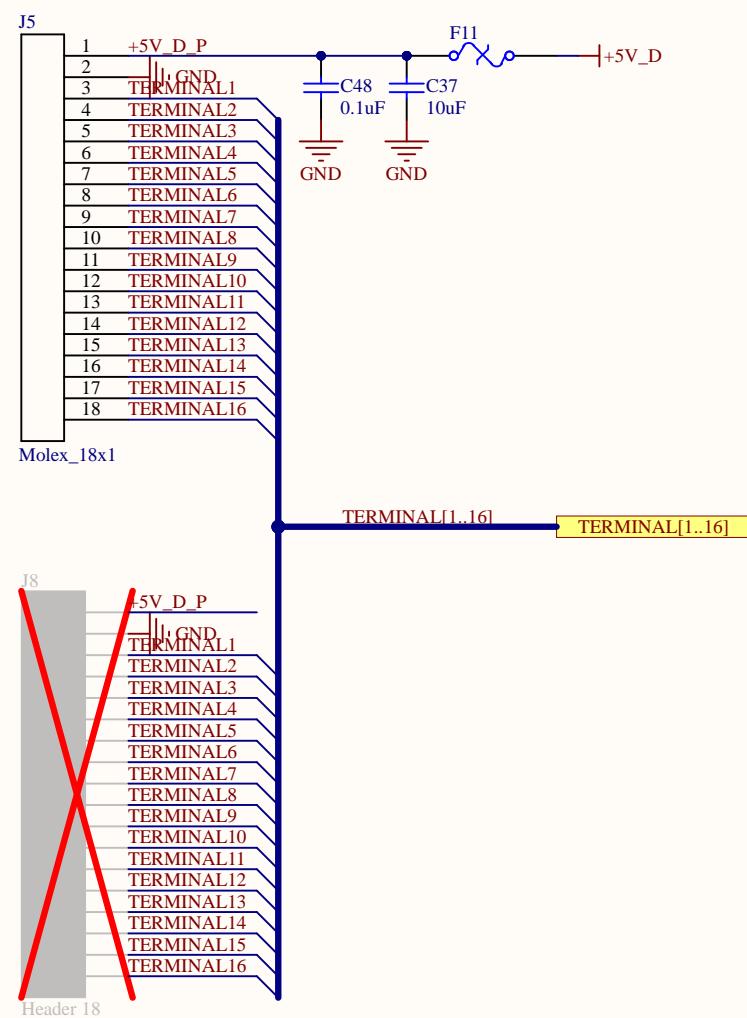
D

A

B

C

D



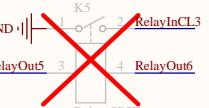
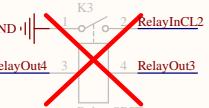
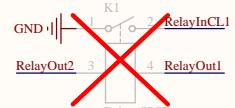
1

2

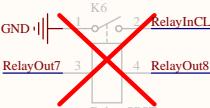
3

4

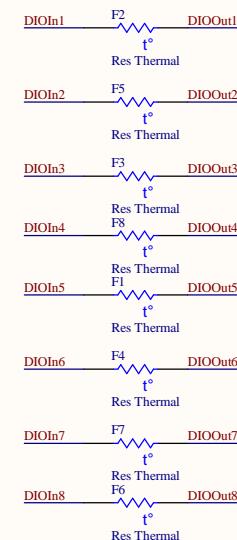
A



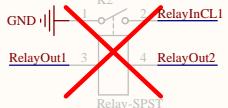
[▲] Optional mechanical/SSR for all Nq models.
Relays unpopulated by default.



Always populated



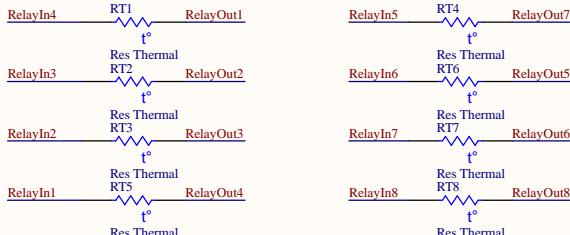
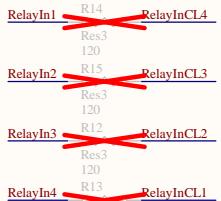
B



[▲] SSR can be CPC1706 or CPC1926

C

120R (603-RC0603FR-07120RL) for SSR, OR for Mech, none for DIO



[▲] Could change these resistors to 200ohm or even 910 ohm to reduce current and reduce BOM count.

1

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D

A

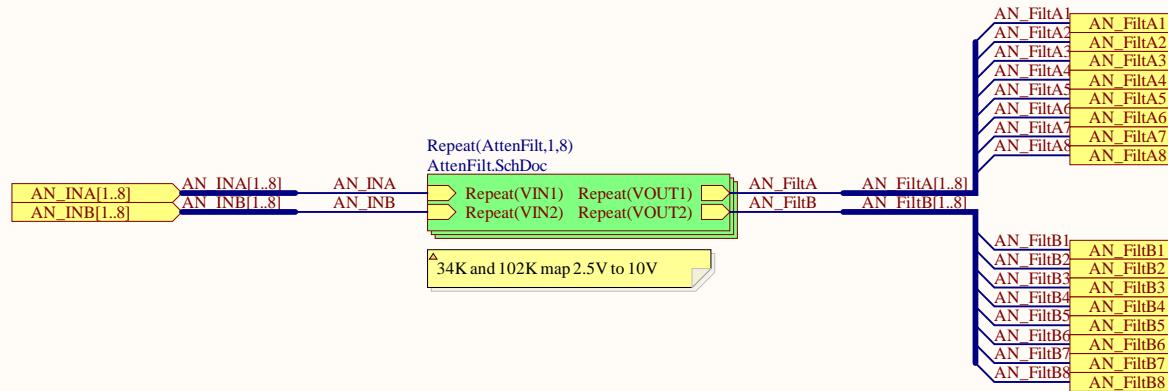
B

C

D

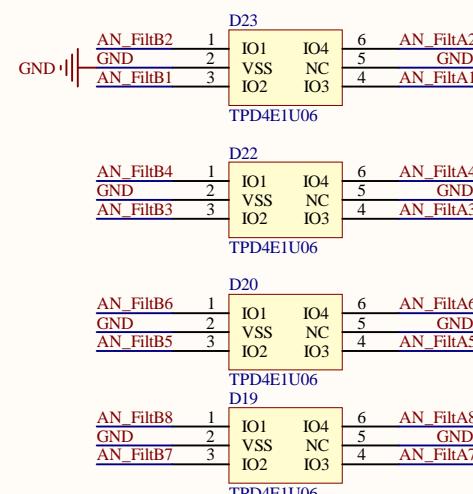
A

A



B

B



C

C

D

D

1

2

3

4

A

A

B

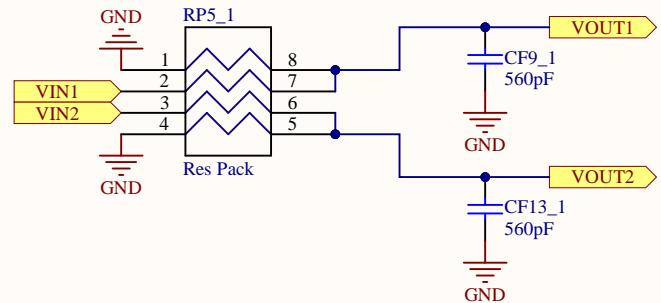
B

C

C

D

D



1

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4

1

2

3

4

A

A

B

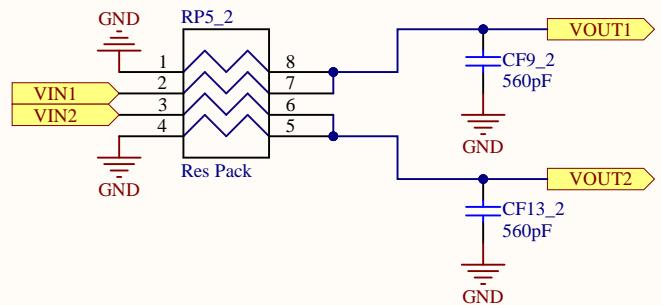
B

C

C

D

D



1

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1

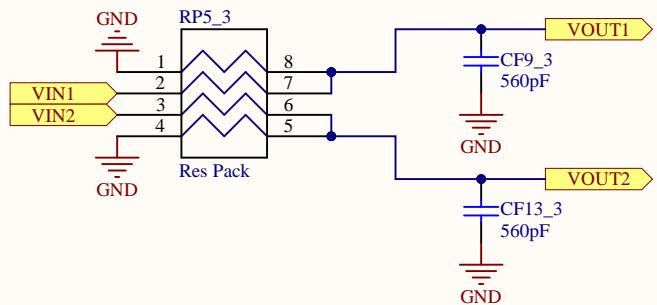
2

3

4

A

A



B

B

C

C

D

D

1

2

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1

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4

A

A

B

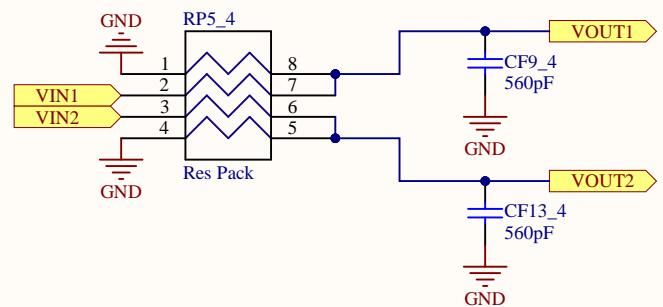
B

C

C

D

D



1

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1

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4

A

A

B

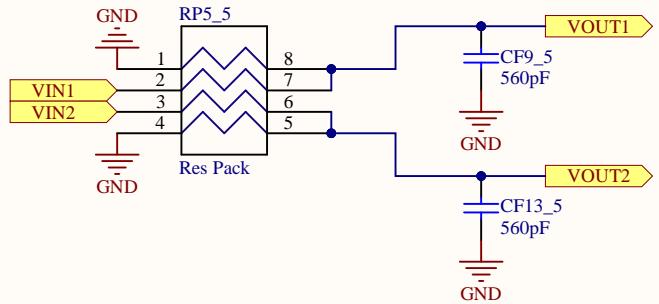
B

C

C

D

D



1

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1

2

3

4

A

A

B

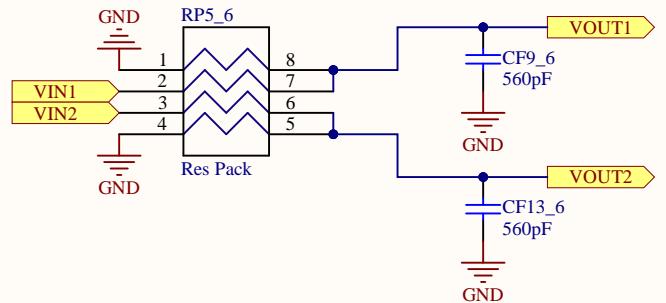
B

C

C

D

D



1

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1

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4

A

A

B

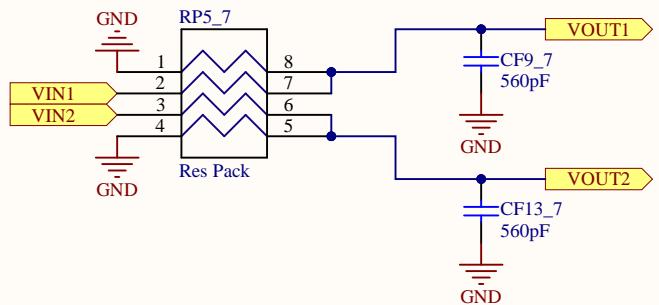
B

C

C

D

D



1

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1

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4

A

A

B

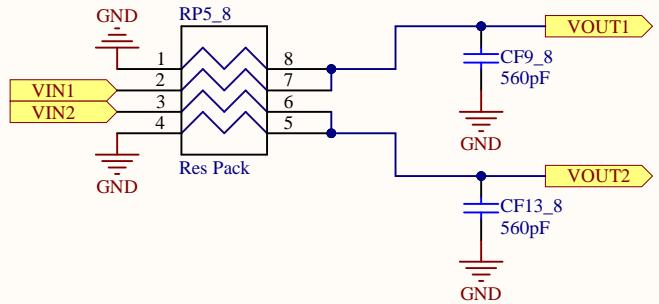
B

C

C

D

D



1

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A

A

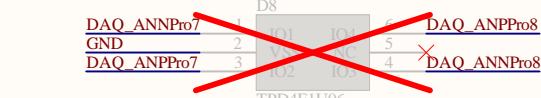
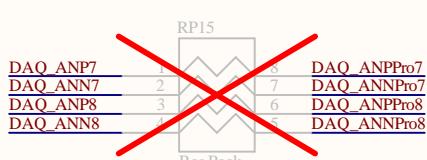
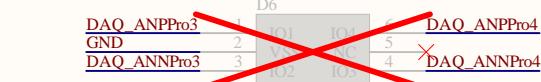
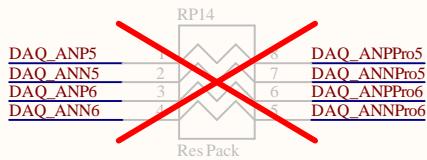
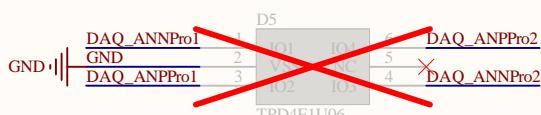
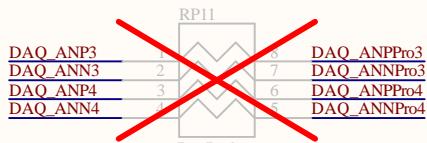
DAQ_ANP[1..8] **DAO_ANP[1..8]**



DAQ_ANPPro[1..8] **DAQ_ANPPro[1..8]**

DAQ_ANNPro[1..8] **DAQ_ANNPro[1..8]**

DAQ_ANN[1..8] **DAO_ANN[1..8]**



C

C

A

A

B

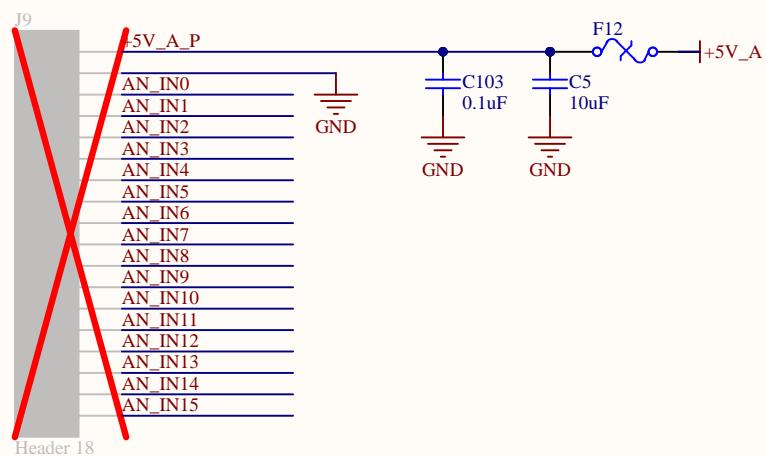
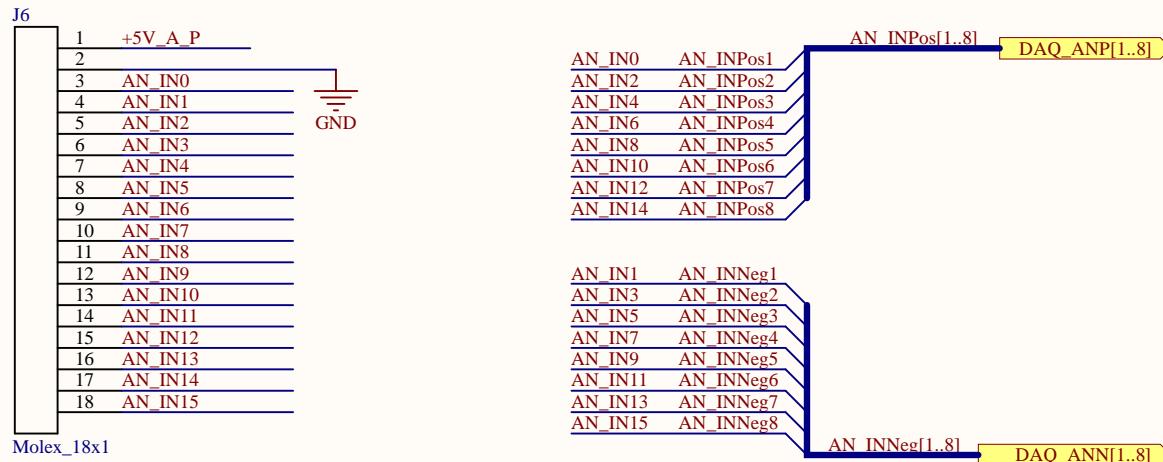
B

C

C

D

D



A

A

B

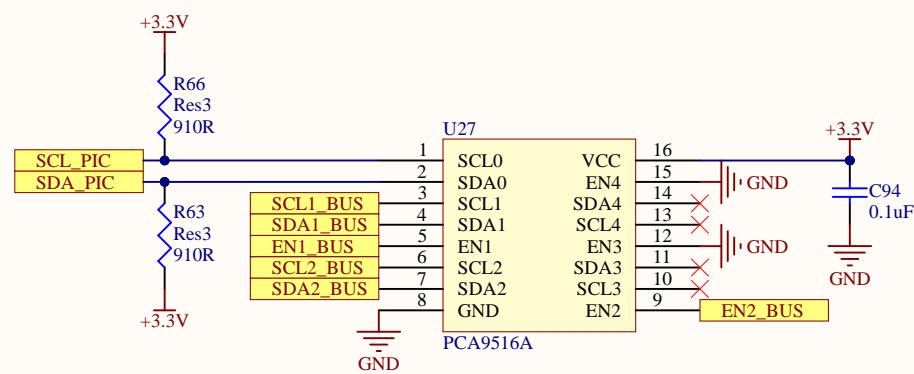
B

C

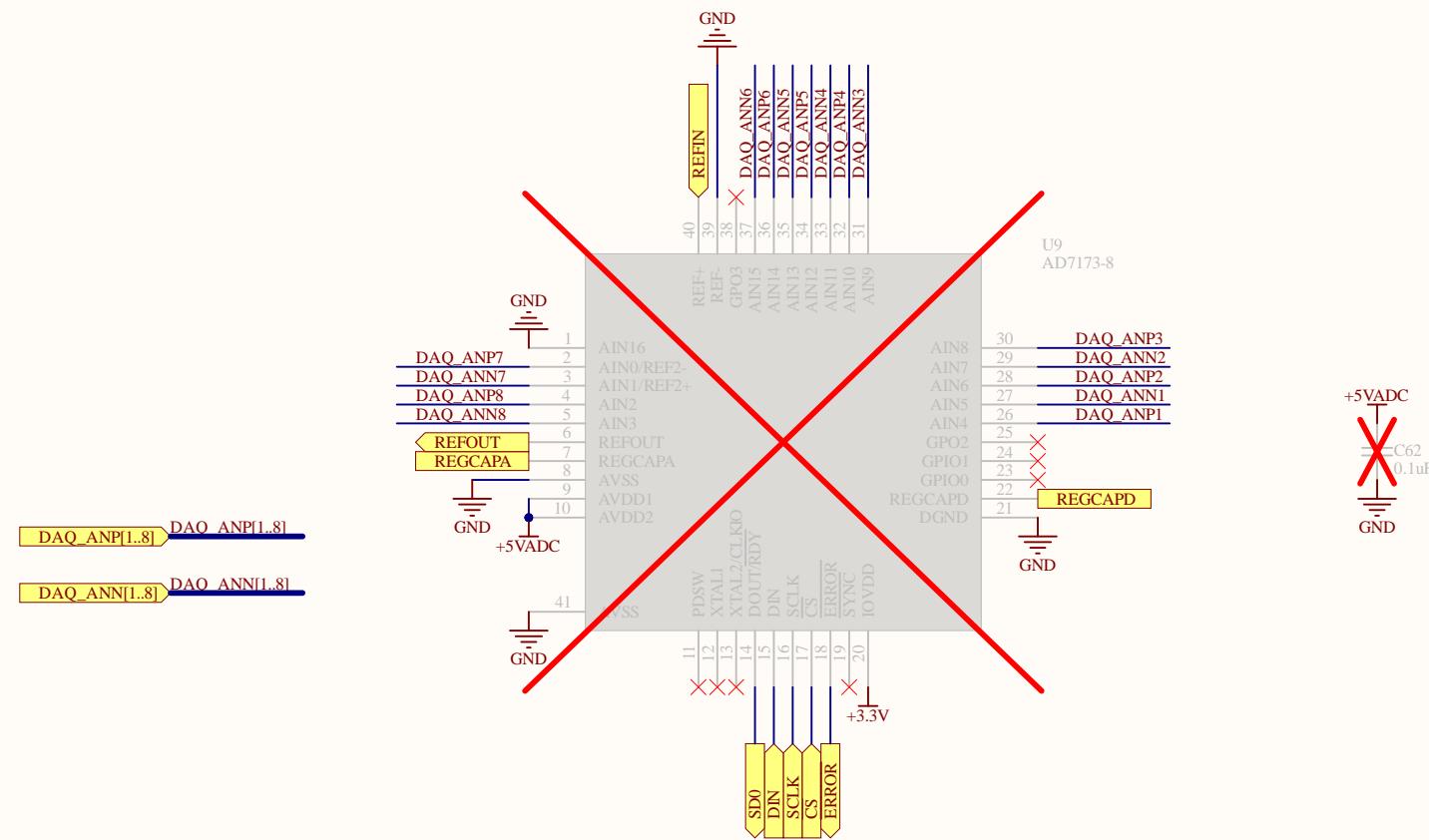
C

D

D



A



B

DAQ_ANP[1..8] DAO_ANP[1..8]
 DAQ_ANN[1..8] DAO_ANN[1..8]

Only populated on Nq2.

C

A

B

C

D

1

2

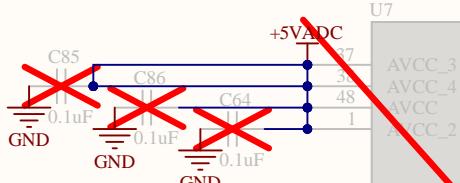
3

4

A

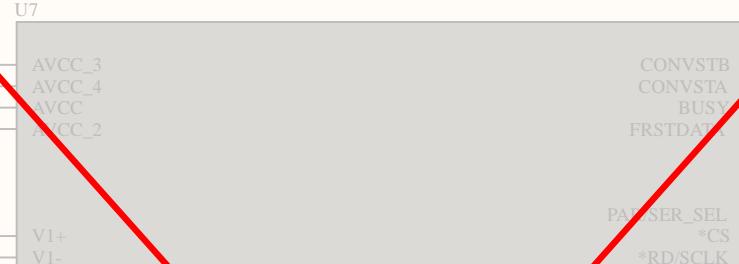
A

Only populated on Nq3.



DAQ_ANP1 49
DAQ_ANN1 50
DAQ_ANP2 51
DAQ_ANN2 52
DAQ_ANP3 53
DAQ_ANN3 54
DAQ_ANP4 55
DAQ_ANN4 56
DAQ_ANP5 57
DAQ_ANN5 58
DAQ_ANP6 59
DAQ_ANN6 60
DAQ_ANP7 61
DAQ_ANN7 62
DAQ_ANP8 63
DAQ_ANN8 64

REFIO 42
REF_SELECT 34
REFCAPA 44
REFCAPB 45
C83 10uF to GND
AD7609
REFGND 46
REFGND_2 43
REGCAP_2 36
REGCAP_1 39

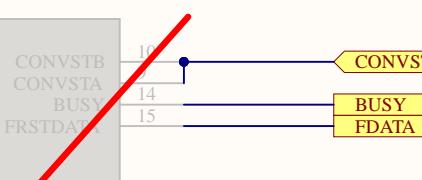


DAQ_ANP[1..8]

DAQ_ANN[1..8]

C

C



DB15, DB13, DB12, DB11, DB10, DB14, DB9, DB8/DOUTB, DB7/DOUTA, DB6, DB5, DB4, DB3, DB2, DB1, DB0, SD0

RESET, *STBY, RANGE

OS_2, OS_1, OS_0

VD/A/V

+3.3V
C63 10uF to GND

+3.3V
OS_1, OS_0

+3.3V
C70 0.1uF to GND

D

D

1

2

3

4

1

2

3

4

A

A

B

B

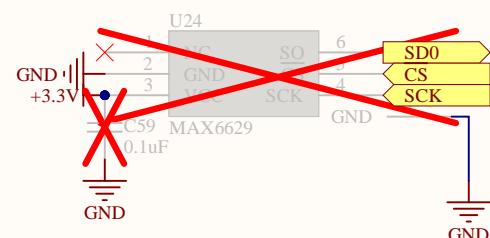
C

C

D

D

Only populated on Nq2/3 for cold junction compensation.



1

2

3

4

1

2

3

4

A

A

B

B

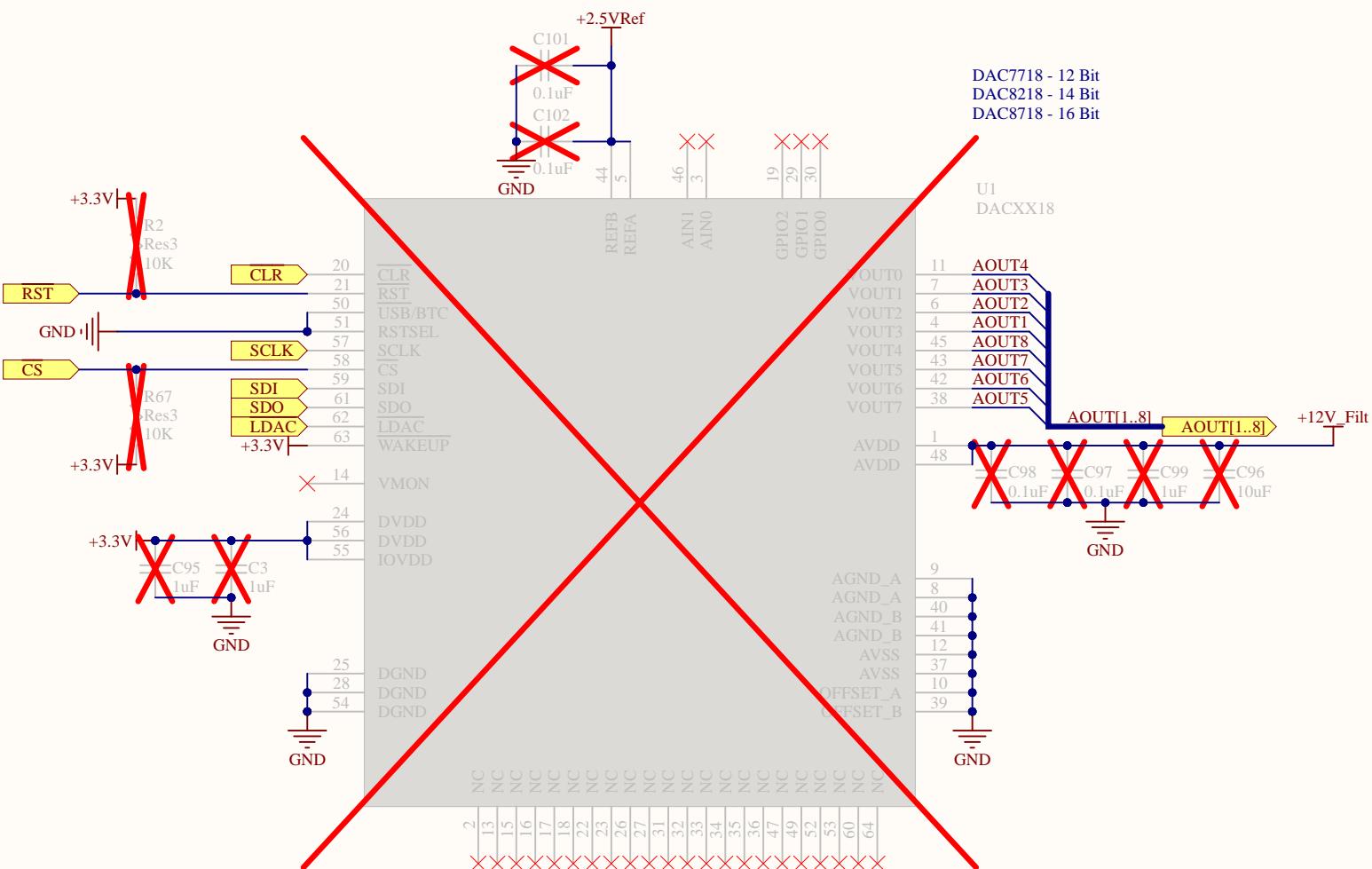
C

C

D

D

Only populated on Nq2/3.



1

2

3

4

