Operating Systems (OS) CPU Scheduling

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Chapter 5: CPU Scheduling

- Basic Concepts
- Scheduling Criteria
- Scheduling Algorithms
- Thread Scheduling
- Multiple-Processor Scheduling
- Real-Time CPU Scheduling
- Operating Systems Examples
- Algorithm Evaluation





Objectives

- To introduce CPU scheduling, which is the basis for multiprogrammed operating systems
- To describe various CPU-scheduling algorithms
- To discuss evaluation criteria for selecting a CPU-scheduling algorithm for a particular system
- To examine the scheduling algorithms of several operating systems

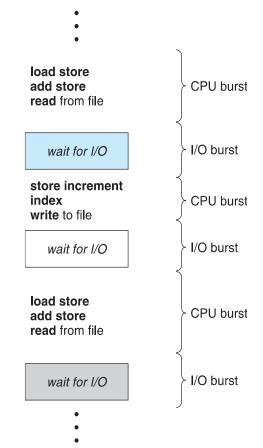
1차 Term Project와 관련





Basic Concepts

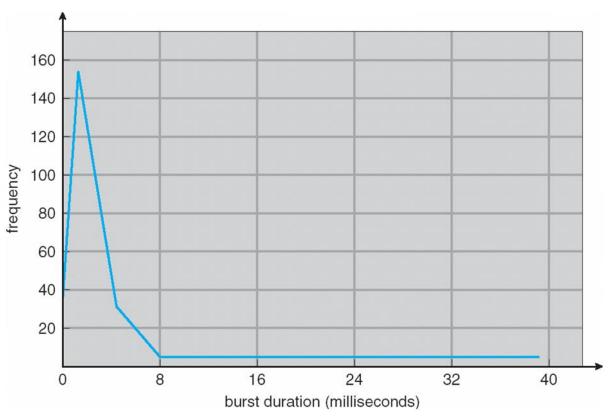
- Maximum CPU utilization obtained with multiprogramming
- CPU-I/O Burst Cycle Process execution consists of a cycle of CPU execution and I/O wait
- <u>CPU burst</u> followed by <u>I/O burst</u>
 - Scheduling의 단위,
 연속적으로 CPU 사용 구간
- CPU burst distribution is of main concern
- 단일처리기(Single CPU Core)
 System v.s. Multiple CPU Cores
 - I/O동안 Idle 낭비



- 버스트(burst)란 특정 기준에 따라 한 단위로서 취급되는 연속된 신호나 데이터의 모임을 말한다. 즉, 입출력 요청을 위해 CPU 사용을 사용했다가 쉬었다가를 반복한다.
- 프로세스가 CPU를 사용할때를 **CPU버스트**, 입/출력을 기다릴때를 **입/출력 버스트**라고 한다.
- 프로세스의 실행은 CPU 버스트를 시작으로 뒤이어 입출력 버스트가 발생하는 식으로 두 버스트의 사이클로 구성된다. 마지막 CPU버스트는 또 다른 입출력 버스트가 뒤따르는 대신에 실행을 종료하기 위한 시스템 요청과 함께 끝난다.



Histogram of CPU-burst Times



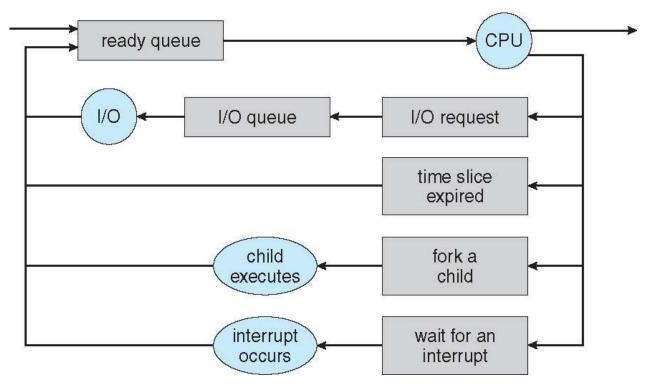
- 짧은 CPU Burst v.s. 긴 CPU Burst?
- I/O 중심 프로그램은 전형적 짧은 CPU Burst
- CPU 중심 프로그램은 다수의 긴 CPU Burst
- 좌측 그림을 통해 적절한 CPU Burst 알고리즘 선택 가능





Process Scheduler Example (이전 슬라이드 그림)

- Ready Queue에서 나온 프로세스가 CPU 디스패치 됨을 이해
- Ready Queue는 반드시 FIFO가 아닐 수 있음
 - 따라서, FIFO, Priority, Tree 등으로 Queue 구현 가능
 - Queue에 들어가는 레코드 들은 PCB임.







CPU Scheduler

- Short-term scheduler selects a process from among the processes in ready queue, and allocates the CPU to one of them
 - Queue may be ordered in various ways
- CPU scheduling decisions may take place when a process:
 - 1. Switches from running to waiting state
 - 2. Switches from running to ready state
 - 3. Switches from waiting to ready
 - 4. Terminates
- Scheduling under 1 and 4 is nonpreemptive
- All other scheduling is preemptive
 - Consider access to shared data
 - Consider preemption while in kernel mode
 - Consider interrupts occurring during crucial OS activities

과거에는 비선점형, 지금은 선점형 (Preemptive) - Ex) Windows 95 이후, OSX

1, 4번은 선택여지 없이 스케쥴링 처리 2, 3은 선택이 가능 -> Preemptive 이슈

Preemptive(선점형):
 하나의 Process가
 다른 Process대신
 CPU차지 가능





Preemptive Scheduler 문제

- 문제점은?
 - 공유자료를 접근하는 경우 비용 발생
 - ▶ 한 프로세스가 자료를 갱신하고 있는 동안 선점되어 두번째 프로세스가 실행 가능하게 된 경우?
 - ▶ 자료를 동시에 접근할 때 발생할 수 있는 문제 -> Deadlock issue
 - ▶ UNIX의 경우, Context switching (문맥교환)을 실행하기 전에 시스템 호출의 완료 혹은 입출력 block을 기다림
 - ▶ 즉, 커널 자료구조가 비일관적인 상태에 있는 있는 동안에는 해당 프로세스를 선점하지 않도록.
 - 문제점은? 실시간 컴퓨팅 및 다중처리





Dispatcher

- Scheduler내의 CPU Dispatcher
- Dispatcher module gives control of the CPU to the process selected by the short-term scheduler; this involves:
 - switching context
 - switching to user mode
 - jumping to the proper location in the user program to restart that program
- Dispatch latency time it takes for the dispatcher to stop one process and start another running
 - 하나의 프로세스를 중단시키고 다른 프로세스를 실행시키는 데까지 소요되는 시간





Scheduling Criteria - 성능 평가 Metric으로 이해

- CPU utilization keep the CPU as busy as possible
- **Throughput** # of processes that complete their execution per time unit
- **Turnaround time** amount of time to execute a particular process
 - 특정한 프로세스의 입장에서 보면, 중요한 기준은 그 프로세스를 실행하는데 소요된 시간: 프로세스를 제출 후 완료까지의 시간
- Waiting time amount of time a process has been waiting in the ready queue
 - 스케쥴링 알고리즘은 오직 프로세스가 준비완료 큐에서 대기하는 시간의 양에만 영향 -> 대기시간은 준비완료 큐에서 대기하면서 보낸 시간
- Response time amount of time it takes from when a request was submitted until the first response is produced, not output (for timesharing environment)
 - 대화식 시스템(Interactive System)에서는 총처리시간 보다 요구를 제출한 후 첫번째 응답까지의 시간이 중요.
 - 평균응답 시간을 최소화 하기 보다는 응답 시간의 변동폭(Variance)을 최소화 하는 것이 중요.





Scheduling Algorithm Optimization Criteria

- Max CPU utilization
- Max throughput
- Min turnaround time
- Min waiting time
- Min response time





First-Come, First-Served (FCFS) Scheduling

<u>Process</u>	Burst Time
P_1	24
P_2	3
P_3	3

• Suppose that the processes arrive in the order: P_1 , P_2 , P_3 The Gantt Chart for the schedule is:



- Waiting time for $P_1 = 0$; $P_2 = 24$; $P_3 = 27$
- Average waiting time: (0 + 24 + 27)/3 = 17

문제점은? - 평균대기시간 Issue



FCFS Scheduling (Cont.)

Suppose that the processes arrive in the order:

$$P_2$$
, P_3 , P_1

The Gantt chart for the schedule is:

이 경우가 문제점 예



- Waiting time for $P_1 = 6$; $P_2 = 0$; $P_3 = 3$
- Average waiting time: (6 + 0 + 3)/3 = 3
- Much better than previous case

중심) 프로세스와 많은 수의 I/O 프로세스가 만드는 문제점 고민 (생각해보기)

하나의 CPU-bound(CPU

- Convoy effect short process behind long process
 - Consider one CPU-bound and many I/O-bound processes



Shortest-Job-First (SJF) Scheduling

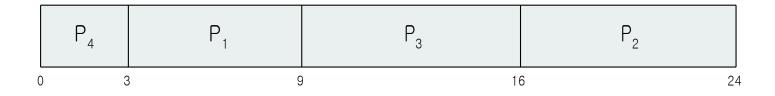
- Associate with each process the length of its next CPU burst
 - Use these lengths to schedule the process with the shortest time
- SJF is optimal gives minimum average waiting time for a given set of processes
 - The difficulty is knowing the length of the next CPU request
 - Could ask the user



Example of SJF

<u>Process</u>	Burst Time
P_1	6
P_2	8
P_3	7
P_4	3

SJF scheduling chart



• Average waiting time = (3 + 16 + 9 + 0) / 4 = 7

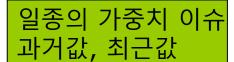




Determining Length of Next CPU Burst

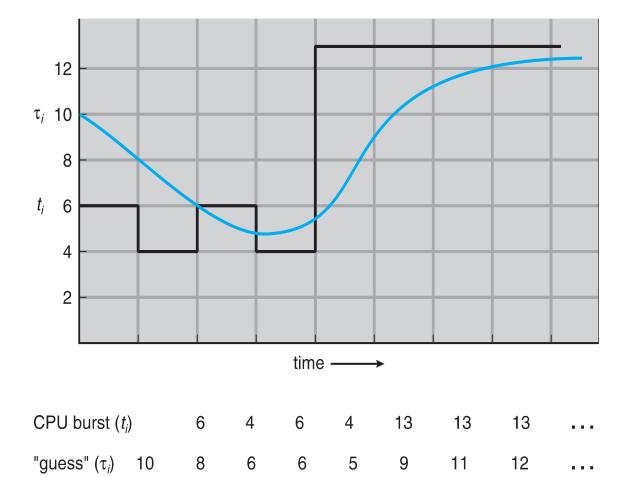
다음 CPU Burst 길이 추정기법

- Can only estimate the length should be similar to the previous one
 - Then pick process with shortest predicted next CPU burst
- Can be done by using the length of previous CPU bursts, using exponential averaging
 - 1. $t_n = \text{actual length of } n^{th} \text{ CPU burst}$
 - 2. τ_{n+1} = predicted value for the next CPU burst
 - 3. α , $0 \le \alpha \le 1$
 - 4. Define: $\tau_{n=1} = \alpha t_n + (1-\alpha)\tau_n$.
- Commonly, α set to ½
- Preemptive version called shortest-remaining-time-first





Prediction of the Length of the Next CPU Burst







Examples of Exponential Averaging



- \bullet $\alpha = 0$
 - $\tau_{n+1} = \tau_n$
 - Recent history does not count



- $\alpha = 1$
 - $\tau_{n+1} = \alpha t_n$
 - Only the actual last CPU burst counts
- If we expand the formula, we get:

$$\tau_{n+1} = \alpha t_n + (1 - \alpha)\alpha t_{n-1} + \dots + (1 - \alpha)^{j} \alpha t_{n-j} + \dots + (1 - \alpha)^{n+1} \tau_0$$

• Since both α and $(1 - \alpha)$ are less than or equal to 1, each successive term has less weight than its predecessor



Example of Shortest-remaining-time-first

 Now we add the concepts of varying arrival times and preemption to the analysis

		<u>Pr</u>	ocess Ai	<u>rrival T</u>	<u>ime</u>	Burst Ti	<u>me</u>	
		F)	0		8	SJF는 선점	
		F	2	1		4	비선점형일 이전의 프로	수 있음.
		F	3	2		9	이선의 프도 실행되는 동	
		F	4	3		5	프로세스가	준비완료
•	 Preemptive SJF Gantt Chart 						큐에 도착 시 선택상황 발	• /
	P ₁ F	2	₽ Р ₄		P ₁		P ₃	

17

Average waiting time = [(10-1)+(1-1)+(17-2)+5-3)]/4 = 26/4 = 6.5
 msec

10





26

Priority Scheduling

- A priority number (integer) is associated with each process
- The CPU is allocated to the process with the highest priority (smallest integer = highest priority)
 - Preemptive
 - Nonpreemptive
- SJF is priority scheduling where priority is the inverse of predicted next CPU burst time
- Problem = Starvation low priority processes may never execute
- Solution = Aging as time progresses increase the priority of the process

1973년에 MIT에서 IBM7094를 폐쇄했을 때, 1967년에 입력된 낮은 우선순위의 프로세스가 아직도 실행되지 못하고 있는 것을 발견.:)

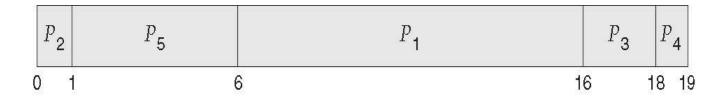




Example of Priority Scheduling

<u>Process</u>	Burst Time	<u>Priority</u>
P_1	10	3
P_2	1	1
P_3	2	4
P_4	1	5
P_5	5	2

Priority scheduling Gantt Chart



Average waiting time = 8.2 msec





Round Robin (RR)

- Each process gets a <u>small unit of CPU time (time quantum q)</u>, usually 10-100 milliseconds. After this time has elapsed, the process is preempted and added to the end of the ready queue.
- If there are n processes in the ready queue and the time quantum is q, then each process gets 1/n of the CPU time in chunks of at most q time units at once. No process waits more than (n-1)q time units.
- Timer interrupts every quantum to schedule next process
- Performance
 - q large ⇒ FIFO
 - q small ⇒ q must be large with respect to context switch, otherwise overhead is too high

문제저: 1) 프로세스의 CPU Burst가 Time quantum보다 작을 경우

- -> CPU가 자발적 Dispatch 후 다음 프로세스 진행
- 2) 현재 CPU Burst가 Time quantu보다 긴 경우
- -> 타이머 완료 후 Interrupt -> Context Switching : 평균대기시간 길어질 여지



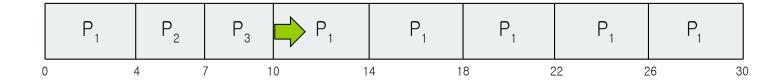


Example of RR with Time Quantum = 4

Burst Time
24
3
3

특이점? RR은 Time Quantum 크기에 큰 영향을 받음.

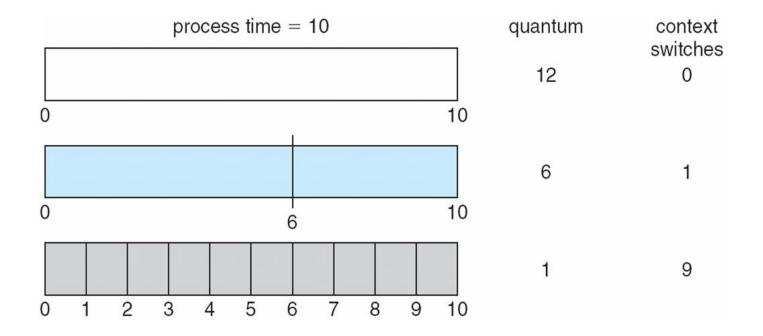
The Gantt chart is:



- Typically, higher average turnaround than SJF, but better response
- q should be large compared to context switch time
- q usually 10ms to 100ms, context switch < 10 usec



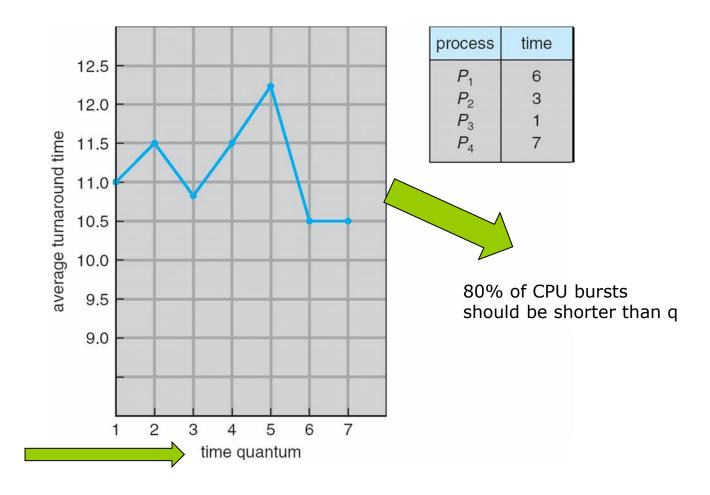
Time Quantum and Context Switch Time







Turnaround Time Varies With The Time Quantum







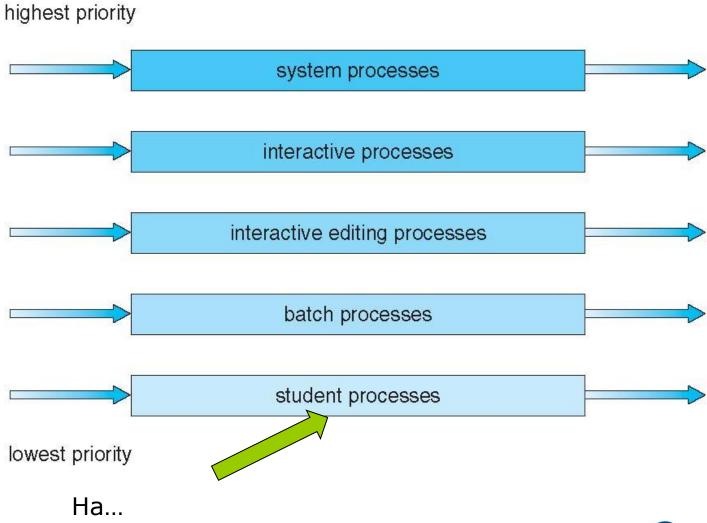
Multilevel Queue

- Ready queue is partitioned into separate queues, eg:
 - foreground (interactive)
 - background (batch)
- Process permanently in a given queue
- Each queue has its own scheduling algorithm:
 - foreground RR
 - background FCFS
- Scheduling must be done between the queues:
 - Fixed priority scheduling; (i.e., serve all from foreground then from background). Possibility of starvation.
 - Time slice each queue gets a certain amount of CPU time which it can schedule amongst its processes; i.e., 80% to foreground in RR, 20% to background in FCFS





Multilevel Queue Scheduling





Multilevel Feedback Queue

- A process can move between the various queues; aging can be implemented this way
- Multilevel-feedback-queue scheduler defined by the following parameters:
 - number of queues
 - scheduling algorithms for each queue
 - method used to determine when to upgrade a process
 - method used to determine when to demote a process
 - method used to determine which queue a process will enter when that process needs service

Multilevel Queue에서는 프로세스들이 큐 사이를 이동하지 않지만 Multilevel Feedback Queue에서는 프로세스들이 큐 사이를 이동한다.
* CPU를 많이 사용하는 프로세스는 낮은 우선순위 큐로 이동됨 입출력 중심의 프로세스와 대화식 프로세스들을 높은 우선순위의 큐에 넣는다. 낮은 우선순위의 큐에서 오래 대기하는 프로세스들을 높은 우선순위의 큐로 이동 (기아 상태를 예방, 에이징)





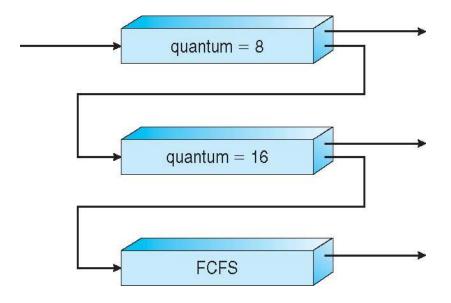
Example of Multilevel Feedback Queue

Three queues:

- Q₀ RR with time quantum 8 milliseconds
- Q₁ RR time quantum 16 milliseconds
- $Q_2 FCFS$

Scheduling

- A new job enters queue Q₀ which is served FCFS
 - When it gains CPU, job receives 8 milliseconds
 - If it does not finish in 8 milliseconds, job is moved to queue Q₁
- At Q₁ job is again served FCFS and receives 16 additional milliseconds
 - If it still does not complete, it is preempted and moved to queue Q₂





Thread Scheduling

- Distinction between user-level and kernel-level threads
- When threads supported, threads scheduled, not processes
- Many-to-one and many-to-many models, thread library schedules user-level threads to run on LWP
 - Known as process-contention scope (PCS) since scheduling competition is within the process
 - Typically done via priority set by programmer
- Kernel thread scheduled onto available CPU is system-contention
 scope (SCS) competition among all threads in system





Pthread Scheduling

- API allows specifying either PCS or SCS during thread creation
 - PTHREAD_SCOPE_PROCESS <u>schedules threads using</u> <u>PCS scheduling</u>
 - PTHREAD_SCOPE_SYSTEM schedules threads using SCS scheduling
- Can be limited by OS Linux and Mac OS X only allow PTHREAD_SCOPE_SYSTEM

Linux - Kernel thread허용 X Unix는 User thread, Kernel thread지원

: 스레드가 어떤 영역(scope)에서 다루어지고 있는지를 얻어오기 위해서 사용된다. PTHREAD_SCOPE_SYSTEM과 PTHREAD_SCOPE_PROCESS의 2가지 영역중에 선택할수 있다.

SYSTEM 영역 스레드는 user 모드 스레드라고 불리우며, PROCESS 스레드는 커널모드 스레드라고 불리운다. 리눅스의 경우 유저모드 스레드인데, 즉 커널에서 스레드를 스케쥴링하는 방식이 아닌 스레드 라이브러리를 통해서 스레드를 스케쥴링 하는 방식을 사용한다.





Pthread Scheduling API

```
#include <pthread.h>
                                               Linux - Kernel thread허용 X
                                           Unix는 User thread, Kernel thread지원
#include <stdio.h>
#define NUM THREADS 5
int main(int argc, char *argv[]) {
   int i, scope;
   pthread t tid[NUM THREADS];
  pthread attr t attr;
   /* get the default attributes */
   pthread attr init(&attr);
   /* first inquire on the current scope */
   if (pthread attr getscope(&attr, &scope) != 0)
      fprintf(stderr, "Unable to get scheduling scope\n");
   else {
      if (scope == PTHREAD SCOPE PROCESS)
         printf("PTHREAD SCOPE PROCESS");
      else if (scope == PTHREAD SCOPE SYSTEM)
         printf("PTHREAD SCOPE SYSTEM");
      else
         fprintf(stderr, "Illegal scope value.\n");
```

Pthread Scheduling API

```
/* set the scheduling algorithm to PCS or SCS */
   pthread attr setscope (&attr, PTHREAD SCOPE SYSTEM);
   /* create the threads */
   for (i = 0; i < NUM THREADS; i++)
      pthread create(&tid[i], &attr, runner, NULL);
   /* now join on each thread */
   for (i = 0; i < NUM THREADS; i++)
      pthread join(tid[i], NULL);
/* Each thread will begin control in this function */
void *runner(void *param)
   /* do some work ... */
  pthread exit(0);
```





Multiple-Processor Scheduling

- CPU scheduling more complex when multiple CPUs are available
- Homogeneous processors within a multiprocessor
- Asymmetric multiprocessing only one processor accesses the system data structures, alleviating the need for data sharing
- Symmetric multiprocessing (SMP) each processor is selfscheduling, all processes in common ready queue, or each has its own private queue of ready processes
 - Currently, most common
- Processor affinity process has affinity for processor on which it is currently running
 - soft affinity
 - hard affinity: migration 허용X
 - Variations including processor sets





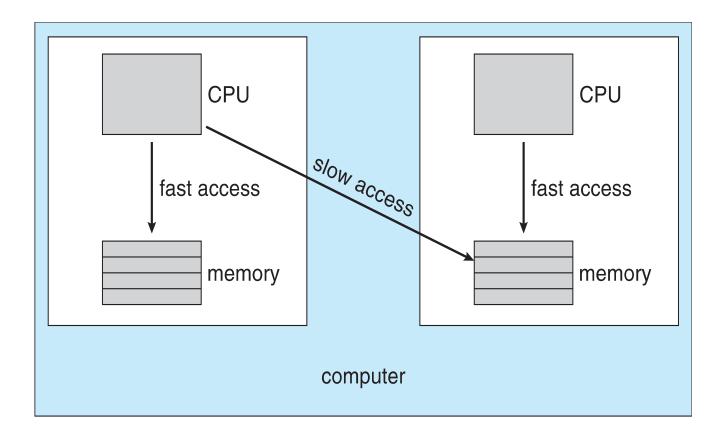
NUMA and CPU Scheduling

- 오래된 컴퓨터에는 시스템 당 CPU 수가 비교적 적어 SMP (Symmetric Multi-Processor)라는 아키텍처를 허용했습니다.
- 즉 이는 시스템에 있는 각각의 CPU는 사용 가능한 메모리로 유사하게 (또는 대칭) 액세스할 수 있음을 의미합니다.
- 최근에는 소켓 당 CPU 수가 많아졌기 때문에 시스템에 있는 모든 RAM에 대해 대칭 액세스를 제공하는 것은 고가의 비용이 들게 됩니다.
- CPU 수가 많은 시스템의 대부분에서는 SMP 대신 NUMA (Non-Uniform Memory Access)라는 아키텍처가 사용되고 있습니다.





NUMA and CPU Scheduling



Note that memory-placement algorithms can also consider affinity





Multiple-Processor Scheduling – Load Balancing

- If SMP, need to keep all CPUs loaded for efficiency
- Load balancing attempts to keep workload evenly distributed
- Push migration periodic task checks load on each processor, and if found pushes task from overloaded CPU to other CPUs
- Pull migration idle processors pulls waiting task from busy processor



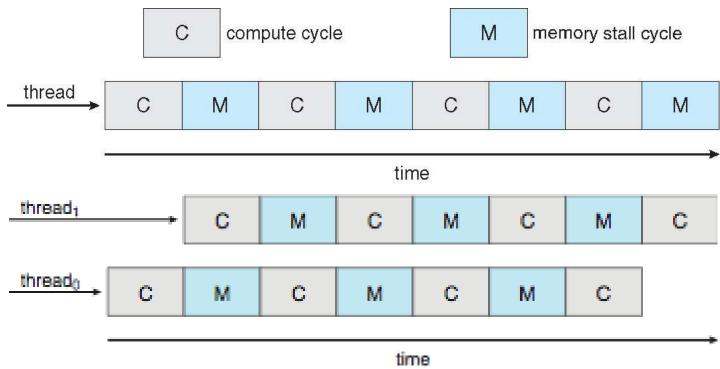
Multicore Processors

- Recent trend to place multiple processor cores on same physical chip
- Faster and consumes less power
- Multiple threads per core also growing
 - Takes advantage of memory stall to make progress on another thread while memory retrieve happens

Smart phone CPU cores 이슈



Multithreaded Multicore System



Multicore processors

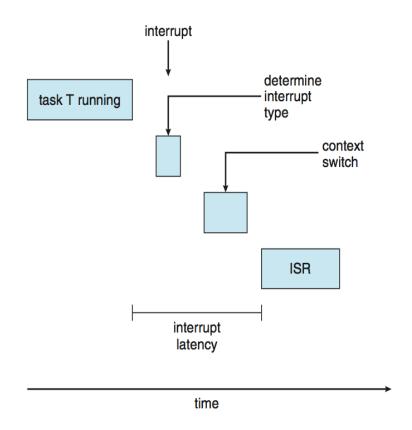
- memory stall: 프로세서가 메모리에 접근할 때, Cache가 아닌 프로세서 외부의 데이터가 사용 가능할 때까지 많은 시간이 소요됨.
- 해결 : 각 프로세서가 두 개이상의 thread를 interleave하여 memory stall 상태일 때 core는 다른 thread로 switch.





Real-Time CPU Scheduling

- Can present obvious challenges
- Soft real-time systems no guarantee as to when critical real-time process will be scheduled
- Hard real-time systems task must be serviced by its deadline
- Two types of latencies affect performance
 - Interrupt latency time from arrival of interrupt to start of routine that services interrupt
 - Dispatch latency time for schedule to take current process off CPU and switch to another



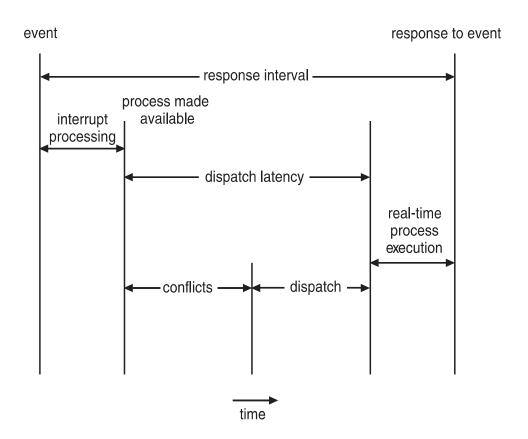
*ISR: Interrupt Service Routine





Real-Time CPU Scheduling (Cont.)

- Conflict phase of dispatch latency:
 - Preemption of any process running in kernel mode
 - Release by lowpriority process of resources needed by highpriority processes

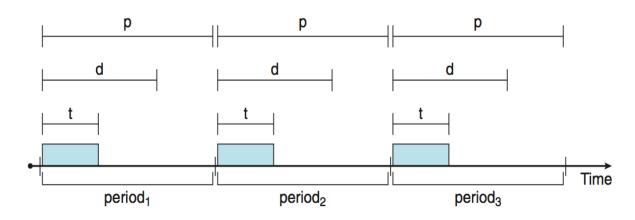






Priority-based Scheduling

- For real-time scheduling, scheduler must support preemptive, prioritybased scheduling
 - But only guarantees soft real-time
- For hard real-time must also provide ability to meet deadlines
- Processes have new characteristics: periodic ones require CPU at constant intervals
 - Has processing time t, deadline d, period p
 - $0 \le t \le d \le p$
 - Rate of periodic task is 1/p





Virtualization and Scheduling

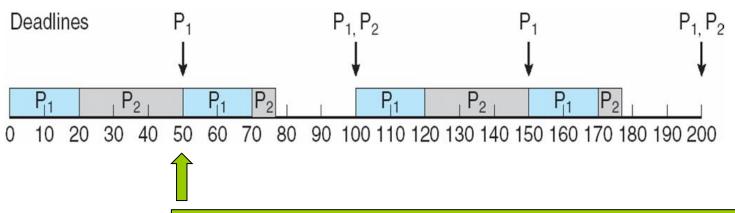
- Virtualization software schedules multiple guests onto CPU(s)
- Each guest doing its own scheduling
 - Not knowing it doesn't own the CPUs
 - Can result in poor response time
 - Can effect time-of-day clocks in guests
- Can undo good scheduling algorithm efforts of guests



Rate Montonic Scheduling

- A priority is assigned based on the inverse of its period
- Shorter periods = higher priority;
- Longer periods = lower priority
- P₁ is assigned a higher priority than P₂.

주기Period: P1= 50, P2=100 P1의 수행시간 t1=20, P2의 수행시간 t2=35,

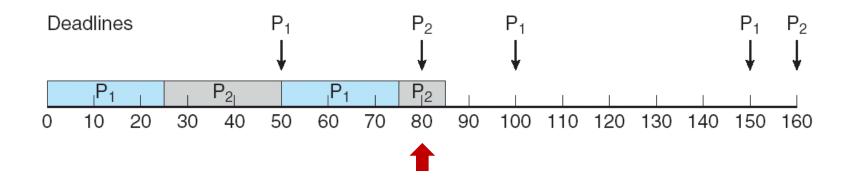


P1의 주기가 50이므로, 50이 되었을 때 P1이 P2를 밀어내고 선점함 P2의 5남은 것은 P1의 선점 이후에 70~75에 처리





Missed Deadlines with Rate Monotonic Scheduling



주기Period: P1= 50, P2=80

P1의 수행시간 t1=25, P2의 수행시간 t2=35,

Then, 짧은주기 Priority: P1 > P2

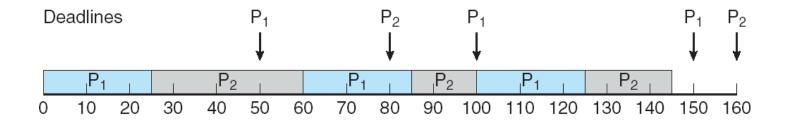




Earliest Deadline First Scheduling (EDF)

Priorities are assigned according to deadlines:

the earlier the deadline, the higher the priority; the later the deadline, the lower the priority







Proportional Share Scheduling

- T shares are allocated among all processes in the system.
- An application receives N shares where N < T
- This ensures each application will receive N/T of the total processor time



POSIX Real-Time Scheduling

- The POSIX.1b standard
- API provides functions for managing real-time threads
- Defines two scheduling classes for real-time threads:
- SCHED_FIFO threads are scheduled using a FCFS strategy with a FIFO queue. There is no time-slicing for threads of equal priority
- SCHED_RR similar to SCHED_FIFO except time-slicing occurs for threads of equal priority
- Defines two functions for getting and setting scheduling policy:
- 1. pthread_attr_getsched_policy(pthread_attr_t *attr,
 int *policy)
- 2. pthread_attr_setsched_policy(pthread_attr_t *attr,
 int policy)



POSIX Real-Time Scheduling API

```
#include <pthread.h>
#include <stdio.h>
#define NUM THREADS 5
int main(int argc, char *argv[])
   int i, policy;
  pthread t tid[NUM THREADS];
  pthread attr t attr;
   /* get the default attributes */
  pthread attr init(&attr);
   /* get the current scheduling policy */
   if (pthread attr getschedpolicy(&attr, &policy) != 0)
      fprintf(stderr, "Unable to get policy.\n");
  else {
      if (policy == SCHED OTHER) printf("SCHED OTHER\n");
      else if (policy == SCHED RR) printf("SCHED RR\n");
      else if (policy == SCHED FIFO) printf("SCHED FIFO\n");
```



POSIX Real-Time Scheduling API (Cont.)

```
/* set the scheduling policy - FIFO, RR, or OTHER */
   if (pthread attr setschedpolicy(&attr, SCHED FIFO) != 0)
      fprintf(stderr, "Unable to set policy.\n");
   /* create the threads */
   for (i = 0; i < NUM THREADS; i++)
      pthread create(&tid[i], &attr, runner, NULL);
   /* now join on each thread */
   for (i = 0; i < NUM THREADS; i++)
      pthread join(tid[i], NULL);
/* Each thread will begin control in this function */
void *runner(void *param)
   /* do some work ... */
   pthread exit(0);
```





Operating System Examples

- Linux scheduling
- Windows scheduling
- Solaris scheduling





Linux Scheduling Through Version 2.5

- Prior to kernel version 2.5, ran variation of standard UNIX scheduling algorithm
- Version 2.5 moved to constant order O(1) scheduling time
 - Preemptive, priority based
 - Two priority ranges: time-sharing and real-time
 - Real-time range from 0 to 99 and nice value from 100 to 140
 - Map into global priority with numerically lower values indicating higher priority
 - Higher priority gets larger q
 - Task run-able as long as time left in time slice (active)
 - If no time left (expired), not run-able until all other tasks use their slices
 - All run-able tasks tracked in per-CPU runqueue data structure
 - Two priority arrays (active, expired)
 - Tasks indexed by priority
 - When no more active, arrays are exchanged
 - Worked well, but poor response times for interactive processes



Linux Scheduling in Version 2.6.23 +

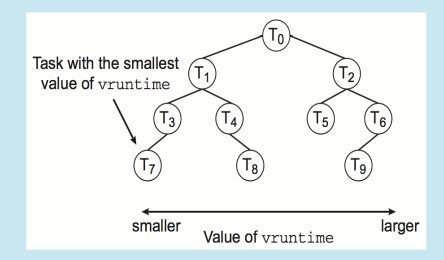
- Completely Fair Scheduler (CFS)
- Scheduling classes
 - Each has specific priority
 - Scheduler picks highest priority task in highest scheduling class
 - Rather than quantum based on fixed time allotments, based on proportion of CPU time
 - 2 scheduling classes included, others can be added
 - 1. default
 - real-time
- Quantum calculated based on nice value from -20 to +19
 - Lower value is higher priority
 - Calculates target latency interval of time during which task should run at least once
 - Target latency can increase if say number of active tasks increases
- CFS scheduler maintains per task virtual run time in variable vruntime
 - Associated with decay factor based on priority of task lower priority is higher decay rate
 - Normal default priority yields virtual run time = actual run time
- To decide next task to run, scheduler picks task with lowest virtual run time





CFS Performance

The Linux CFS scheduler provides an efficient algorithm for selecting which task to run next. Each runnable task is placed in a red-black tree—a balanced binary search tree whose key is based on the value of vruntime. This tree is shown below:

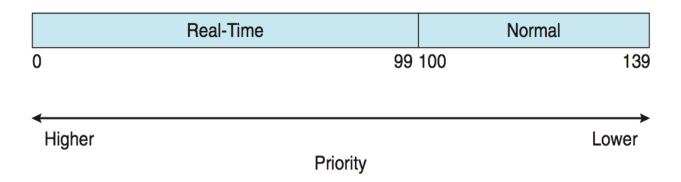


When a task becomes runnable, it is added to the tree. If a task on the tree is not runnable (for example, if it is blocked while waiting for I/O), it is removed. Generally speaking, tasks that have been given less processing time (smaller values of vruntime) are toward the left side of the tree, and tasks that have been given more processing time are on the right side. According to the properties of a binary search tree, the leftmost node has the smallest key value, which for the sake of the CFS scheduler means that it is the task with the highest priority. Because the red-black tree is balanced, navigating it to discover the leftmost node will require O(lgN) operations (where N is the number of nodes in the tree). However, for efficiency reasons, the Linux scheduler caches this value in the variable rb_leftmost, and thus determining which task to run next requires only retrieving the cached value.



Linux Scheduling (Cont.)

- Real-time scheduling according to POSIX.1b
 - Real-time tasks have static priorities
- Real-time plus normal map into global priority scheme
- Nice value of -20 maps to global priority 100
- Nice value of +19 maps to priority 139





Windows Scheduling

- Windows uses priority-based preemptive scheduling
- Highest-priority thread runs next
- Dispatcher is scheduler
- Thread runs until (1) blocks, (2) uses time slice, (3) preempted by higher-priority thread
- Real-time threads can preempt non-real-time
- 32-level priority scheme
- Variable class is 1-15, real-time class is 16-31
- Priority 0 is memory-management thread
- Queue for each priority
- If no run-able thread, runs idle thread





Windows Priority Classes

- Win32 API identifies several priority classes to which a process can belong
 - REALTIME_PRIORITY_CLASS, HIGH_PRIORITY_CLASS, ABOVE_NORMAL_PRIORITY_CLASS,NORMAL_PRIORITY_CLASS, BELOW_NORMAL_PRIORITY_CLASS, IDLE_PRIORITY_CLASS
 - All are variable except REALTIME
- A thread within a given priority class has a relative priority
 - TIME_CRITICAL, HIGHEST, ABOVE_NORMAL, NORMAL, BELOW_NORMAL, LOWEST, IDLE
- Priority class and relative priority combine to give numeric priority
- Base priority is NORMAL within the class
- If quantum expires, priority lowered, but never below base



Windows Priority Classes (Cont.)

- If wait occurs, priority boosted depending on what was waited for
- Foreground window given 3x priority boost
- Windows 7 added user-mode scheduling (UMS)
 - Applications create and manage threads independent of kernel
 - For large number of threads, much more efficient
 - UMS schedulers come from programming language libraries like
 C++ Concurrent Runtime (ConcRT) framework





Windows Priorities

	real- time	high	above normal	normal	below normal	idle priority
time-critical	31	15	15	15	15	15
highest	26	15	12	10	8	6
above normal	25	14	11	9	7	5
normal	24	13	10	8	6	4
below normal	23	12	9	7	5	3
lowest	22	11	8	6	4	2
idle	16	1	1	1	1	1





Solaris

- Priority-based scheduling
- Six classes available
 - Time sharing (default) (TS)
 - Interactive (IA)
 - Real time (RT)
 - System (SYS)
 - Fair Share (FSS)
 - Fixed priority (FP)
- Given thread can be in one class at a time
- Each class has its own scheduling algorithm
- Time sharing is multi-level feedback queue
 - Loadable table configurable by sysadmin





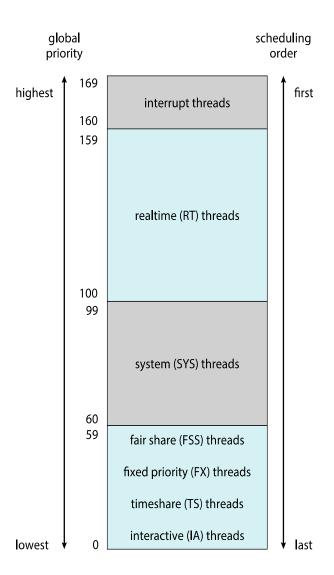
Solaris Dispatch Table

priority	time quantum	time quantum expired	return from sleep
0	200	0	50
5	200	0	50
10	160	0	51
15	160	5	51
20	120	10	52
25	120	15	52
30	80	20	53
35	80	25	54
40	40	30	55
45	40	35	56
50	40	40	58
55	40	45	58
59	20	49	59





Solaris Scheduling







Solaris Scheduling (Cont.)

- Scheduler converts class-specific priorities into a per-thread global priority
 - Thread with highest priority runs next
 - Runs until (1) blocks, (2) uses time slice, (3) preempted by higher-priority thread
 - Multiple threads at same priority selected via RR



Algorithm Evaluation

- How to select CPU-scheduling algorithm for an OS?
- Determine criteria, then evaluate algorithms
- Deterministic modeling
 - Type of analytic evaluation
 - Takes a particular predetermined workload and defines the performance of each algorithm for that workload
- Consider 5 processes arriving at time 0:

Process	Burst Time		
P_1	10		
P_2	29		
P_3	3		
P_4	7		
P_5	12		



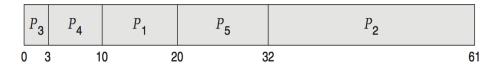


Deterministic Evaluation

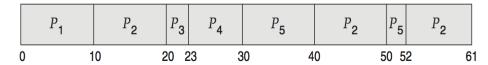
- For each algorithm, calculate minimum average waiting time
- Simple and fast, but requires exact numbers for input, applies only to those inputs
 - FCS is 28ms:



Non-preemptive SFJ is 13ms:



• RR is 23ms:







Queueing Models

- Describes the arrival of processes, and CPU and I/O bursts probabilistically
 - Commonly exponential, and described by mean
 - Computes average throughput, utilization, waiting time, etc
- Computer system described as network of servers, each with queue of waiting processes
 - Knowing arrival rates and service rates
 - Computes utilization, average queue length, average wait time, etc





Little's Formula

- n = average queue length
- W = average waiting time in queue
- λ = average arrival rate into queue
- Little's law in steady state, processes leaving queue must equal processes arriving, thus:

$$n = \lambda \times W$$

- Valid for any scheduling algorithm and arrival distribution
- For example, if on average 7 processes arrive per second, and normally 14 processes in queue, then average wait time per process = 2 seconds



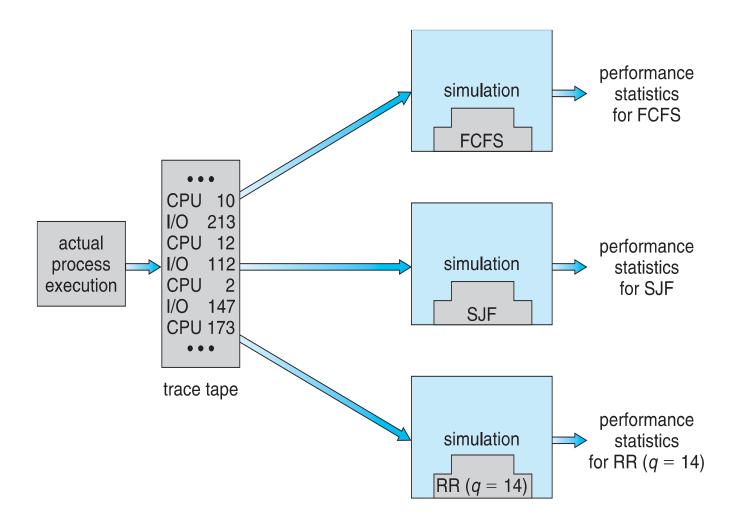
Simulations

- Queueing models limited
- Simulations more accurate
 - Programmed model of computer system
 - Clock is a variable
 - Gather statistics indicating algorithm performance
 - Data to drive simulation gathered via
 - Random number generator according to probabilities
 - Distributions defined mathematically or empirically
 - Trace tapes record sequences of real events in real systems





Evaluation of CPU Schedulers by Simulation







Implementation

- Even simulations have limited accuracy
- Just implement new scheduler and test in real systems
 - High cost, high risk
 - Environments vary
- Most flexible schedulers can be modified per-site or per-system
- Or APIs to modify priorities
- But again environments vary





End of Chapter 5

