

Mickaël Dardaillon

Education

- 2014 Doctorate degree, INSA Lyon, Computer Science.
 Models of Computation, Compilation, Telecommunications and Embedded Systems.
- 2011 **Master degree**, *Université d'Orléans*, Electrical Engineering. Electronics, Signal Processing and Microsystems.
- 2011 **Engineer degree**, *Polytech'Orléans*, Electrical Engineering. Electronics, Signal and Image Processing.

Experience

- 2015–2017 **Staff Software Engineer**, *National Instruments*, Austin, Texas. Models of Computation and High-Level Synthesis.
- 2014–2015 **Research Intern**, *Nokia Technologies*, Berkeley, California. Implementation of LTE-Advanced Protocol on FPGA and Manycores.
- 2011–2014 **PhD Student**, *CITI-Inria*, *INSA Lyon & CEA*, *LETI*, Lyon & Grenoble, France. Compilation for Parametric Dataflow Targeting MPSoC.
 - 2011 **Research Intern**, *CITI-Inria*, *INSA Lyon*, Lyon, France. Implementation of Cryptographic Primitives on Wireless Sensor Node.

Teaching

Undergraduate Architecture, Algorithmic and Databases.

Lab teaching and examination design

Elementary First Lego League Robotics.

Team coaching

Technical Skills

Hardware Design and Implementation.

Compilation

Network

Design and Implementation.

FPGA architecture development

Theory and implementation

PHY layer development

Applications Software Defined Radio, Digital Signal Processing.

Soft Skills

Collaboration Teamwork and Communication. Work within local and international teams
Organization Planning and Prioritization. Feature research, design and implementation

Publications

Thesis manuscript

2014 DardaillonThesis14

Journal

2016 **DardaillonTaco16**

Book chapter

2014 Dardaillonlgi14

International conferences

- 2015 **DardaillonIcecs15**
- 2014 DardaillonCases14
- 2012 **DardaillonReconfig12**

Dardaillonlwcmc12

National conference and seminar

- 2015 DardaillonDreams15
- 2014 DardaillonCompas14