```
* Name:
               clock.c
     * Description: STM32 peripherals initialization and functions
     * Version: V1.00
     * Author: Daris Lychuk
5
6
7
     * This software is supplied "AS IS" without warranties of any kind.
9
10
     * History:
11
12
               V1.00 Initial Version
              V1.1 reformatted (kjn)
13
                          14
15
    #include "project.h"
16
    // CLOCK AND TIMING FUNCTIONS
17
18
    /*
19
    * Name:
20
                  void clockInit()
21
    * Paramaters: none
    ^{\star} Description: This function will initialize the device internal
22
23
                  clock to 24 Mhz
    */
24
25
    void clockInit(void)
26
27
        uint32 t temp = 0 \times 00;
28
        //If you hover over the RCC you can go to the definition and then
29
        //see it is a structure of all the RCC registers. Then you can
30
        //simply assign a value.
31
        RCC - > CFGR = 0 \times 07050002;
                                  // Output PLL/2 as MCO,
32
                                   // PLLMUL X3, PREDIV1 is PLL input
33
34
        RCC -> CR = 0 \times 01010081;
                                  // Turn on PLL, HSE, HSI
35
36
        while (temp != 0x02000000) // Wait for the PLL to stabilize
37
            temp = RCC->CR & 0 \times 020000000; //Check to see if the PLL lock bit is set
38
39
40
41
        //Enable peripheral clocks for various ports and subsystems
        //Bit 4: Port C Bit3: Port B Bit 2: Port A
42
        RCC->APB2ENR |= RCC APB2ENR IOPAEN | RCC APB2ENR AFIOEN | RCC APB2ENR IOPBEN; // |
43
    RCC APB2ENR USART1EN;
44
        RCC->APB1ENR |= RCC_APB1ENR_USART3EN;
        AFIO->MAPR |= AFIO MAPR SWJ CFG JTAGDISABLE;
47
        // Write a 0xB ( 1011b ) into the configuration and mode bits for PA8 (AFIO)
   //
        GPIOA->CRH |= GPIO CRH MODE15;
48
49
        GPIOA->CRH &= ~GPIO CRH CNF15;
    //
50
51
        GPIOB->CRH |= GPIO CRH MODE15;
        GPIOB->CRH &= ~GPIO CRH CNF15;
52
53
54
        GPIOB->CRH |= GPIO CRH CNF10 1 | GPIO CRH MODE10 0 | GPIO CRH MODE10 1;
        GPIOB->CRH &= ~GPIO CRH CNF10 0;
55
56
57
        GPIOB->CRH |= GPIO CRH CNF11 0 ;
58
        GPIOB->CRH &= ~GPIO_CRH_CNF11_1 & ~GPIO_CRH_MODE11_1 & ~GPIO_CRH_MODE11_0 ;
59
   }
61 void delay(uint32 t count)
62 {
63
        int i=0;
64
        for(i=0; i< count; ++i)</pre>
65
        {
66
67
    }
68
```