Semnale de control MIPS32

Instrucţiune	Opcode Instr[31-26]	Reg Dst	ExtOp	ALUSrc	Branch	BGTZ	Jump	Mem Write	Memto Reg	Reg Write	ALUOp[1:0]	function Instr[5-0]	ALUCtrl[3:0]
ADD	000000	1	X	0	0	0	0	0	0	1	10(R)	000000	0000(+)
SUB	000000	1	X	0	0	0	0	0	0	1	10(R)	000001	0100(-)
SLL	000000	1	X	0	0	0	0	0	0	1	10(R)	000010	0001(<<)
SRL	000000	1	X	0	0	0	0	0	0	1	10(R)	000011	0011(>>)
AND	000000	1	X	0	0	0	0	0	0	1	10(R)	000100	0110(&)
OR	000000	1	X	0	0	0	0	0	0	1	10R	000101	0010(|)
SLT	000000	1	X	0	0	0	0	0	0	1	10(R)	000110	0111(<>)
XOR	000000	1	X	0	0	0	0	0	0	1	10(R)	000111	1000(^)
ADDI	000001	0	1	1	0	0	0	0	0	1	00(+)	XXXXXX	0000(+)
LW	000010	0	1	1	0	0	0	0	1	1	00(+)	XXXXXX	0000(+)
SW	000011	X	1	1	0	0	0	1	X	0	00(+)	XXXXXX	0000(+)
BEQ	000100	X	1	0	1	0	0	0	X	0	01(-)	XXXXXX	0100(-)
ORI	000101	1	1	1	0	0	0	0	0	1	11(|)	XXXXXX	0010(|)
BGTZ	000110	X	1	0	0	1	0	0	X	0	01(-)	XXXXXX	0100(-)
J	000111	X	X	X	0	0	1	0	X	0	10	100000	0000