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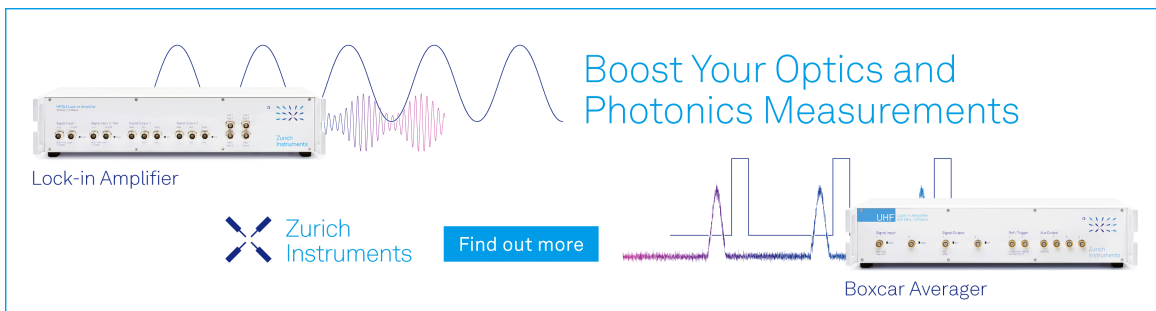


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Ultralow $1/f$ noise in epigraphene devices

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ABSTRACT

We report the lowest recorded levels of $1/f$ noise for graphene-based devices, at the level of $S_V/V^2 = S_I/I^2 = 4.4 \times 10^{-16}$ (1/Hz), measured at $f = 10$ Hz ($S_V/V^2 = S_I/I^2 < 10^{-16}$ 1/Hz for $f > 100$ Hz) in large-area epitaxial graphene on silicon carbide (epigraphene) Hall sensors. This performance is made possible through the combination of high material quality, low contact resistance achieved by edge contact fabrication process, homogeneous doping, and stable passivation of the graphene layer. Our study explores the nature of $1/f$ noise as a function of carrier density and device geometry and includes data from Hall sensors with device area range spanning over six orders of magnitude, with characteristic device length ranging from $L = 1 \mu\text{m}$ to 1 mm. In optimized graphene Hall sensors, we demonstrate arrays to be a viable route to improve further the magnetic field detection: a simple parallel connection of two devices displays record-high magnetic field sensitivity at room temperature, with minimum detectable magnetic field levels down to $B_{\min} = 9.5 \text{ nT}/\sqrt{\text{Hz}}$. The remarkable low levels of $1/f$ noise observed in epigraphene devices hold immense capacity for the design and fabrication of scalable epigraphene-based sensors with exceptional performance.

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Two-dimensional (2D) materials have tremendous potential for a variety of sensing applications because faint stimuli (e.g., light, magnetic field, and chemical environment) can readily produce large changes in their electrical resistance. In particular, for graphene, its high carrier mobility, thermal conductivity, saturation velocity, and the ability to tune carrier density make this 2D material ideally suited for the implementation of sensors with resistive readout schemes in electronics,^{1,2} chemical,³ radiation,^{4,5} and biological⁶ applications. For practical sensors, however, the high sensitivity must be complemented with low-noise, to achieve signal-to-noise ratios (SNR) higher than unity. At low frequencies (DC), $1/f$ noise (also known as flicker noise, pink noise, or drift) is the main bottleneck to achieve high SNR.^{1,7–9} Flicker noise, characterized by a spectral density $S(f) \sim 1/f^\gamma$ (where f represents frequency and $\gamma \approx 1$), is ubiquitous in many physical processes. For solid state devices including graphene, when a device is biased, the excess current noise can be written as fluctuations in electrical current: $\delta I \propto q(\delta N)\mu + qN(\delta\mu)$, where q is the charge of

an electron, N is the number of charge carriers, and μ is the mobility. Thus, contribution to the noise could arise from fluctuations in number of charge carriers $N^{10–12}$ and/or fluctuation in mobility μ .^{13–15} Specific to graphene, under theoretical conditions, it is possible to approach vanishing noise when graphene carrier density approaches zero (Dirac point), provided that, at effectively zero carrier concentration, the mobility of holes is similar to the mobility of electrons.^{12,16–21} This is in general only possible at low temperatures, where suppressed thermal fluctuations enable approaching a zero-carrier density scenario. In practice, for graphene devices operating at room temperature, various approaches have been explored to reduce the contribution of $1/f$ noise, such as electron irradiation,²² passivation with high-k dielectrics,^{11,16} and low k dielectrics.² Yet, noise mitigation strategies are actively sought for graphene devices to optimize their performance.

To study noise in our devices, we have minimized all possible material-, device-, and instrumental-related noise sources. Since epitaxial growth of graphene involves annealing the SiC substrates at very

high temperatures, $T > 1800^\circ\text{C}$, crystal imperfections, vacancies, and thus, charge traps are considerably reduced.^{5,23–26} Also, we have meticulously optimized the epitaxial growth parameters to obtain high-quality graphene on SiC resulting in an impressive reduction of bilayer (BL) graphene domains to a coverage $< 0.1\%$ of the surface.^{27,28} Our device fabrication incorporates an edge contact approach that consistently achieves contact resistance values below $50\ \Omega\cdot\mu\text{m}$.²⁹ Finally, to characterize noise, we employ a digital cross correlation spectrum analyzer to effectively eliminate instrumental noise.³⁰

Figure 1(a) shows the Raman spectrum of optimized epigraphene on Si face (0001), as well as the SiC substrate. In epigraphene, the Raman spectrum contains information about both the graphene and interfacial carbon buffer layer. The buffer layer is characterized by a broad Raman band centered at approximately at 1355 cm^{-1} , which makes it hard to distinguish it from the (disorder) D peak (1360 cm^{-1}).³¹ However, the absence of $\text{D}' \sim 1620\text{ cm}^{-1}$, $\text{D} + \text{D}' \sim 2950\text{ cm}^{-1}$ (due to disorder), and the prominent 2D and 2D' peaks suggest that the epigraphene layer is essentially defect-free.^{32,33} Also visible in the spectrum is the $\text{D} + \text{D}''$ (also referred to as G'') at $\sim 2500\text{ cm}^{-1}$, which is identified purely for single layer graphene and disappears for increasing layer numbers.³³ To complement Raman studies, Fig. 1(b) shows the atomic force microscopy (AFM) topography image of epigraphene over a region of size $20 \times 20\ \mu\text{m}^2$. The surface is typically characterized by the presence of step edges with heights comparable to the unit cell height of 4H-SiC ($\leq 1\text{ nm}$).³⁴ Additionally, the phase-contrast AFM scan image Fig. 1(c) enables to distinguish monolayer (ML) from bilayer (BL) graphene domains; the light blue contrast represents very sparse BL domains, which are found mainly in regions with deep terrace formation during the growth process. In general, the surface study of our optimized material reveals that epigraphene has a regular surface morphology without gaps, and that growth of BL graphene domains is heavily suppressed. These

characteristics result in a general reduction of the resistance anisotropy of the material and enable scalable patterning of the devices on the surface.

Figure 2 shows a representative transmission mode optical image of the Hall sensor devices patterned on epigraphene, having characteristic lengths of $1\ \mu\text{m}$ and 1 mm . In the micrograph, metallic contacts appear black, while dark (light) shades of gray indicate the presence (absence) of epigraphene.²⁷ To achieve low contact resistance to epigraphene, we employed a specially designed tri-layer resist recipe, which creates edge contact between metal and graphene.²⁹ For these devices, contacts are formed by depositing a 5 nm thick titanium, followed by 100 nm Au as capping layer, using electron-beam evaporation. To explore the influence of device geometry on $1/f$ noise, we fabricated square-shaped devices with widths (w) of 1 , 10 , 100 , and $1000\ \mu\text{m}$. Such wide range in device geometry is achievable due to large scale production of high-quality epigraphene as well as the scalable edge contact fabrication process.

To explore $1/f$ noise in epigraphene at different doping levels, we have utilized three strategies: chemical and electrostatic gating, molecular doping [F4TCNQ—2,3,5,6-tetrafluorotetracyanoquinodimethan and PMMA—poly(methyl-methacrylate)],³⁵ and polymer passivation (120 nm -thick PMMA layer). The latter serves as a control approach to benchmark the noise performance of the devices. First, we describe gated devices using a solid electrolyte implemented with lithium triflate ($\text{CF}_3\text{SO}_3\text{Li}$) diluted in a polyethylene glycol matrix in combination with a co-planar graphene electrode as gate. This approach allows gating epigraphene across Dirac point without degrading its mobility due to the absence of chemical interaction. Figure 3(a) shows a comparison of the resistance and noise power spectral density (PSD) (at $f = 10\text{ Hz}$) as a function of carrier density in a graphene Hall bar device. Each data point in noise PSD is a 30 min long measurement with minimum 100 spectral averages. The results

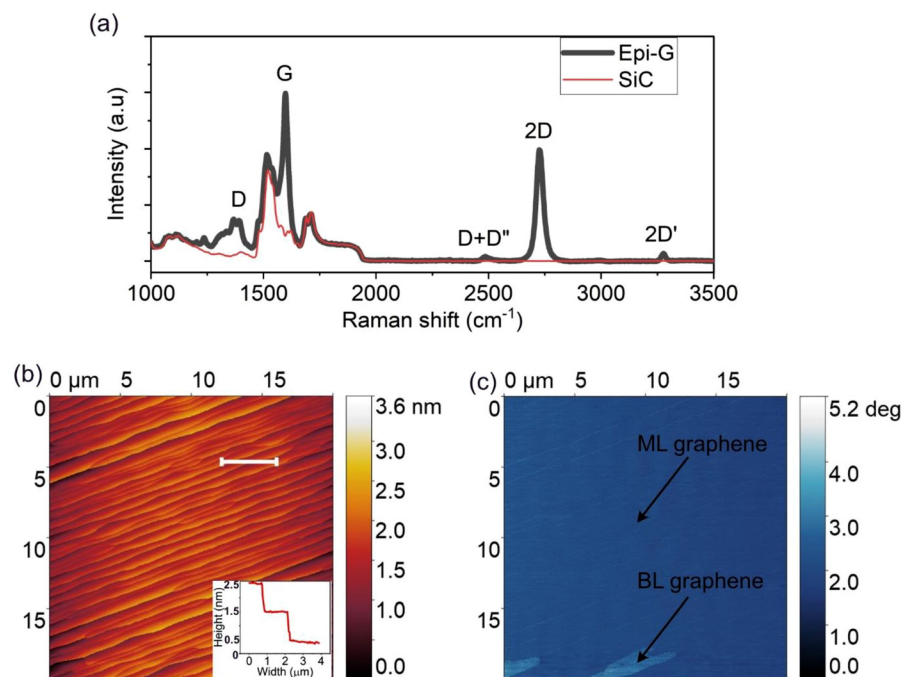


FIG. 1. (a) Raman spectrum of epigraphene (Epi-G) grown on 4H-SiC substrate (black) compared to the 4H-SiC substrate (red). (b) AFM height map of $20 \times 20\ \mu\text{m}^2$ area over a $7 \times 7\text{ nm}^2$ epigraphene sample. The inset in (b) is the line profile of the height map, indicating the step height ($\leq 1\text{ nm}$) on the Si face of SiC substrate after the growth of epigraphene. (c) The AFM phase map of the same area as in (b). The light blue shaded patches at the bottom of the image indicate the BL graphene patches.

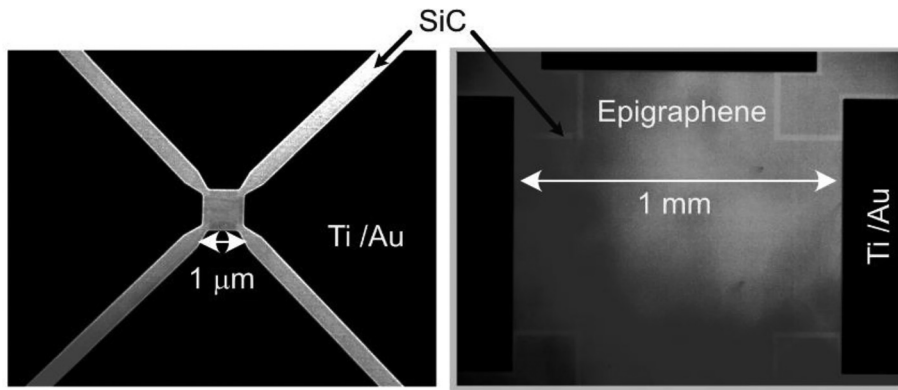


FIG. 2. Transmission mode optical image of 1 μm and 1 mm wide Hall sensor geometry fabricated using epitaxially grown graphene over the SiC substrate. Contacts are fabricated using edge contact fabrication process with 5 nm Ti and 100 nm Au.

confirm that epigraphene is intrinsically n-doped, with Dirac point located at about $V_g = -0.55$ V, and reveal that the maximum device noise ($S_V/V^2 = 7 \times 10^{-13}$ 1/Hz) occurs when electrostatic doping is in the vicinity of the Dirac point (for details of noise measurement setup see Fig. S1). This so-called “ Λ ” shape²⁰ of the noise spectrum can be attributed to the fact that, at 300 K, the epigraphene sample displays an uneven distribution of charge carriers around the charge neutrality point, forming electron-hole puddles resulting in potential (i.e., carrier density) fluctuations. To support this claim, the procedure to calculate disorder potential s is shown in Fig. S2 from the supplementary material. From this, the minimum attainable carrier density of epigraphene at room temperature is $|n_s| < 3 \times 10^{11} \text{ cm}^{-2}$, which yields an estimated disorder potential $s = 16$ meV. Thus, when the electrostatic gate is tuned to further reduce the carrier density in the material, the formation of electron-hole puddles manifests as a non-linear Hall (two-band model) coefficient.² Altogether, chemical and electrostatic doping of epigraphene reveals the low potential fluctuations in epigraphene ($s = 16$ meV), and that the lowest well-defined carrier density attainable in the material at room temperature is of the order of $|n| \approx 3 \times 10^{11} \text{ cm}^{-2}$. Higher carrier density levels result in decreased $1/f$ noise.

Having established the carrier density dependence of $1/f$ noise in epigraphene, we compare the noise performance of the different

doping and passivation approaches. Figure 3(b) shows a comparison of noise power spectral density per unit area for epigraphene doped using the three different schemes, PMMA, F4TCNQ, and $\text{CF}_3\text{SO}_3\text{Li}$. The comparison shows that the solid electrolyte ($n = 0.4 \times 10^{12} \text{ cm}^{-2}$) used for gating the epigraphene displays the highest levels of noise, while molecular doping ($n = 0.42 \times 10^{12} \text{ cm}^{-2}$) and the PMMA-encapsulated device ($n = 7 \times 10^{12} \text{ cm}^{-2}$) show noise power amplitude about three orders of magnitude lower. While the high noise in chemically/electrostatically doped graphene could be attributed to mobile lithium ions causing carrier density fluctuations in epigraphene, the low noise in PMMA and F4TCNQ samples deserves more attention. This is because, with epigraphene exhibiting Λ shape in its PSD, the $1/f$ noise is expected to follow the empirical Hooge’s law $\frac{S_V}{V^2} = \frac{S_I}{I^2} = \frac{S_R}{R^2} = \frac{\alpha_H}{Nf}$, where S_V , S_I , and S_R are the power spectral density (PSD) of the fluctuations in the values of voltage V , current I , and resistance R , N is the charge carrier number, f is the frequency, and α_H is the empirical Hooge parameter. With PMMA passivation resulting in heavily n-doped epigraphene, an order of magnitude higher than molecularly (F4TCNQ) doped epigraphene (see also tabulated data in supplementary material Table I), the small difference in noise amplitude results in different Hooge parameters for the different epigraphene doping. For PMMA samples at high doping, we obtain $\alpha_{H\text{-PMMA}} \approx 5 \times 10^{-4}$

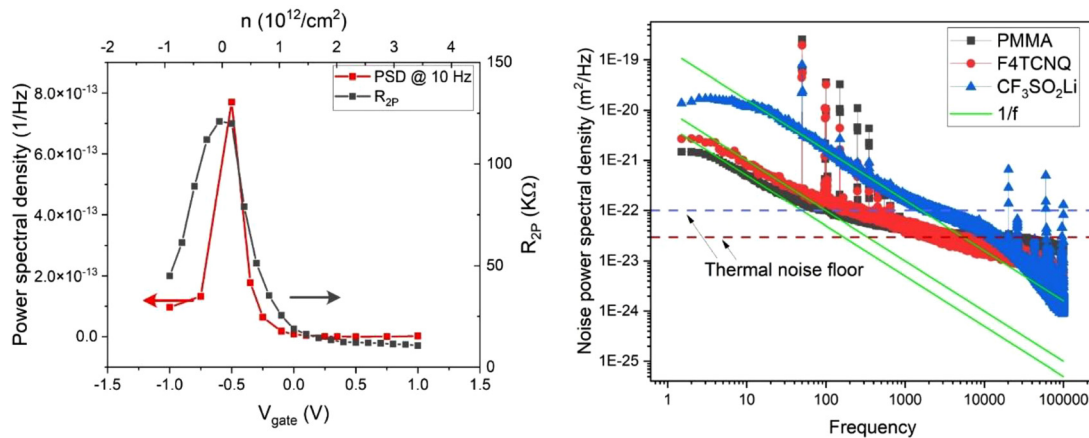


FIG. 3. (a) Comparison of two-point resistance (R_{2P}) and noise power spectral density amplitude (at $f = 10$ Hz) measured for electrolytically gated epigraphene. Negative charge carrier density corresponds to the p type of carrier. (b) Comparison of noise power spectral density (PSD) of PMMA ($n = 7 \times 10^{12} \text{ cm}^{-2}$), F4TCNQ ($n = 0.42 \times 10^{12} \text{ cm}^{-2}$), and $\text{CF}_3\text{SO}_3\text{Li}$ ($n = 0.4 \times 10^{12} \text{ cm}^{-2}$). PSD is normalized with respect to device geometry. For $\text{CF}_3\text{SO}_3\text{Li}$, the values correspond to the gate voltage fixed at the Dirac point.

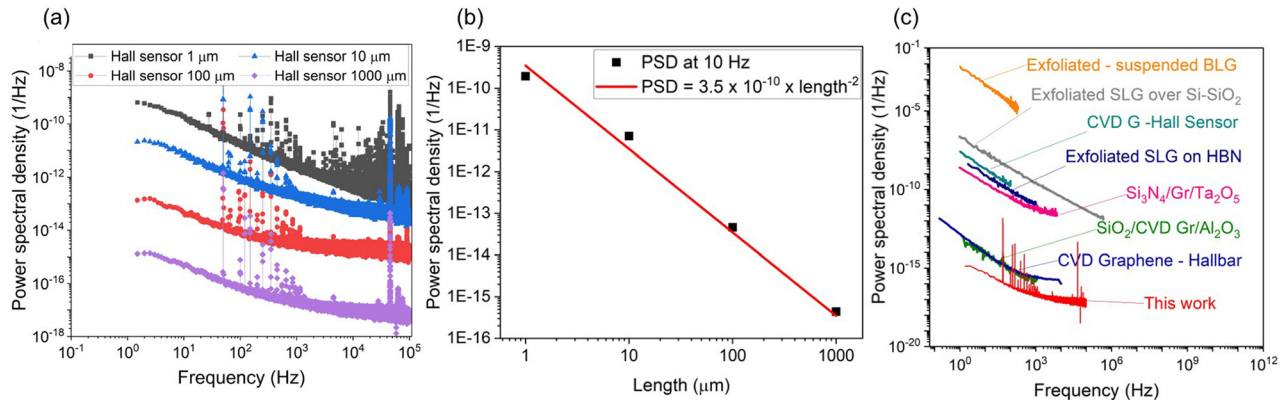


FIG. 4. (a) Noise PSD measured for device width varying from 1 to 1000 μm , within 2 to 100 kHz bandwidth. (b) Noise PSD measured at 10 Hz shows area dependency of noise PSD. (c) Comparison of noise PSD of epitaxial graphene (Epi-G) to historical data over wide variety of graphene devices. Observed noise in epitaxial graphene is an order lower than previously reported values.^{11,16,17,30,36–38}

(see the supplementary material, Fig. S3), while for F4TCNQ, it can be as low as $\alpha_{\text{H-F4TCNQ}} \approx 10^{-5.2}$.

We verify the geometrical scaling of noise anticipated by Hooge's law in devices of different sizes, spanning an area range from $A = 1$ to $10^6 \mu\text{m}^2$. The total number of electrons N in a sample is related to the device size by the relation $N = l^2/(q\mu R)$, where l is the length of the sample, q is the elementary charge, μ is the mobility, and R is the sample resistance. Together with Hooge's law, the dependence of noise PSD over device geometry $S_V \propto 1/l^2$. Figure 4(a) illustrates the noise PSD measurements conducted on different PMMA-passivated epigraphene devices with varying $l = w$ ranging from 1 to 1000 μm (see supplementary material Table II for tabulated values of resistance, carrier density, and carrier mobility). As shown in Fig. 4(b), the variation in noise $\text{PSD} \propto a \times l^{-2}$ (where l is in units of μm) with the fitting coefficient a ranging from 3 to $5 \times 10^{-10} (\mu\text{m}^2/\text{Hz})$. Figure 4(c) shows a comparison of the PSD observed in large epigraphene devices, with noise reported for other types of graphene including exfoliated graphene, CVD-grown graphene, as well as graphene encapsulated by materials such as hBN, Ta_2O_5 , and Al_2O_3 (for the area normalized

comparison of PSDs see the supplementary material, Fig. S4). Remarkably, the noise PSD observed in our epigraphene devices is significantly lower compared to previously reported values in various graphene-based devices obtained from different sources.

Additionally, we demonstrate the implications of low noise in increasing the magnetic field sensitivity of Hall sensors, for which we arranged a parallel combination of sensors as shown in Fig. 5(a). At 20 kHz, the noise voltage measured in the parallel configuration of Hall sensors is about $1/\sqrt{2}$ that of the single device, resulting in a remarkable sensitivity of $B_{\text{min}} = V_N/(I_B R_H)$ ($\text{T}/\sqrt{\text{Hz}}$) = 9.5 nT/ $\sqrt{\text{Hz}}$, setting a record for low sensitivity for graphene-based Hall sensors operating at room temperature.

In summary, the efforts here demonstrated for epigraphene optimization in terms of material and devices open the route for truly scalable graphene electronics. The unprecedented low levels of noise measured in our devices result from a combination of optimized epitaxial growth at very high temperature $T > 1800^\circ\text{C}$, the formation of low resistance ohmic contacts to epigraphene, and the uniform doping and passivation of devices. The scalability of epigraphene technology enables

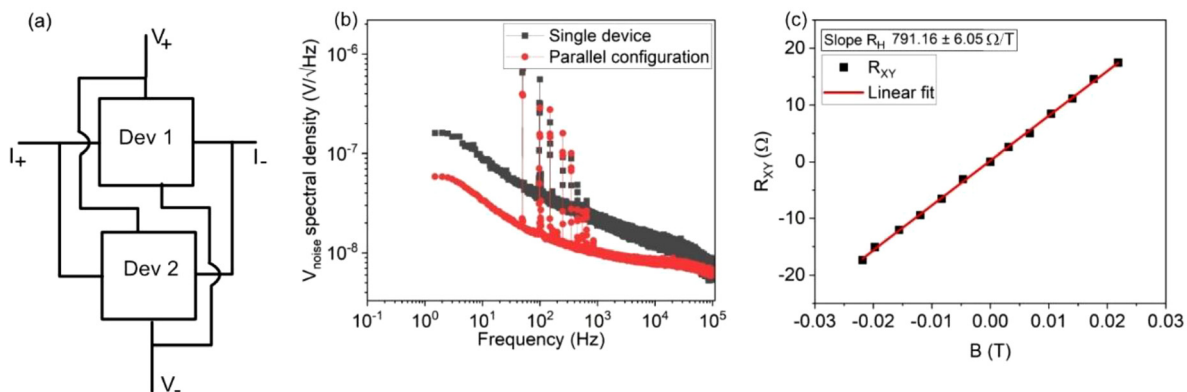


FIG. 5. (a) Parallel configuration of the two Hall sensors. (b) Comparison of noise voltage spectral density between single 1 mm Hall sensor and parallel configuration of two 1 mm Hall sensor devices as shown in (a). The voltage spectral density measured at $I = 400 \mu\text{A}$ for both single and parallel configurations. Both the devices are doped with molecular dopant F4TCNQ, to tune the Fermi level near to the Dirac point. (c) Hall measurement of the parallel setup. Slope here represents the Hall coefficient $R_H \approx 791 \Omega/\text{T}$. The data points are corrected for the thermal offset value.

us to design, fabricate, and test large area devices, a key requisite for increasing the complexity of future epigraphene electronics. Specifically, for Hall sensor devices, our results show that future array sensors could be a path toward decreasing the minimum detectable magnetic field that limits the Hall magnetometers. As a proof of concept, a simple parallel combination of sensors [Figs. 5(a)–5(c)] leads to a record low $B_{min} = V_N / (I_B R_H) = 9.5 \text{ nT}/\sqrt{\text{Hz}}$ for graphene Hall magnetometers operating at room temperature. We foresee that fabrication of arrays is a feasible route to push down the limits of B_{min} , scaling down as $\sim 1/\sqrt{k}$, with k the number of interconnected Hall sensors. Our finding is instrumental in guiding the design of sensors utilizing scalable material growth and fabrication techniques, with the aim of minimizing the impact of noise and enhancing the sensitivity of the devices.

See the supplementary material for information on cross correlated noise measurement setup, calculation of disorder potential, Hooge parameter, and area normalized comparison of noise PSDs.

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AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

Author Contributions

Naveen Shetty: Conceptualization (equal); Data curation (lead); Formal analysis (lead); Investigation (lead); Methodology (lead); Resources (equal); Software (lead); Visualization (equal); Writing – original draft (lead); Writing – review & editing (equal). **F. Chianese:** Resources (equal); Writing – review & editing (supporting). **H. He:** Formal analysis (supporting); Supervision (supporting); Writing – review & editing (equal). **J. Huhtasaari:** Resources (equal); Writing – review & editing (supporting). **S. Ghasemi:** Writing – review & editing (supporting). **K. Moth-Poulsen:** Formal analysis (equal); Writing – review & editing (equal). **S. Kubatkin:** Conceptualization (supporting); Investigation (supporting); Supervision (supporting); Validation (supporting); Writing – review & editing (equal). **T. Bauch:** Conceptualization (equal); Formal analysis (supporting); Investigation (supporting); Software (supporting); Supervision (supporting); Visualization (equal); Writing – review & editing (supporting). **S. Lara-Avila:** Conceptualization (lead); Funding acquisition (equal); Methodology (equal); Supervision (lead); Validation (lead); Visualization (lead); Writing – review & editing (equal).

DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding authors upon reasonable request.

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