	Date (2007)
	Hay & Full wave Voltage
	Rectifiers
	Aim: To build a full wave retifier using:
1.	Dhidal day ('LU L
2.	Bridge design Cwith 7805 regulator)
3.	Bridge rechier as a dual power supply without
	Bridge design Cwith 7805 regulator) Bridge rechbier as a dual power supply. (without originator)
4.	Bridge sectifier as a dual power supply with regulator
5.	Bridge sectifier as a dual power supply with regulator? Half wave rectifier using function generator.
	3 8
	Theory: Restifier circuits are used to obtain non-zero
	Theory: Restifier circuits are used to obtain non-zero DC components from an A.C input signal. They can be
	DI A 19 Mas
1.	Half wave rechtier: These circuits block the negative
	part of the input AC signal & returns only the positive component. Full wave rectifier: These circuits pass both halves of
	positive component.
2.	Full wave rectifier: These circuits pass both halves of
	the input wave one unhanged of the other invested.
	There can be of & types: Full wave center tapped rectifier circuit
2	Full wave center tapped rectifier circuit
6	Full wave bridge restifier circuit.
	O CONTRACTOR OF THE CONTRACTOR
	In a half wave rectifier circuit, during the positive
	half cycle, the diade is forward biased, due to
	which that half of the input waveform is not inhibited
	During the negative half eyels, the diode is revove biased
	In a half wave rectifier circuit, during the positive half cycle, the diade is forward biased, due to which that half of the input waveform is not inhibited. During the negative half cycle, the diade is revove biased. Due to high resistance during reverse bias condition, input signal is prohibited. In a hill wave bridge rectifier, in both half cycles.
	input signal is prohibited.
	In a full wave bridge rectifier, in both half cycles.

Expt. No. Date 3 0 0 1 1 d of the diodes are forward brased, so the input wave is fully rectified.

Adding a capacitor in parallel to the bridge allows for rectification reduction in supple voltage, according to the formula:

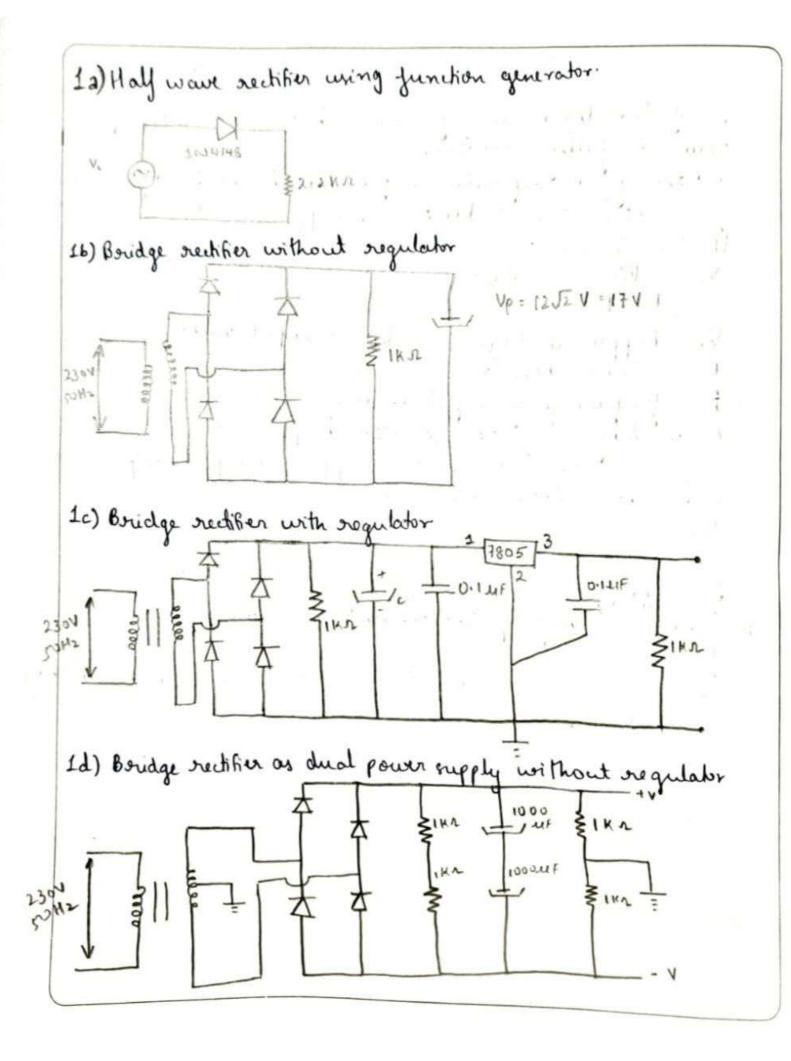
Vr = Vin where

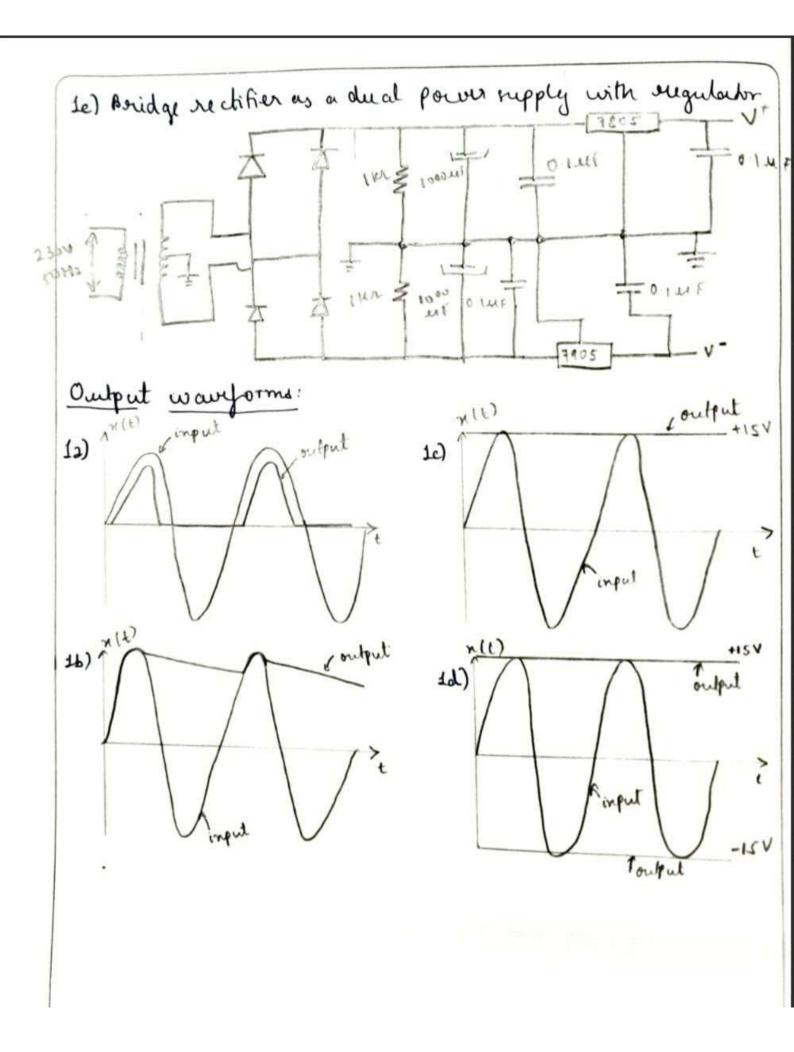
RLCF Vr = Ripple voltage Vin -> input voltage A voltage regulator is used to provide a stable output voltage without any ripples. The peak input voltage must be atteast 2V more than regulating voltage for the voltage regulator to Junction. origup the circuits as shown in the circuit 2. Plot output waveforms & measure plak to peak voltage

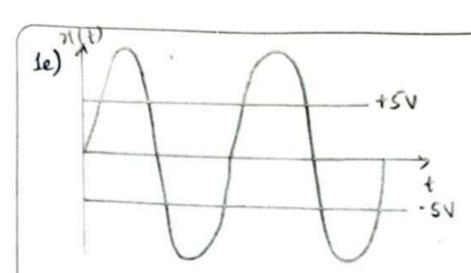
Calculations.

12) Power rating of resistor to be used:

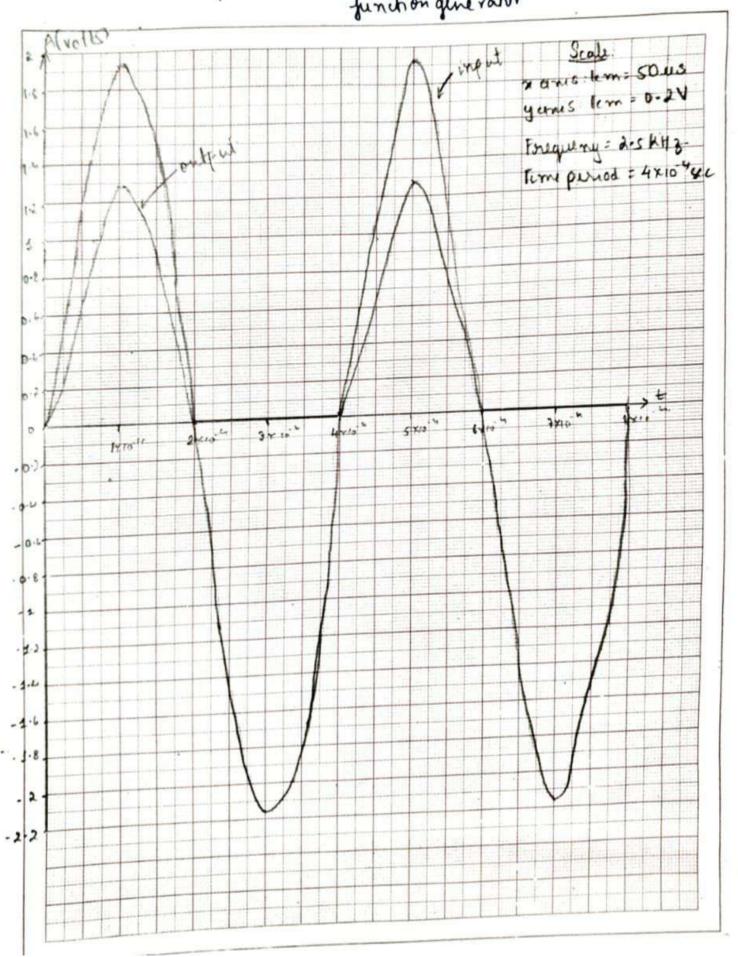
P=
$$\frac{V_{p^2}}{R}$$
, where:







Experiment 10-Half wave rechifier with function que vator



Experiment 16 - Bridge rectifier without regulator Meds) output Scale: xanis Irm = 25 x 10 ge yerris lem - 1.54 input frequency sons. Time period: 000 sc 0.04 (40) 0.03 0.035 008 0.025 0.015 0 04 0.005 -3 -6 -

Engeriment 3.c-Bridge sectifier with originator

