Deeb Armaly
ECE 4730
Lab 2
Cannons Algorithm

Below is the graph of speedup times given the number of processors compared to the sequential time:

Matrix Size	Serial Time(s)	Tasks	Parallel Time	SpeedUp
100	0.004024	4	0.0013	3.095
200	0.034153	4	0.009	3.795
400	0.34153	4	0.0747	4.572
600	1.214196	4	0.2751	4.414
300	0.12475	9	0.0149	8.372
600	1.214196	9	0.1163	10.440
900	4.784434	9	0.4159	11.504
400	0.3516	16	0.0551	6.381
800	2.8366	16	0.3242	8.750
1200	15.7188	16	1.1526	13.638