



* BEGIN *

**UMA11LSCEP15BDRLLN Library Tape Out Kit of
0.11 um Embedded E2PROM 2P8M Metal Metal
Capacitor Al Process for FAB8N**

(Ver. A01_P. B)

**DSM NO. : G-9LT-EE2PROM11-2P8M-MMC/AL/UMA11LSCEP15BDR
LLN-LIBRARY/TAPE_OUT_KIT-8N**

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2. Revision History

Ver.	Phase	Approved Date	From	To	Remark (Purpose)
A01	B	08/22/2016	-	-	Original

3. Design Rule Check

UMA11LSCEP15BDRLN_A01 TAPEOUTKIT.tar.gz

1. Cell List
2. Databook
3. Antenna Model
4. Antenna Model (tech)
5. Cadence Noise Model (.cdb.txt)
6. Cadence Noise Model prefix(*).cdb
7. Composer Symbol Library (cdsinfo.tag)
8. Composer Symbol Library (Cell List)
9. Composer Symbol Library (prop.xx)
10. EDIF Symbol
11. Fastscan ATPG Model
12. LEF (5.5) Files (for SOC Encounter)
13. LVS Spice Netlist
14. LVS Spice Netlist
15. Milkyway Technology Files (gds2Arcs.map)
16. Milkyway Technology Files (gdsInLayer.map)
17. Milkyway Technology Files (gdsOutLayer.map)
18. Milkyway Technology Files prefix(_metal).tf
19. Milkyway timing/cell/power/frame/milkyway view (.lock)
20. Milkyway time/cell/power/frame/milkyway view (Cell List)
21. Milkyway timing/cell/power/frame/milkyway view (lib)
22. Milkyway timing/cell/power/frame/milkyway view (lib_1)
23. Milkyway timing/cell/power/frame/milkyway view (lib_2)
24. Milkyway timing/cell/power/frame/milkyway view (lib_bck)
25. Milkyway time/cell/power/frame/milkyway view (Cell List)
26. Release Note
27. SOC Encounter Technology Files
28. Symbol
29. Synopsys Liberty Model/Synopsis Synthesis Symbol (lib|db)
30. Synopsys Liberty Model/Synopsis Synthesis Symbol (slib|sdb)
31. Verilog Simulation Model (pg sdf21)
32. Verilog Simulation Model (pg sdf30)
33. Verilog Simulation Model (sdf21)
34. Verilog Simulation Model (sdf30)
35. Virtuoso Symbol Library (.oalib)
36. Virtuoso Symbol Library (cdsinfo.tag)
37. Virtuoso Symbol Library (data.dm)
38. Virtuoso Symbol Library (Cell List)

- 39. Application Note
- 40. Physical Liberty(GDSII)
- 41. Spice Netlist with Interconnect RC

* END *