|  |  |  |
| --- | --- | --- |
| **ADDR** | **NAME** | **Description** |
| 0x00 | REG0 | for reset control |
| 0x01 | REG2 | 用于xx控制1 |
| 0x03 | REG3 | 用于xx控制1 |
| 0x1c | REG1C | 用于xx控制122 |
| 0x1d | REG1D | reg9edec |
| 0x1f | REG1F | reg9edec |
| 0x21 | REG21\_W1C | TRY W1C |
| 0x33 | STATUS\_BYTE | PMBUS STATUS BYTE |

REG0 (00h)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x0 | | 0x18 | | REG0 | | | | |
| for reset control | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | - | RO | RW | | | RW |
| **Default\_V** | 00 | | 0 | 1 | 100 | | | 0 |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:6 | field5a | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |
| 5 | reserved | | 保留 | | | | | |
| 4 | reg444ba | | 取得xx状态， 1： abb； 0：bbb | | | | | |
| 3:1 | reg3to2a | | reg3to2的对于数据\*3+1 | | | | | |
| 0 | reg00a | | 00reg3to2对于数据\*3+1 | | | | | |

REG2 (01h)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x1 | | 0x6 | | REG2 | | | | |
| 用于xx控制1 | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | - | RO | | RW | | RW |
| **Default\_V** | 00 | | 0 | 00 | | 11 | | 0 |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:6 | field5b | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |
| 5 | reserved | | 保留 | | | | | |
| 4:3 | reg444b | | 取得xx状态， 1： abb； 0：bbb | | | | | |
| 2:1 | reg3to2b | | reg3to2的对于数据\*3+1 | | | | | |
| 0 | reg00ab | | 00reg3to2的对于数据\*3+1 | | | | | |

REG3 (03h)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x3 | | 0x0 | | REG3 | | | | |
| 用于xx控制1 | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | W1C | | - | | W1C | - | | W1C |
| **Default\_V** | 00 | | 00 | | 0 | 00 | | 0 |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:6 | field5c | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |
| 5:4 | reserved | | 保留 | | | | | |
| 3 | reg444c | | 取得xx状态， 1： abb； 0：bbb | | | | | |
| 2:1 | reserved | | reg3to2的对于数据\*3+1 | | | | | |
| 0 | reg00c | | 00reg3to2的对于数据\*3+1 | | | | | |

REG1C (1Ch)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x1c | | 0x0 | | REG1C | | | | |
| 用于xx控制122 | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RO | | RW | | | | | |
| **Default\_V** | 00 | | 000000 | | | | | |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:6 | field5c12 | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |
| 5:0 | reg00c12 | | 00reg3to2的对于数据\*3+1 | | | | | |

REG1D (1Dh)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x1d | | 0x9e | | REG1D | | | | |
| reg9edec | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | |
| **Default\_V** | 10011110 | | | | | | | |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:0 | xx\_name | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |

REG1F (1Fh)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x1f | | 0x9e | | REG1F | | | | |
| reg9edec | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | |
| **Default\_V** | 10011110 | | | | | | | |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:0 | reg9e\_name2 | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |

REG21\_W1C (21h)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x21 | | 0x0 | | REG21\_W1C | | | | |
| TRY W1C | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | W1C | | | | | | | |
| **Default\_V** | 00000000 | | | | | | | |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7:0 | reg21w1c | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |

STATUS\_BYTE (33h)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x33 | | 0x18 | | STATUS\_BYTE | | | | |
| PMBUS STATUS BYTE | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | W1C | RO | RO | RO | RO | RO | RO | RO |
| **Default\_V** | 0 | 0 | 0 | 0 | 11 | 0 | 0 | 0 |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7 | busy | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | |
| 6 | off | | off state | | | | | |
| 5 | vout\_ov\_fault | | vout ov fault | | | | | |
| 4 | iout\_oc\_fault | | iout\_oc fault | | | | | |
| 3 | vin\_uv\_fault | | vin uv | | | | | |
| 2 | tempereature | | temp over | | | | | |
| 1 | cml | | cml error | | | | | |
| 0 | none\_of\_the\_above | | not above | | | | | |