|  |  |  |
| --- | --- | --- |
| **ADDR** | **NAME** | **Description** |
| 0x00 | REG0B | for reset control |
| 0x01 | REG2B | 用于xx控制1 |
| 0x02 | REG3B | 用于xx控制2 |
| 0x03 | REG4B | 用于xx控制3 |
| 0x04 | REG5B | 用于xx控制4 |
| 0x06 | REG6B | 用于xx控制6 |
| 0x07 | REG7B | 用于xx控制7 |

REG0B (00h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x0 | | | | 0x46 | | | | REG0B | | | | | | | | |
| for reset control | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | | | | - | RO | RW | | | RW |
| **Default\_V** | 0000000001 | | | | | | | | | | 0 | 0 | 011 | | | 0 |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:6 | ab | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |
| 5 | reserved | | | 保留 | | | | | | | | | | | | |
| 4 | reg444d | | | 取得xx状态， 1： abb； 0：bbb | | | | | | | | | | | | |
| 3:1 | reg3to2d | | | reg3to2的对于数据\*3+1 | | | | | | | | | | | | |
| 0 | reg00d | | | 00reg3to2的对于数据\*3+1 | | | | | | | | | | | | |

REG2B (01h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x1 | | | | 0x46 | | | | REG2B | | | | | | | | |
| 用于xx控制1 | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | | | | - | RO | RW | | | RW |
| **Default\_V** | 0000000001 | | | | | | | | | | 0 | 0 | 011 | | | 0 |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:6 | ac | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |
| 5 | reserved | | | 保留 | | | | | | | | | | | | |
| 4 | reg444e | | | 取得xx状态， 1： abb； 0：bbb | | | | | | | | | | | | |
| 3:1 | reg3to2e | | | reg3to2的对于数据\*3+1 | | | | | | | | | | | | |
| 0 | reg00e | | | 00reg3to2的对于数据\*3+1 | | | | | | | | | | | | |

REG3B (02h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x2 | | | | 0x40 | | | | REG3B | | | | | | | | |
| 用于xx控制2 | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | | | | - | | | RO | | RW |
| **Default\_V** | 0000000001 | | | | | | | | | | 000 | | | 00 | | 0 |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:6 | add | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |
| 5:3 | reserved | | | 保留 | | | | | | | | | | | | |
| 2:1 | reg444f | | | 取得xx状态， 1： abb； 0：bbb | | | | | | | | | | | | |
| 0 | reg00f | | | 00reg3to2的对于数据\*3+1 | | | | | | | | | | | | |

REG4B (03h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x3 | | | | 0x40 | | | | REG4B | | | | | | | | |
| 用于xx控制3 | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | | | | - | RO | | | | RW |
| **Default\_V** | 0000000001 | | | | | | | | | | 0 | 0000 | | | | 0 |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:6 | eaaee4 | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |
| 5 | reserved | | | 保留 | | | | | | | | | | | | |
| 4:1 | m2r4\_ro\_3b | | | 取得xx状态， 1： abb； 0：bbb | | | | | | | | | | | | |
| 0 | m2r4\_f0 | | | 00reg3to2的对于数据\*3+1 | | | | | | | | | | | | |

REG5B (04h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x4 | | | | 0xfb | | | | REG5B | | | | | | | | |
| 用于xx控制4 | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | | | | | | | | | |
| **Default\_V** | 0000000011111011 | | | | | | | | | | | | | | | |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:0 | rw0x5\_8b | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |

REG6B (06h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x6 | | | | 0x1 | | | | REG6B | | | | | | | | |
| 用于xx控制6 | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RO | | | | | | | | | | | | | | | |
| **Default\_V** | 0000000000000001 | | | | | | | | | | | | | | | |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:0 | ro0x6\_8b | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |

REG7B (07h)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | | | **Default Value** | | | | **Name** | | | | | | | | |
| 0x7 | | | | 0x1 | | | | REG7B | | | | | | | | |
| 用于xx控制7 | | | | | | | | | | | | | | | | |
| **Bit** | **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | | | | | | | | | | | | | | | |
| **Default\_V** | 0000000000000001 | | | | | | | | | | | | | | | |
| **Bit** | **Field Name** | | | **Description** | | | | | | | | | | | | |
| 15:0 | rw0x7\_8b | | | 描述寄存器内容5； 1. 开启， 0： 关闭 | | | | | | | | | | | | |