|  |  |  |
| --- | --- | --- |
| **ADDR** | **NAME** | **Description** |
| 2 |  |  |

**REG\_NAME**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Address** | | **Default Value** | | **Name** | | | | |
| 0x10 | | 0x10 | | REG\_NAME | | | | |
| Description ABCDEFG. | | | | | | | | |
| **Bit** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
| **Access** | RW | RW | RW | RW | RW | RW | RW | RO |
| **Default\_V** | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| **Bit** | **Bit name** | | **Description** | | | | | |
| 7 | B7abc | | Desc B7abc | | | | | |
| 6 | B6abc | | Desc B6abc | | | | | |
| 5 | B5abc | | Desc B5abc | | | | | |
| 4 | B4abc | | Desc B4abc | | | | | |
| 3 | B3abc | | Desc B3abc | | | | | |
| 2 | B2abc | | Desc B2abc | | | | | |
| 1 | B1abc | | Desc B1abc | | | | | |
| 0 | reserved | | Nothing. | | | | | |