

| FAMT. | P-60, P-60/1 | | | | | | 39. | | 100 | | |
|----------------------------|---|------------|-------------|-------------|----------|-----------|------------|-----------|---|-------|--|
| AWI | | | | | | VERSITY | Palanon | | | | |
| | Certificate showing | the Gra | ades o | btaine | d in ea | ch head o | passing l | y | 10.00 | 200 | |
| Straigh adar | | | | | | | | | 1 | 70.00 | |
| | SEAT NO : | | 36005 | | | | | | | | |
| NAME: | | | | | | | | | | | |
| S.E. ELE | ECTRONICS AND TELECOMMU | | | | | | CBCGS (P2) | 040 202 0 | | | |
| | EX | AMINAT | ION HE | LD IN N | OV / DE | C 2023 | CDCGG (RZ | 013 6 3 | CHEME | 1) | |
| | STATE OF TRANSPORTER | 1000 | | No. | 9-110-10 | Section 1 | | | | | |
| | CONTRACTOR OF FEB. | HEAD | Trans. | FIG | 11. | | 10 - | | GRADE | | |
| PAPER CODE | COURSE TITLE / PAPER NAME | PASSIN | MAX. | MIN. | Amil 8 | | - | CREDITS | 100000000000000000000000000000000000000 | | |
| ECC301 | Engineering Mathematics-III | ESE | MARKS 80 | MARKS 32 | 32 | MARKS | GRADES | (C) | (GP) | C*GP | |
| LITTLE WILL | g manifestation in | IA | 20 | 8 | 10 | | 1 | | 7111 | | |
| | tencer tipotechile | TOTAL | 100 | | 42 | | P | 3 | 4 | 12 | |
| ECC301 | Engineering Mathematics-III Lab | TW | 25 | 10 | 19 | | A | 1 | 9 | 9 | |
| ECC302 | Electronic Devices and | ESE | 80 | 32 | 33 | | - | | | | |
| | Circuits | IA | 20 | 8 | 11 | | | | | | |
| F00000 | | TOTAL | 100 | | 44 | | P | 3 | 4 | 12 | |
| ECC303 | Digital System Design | ESE | 80 20 | 32 | 32 | | 100 | | | | |
| | | TOTAL | 100 | 8 | 17 49 | | E | | | 40 | |
| ECC304 | Network Theory | ESE | 80 | 32 | 25 | | | 3 | 5 | 15 | |
| | | IA | 20 | 8 | 14 | | | | | | |
| | | TOTAL | 100 | | 39 | | E | 3 | 5 | 15 | |
| ECC304 ECC305 | Network Theory Lab Electronic Instrumentation | TW | 25 80 | 10 | 20 | | 0 | 1 | 10 | 10 | |
| 200303 | and Control System | IA | 20 | 32 8 | 36 13 | | | | | | |
| ENTERN | - John of Oyaldin | TOTAL | 100 | | 49 | | E | 3 | 5 | 15 | |
| ECL301 | Electronic Devices and | TW | 25 | 10 | 21 | | | | | | |
| | Circuits Lab | POE | 25 | 10 | 19 | | | | | | |
| | | TOTAL | 50 | KILLIO | 40 | | 0 | 1 | 10 | 10 | |
| ECL302 | Digital System Design Lab | TW | 25 | 10 | 20 | | 0 | 1 | 10 | 10 | |
| | Electronic Instrumentation | | 88 | | | | | | | | |
| ECL303 | and Control System Lab | TW | 25 | 10 | 21 | | 0 | 1. | 10 | 10 | |
| ECL304 | Skill Lab - C++ and Java | TW | 25 | 10 | 19 | | | 7 | | | |
| | Programming | POE | 25 | 10 | 21 | | | 1 | luci. | | |
| O-THEORY | | TOTAL | 50 | S CANCELL | 40 | | 0 | 2 | 10 | 20 | |
| ECM301 | Mini Project 1 A | TW | 25 | | 21 | | | | | | |
| | Marie Control of the | POE | 25 50 | | 20 41 | | 0 | 2 | 10 | 20 | |
| TOTAL | | TIOTAL | 750 | | 424 | | | 24 | 17 | 158 | |
| SECTION. | The state of the state of | 102 | 900 | - 9 | | 0.5045 | | | | | |
| 200000200200 | Remark: | Succes | | | SGPI: | 6.58 | | CGPI: | ** | | |
| Result Declare | | 17-02-2 | 024. | | | | | | | | |
| Result Amend Ratnagiri. | ed Off | 1 | | | | | | | | | |
| | A1 | 6 | | | ٨ | | | KV | 4 | | |
| CPM- | | P | | | 5 | | | X | | | |
| Entered By | | Checked By | | | Read By | | | Principal | | | |



Hope Foundation's

FINOLEX ACADEMY OF MANAGEMENT AND TECHNOL



P-60, P-60/1, MIDC, Mirjole Block, Ratnagiri - 415 639.

ON BEHALF OF MUMBAI UNIVERSITY

Certificate showing the Grades obtained in each head of passing by



SEAT NO:

460076

NAME:

PATKAR VINIT VIJAY VINAYA

S.E. ELECTRONICS AND TELECOMMUNICATION ENGINEERING SEMESTER - IV(CBCGS (R2019 °C" scheme)) **EXAMINATION HELD IN MAY / JUNE 2024**

| COURSE CODE PAPER CODE | COURSE TITLE / PAPER NAME | HEAD OF PASSIN G | MAX. | MIN. MARKS | MARKS | GRADES | CREDITS | GRADE POINTS (GP) | C+GP |
|---------------------------|--|---------------------------|------|----------------|-------|--------|---------|-------------------------|---------|
| ECC401 | Engineering Mathematics-IV | ESE | 80 | 32 | 40 E | - | 151 | 100 | 4 01 |
| | | IA | 20 | 8 | 13 E | | | | |
| | | TOTAL | 100 | 300 | 53 | D | 3 | 6 | 18 |
| ECC401 | Engineering Mathematics-IV | TW | 25 | 10 | 19 E | A | 1 | 9 | 9 |
| ECC402 | Microcontrollers | ESE | 80 | 32 | 33 E | 1000 | | | |
| | 100000000000000000000000000000000000000 | IA | 20 | 100 | 12 E | | | | |
| | | TOTAL | 100 | | 45 | E | . 3 | 5 | 15 |
| ECC403 | Linear Integrated Circuits | ESE | 80 | 32 | 46 E | - | - | - | 10 |
| | | IA | 20 | 8 | 12 E | | | | |
| | STATE OF THE REAL PROPERTY. | TOTAL | 100 | | 58 | D | 3 | 6 | 18 |
| ECC404 | Signals & Systems | ESE | 80 | 32 | 3.F | | | | 1 |
| | Service Control of the Control of th | IA | 20 | 6 | 12 E | | | | |
| | | TOTAL | 100 | long- | 15 | 100 | *** | + | OU-mail |
| ECC404 | Signals & Systems | TW | 25 | | 21 E | 0 | 1 | 10 | 10 |
| ECC405 | Principles of Communication | 10000 | 80 | 13000 | 34 E | | | - | |
| | Engineering | IA | 20 | 8 | 12 E | | | | |
| | | TOTAL | 100 | | 46 | E | 3 | 5 | 15 |
| ECL401 | Microcontrollers Lab | TW | 25 | | 19 E | A | 1 | 9 | 9 |
| ECL402 | Linear Integrated Circuits | TW | 25 | | 20 E | | | | |
| | Lab | POE | 25 | Comment of the | 19 E | | 700 | | |
| | | TOTAL | W 50 | | 39 | A | 1 | 9 | 9 |
| ECL403 | Principles of Communication | - 150 Clark | 25 | 10 | 20 E | | 4 | | |
| | Engineering Lab | POE | 25 | 10 | 18 E | 1000 | | | |
| | 00 W 1 1 10 W 1 | TOTAL | 50 | | 38 | A | - 3.1 | 9 | 9 |
| ECL404 | Skill Lab: Python | TW | 25 | 10 | 22 E | | | | |
| | Programming | POE | 25 | 10 | 12 E | | 79.11 | 142 | 0333 |
| ECM401 | William Land | TOTAL | 50 | 40 | 34 | С | 2 | 7 | 14 |
| | Mini Project 1B | TW | 25 | 10 | 19 E | | | | |
| | | POE | 25 | 10 | 18 E | | - | | |
| TOTAL | | TOTAL | 50 | | 37 | 8 | 2 | 8 | 16 |
| VIAL | | | 775 | - | 424 | | 21 | | 142 |

Unsuccessful 17-08-2024

Result Declared On: Result Amended On

Checked By

Read By

Principal

NA NOT APPLICABLE AB - ASSENT / - FEMALE SGPI - 2/C + G)

3000882

