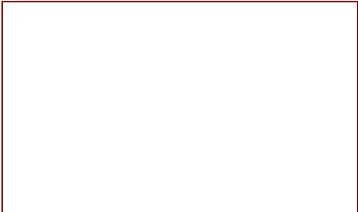


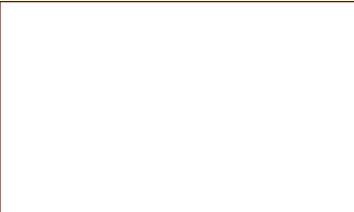
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|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | 5 | 6 |
| A |   |   |   |   |   |
| B |   |   |   |   |   |
| C |   |   |   |   |   |
| D |   |   |   |   |   |
| 1 | 2 | 3 | 4 | 5 | 6 |

SSD2828



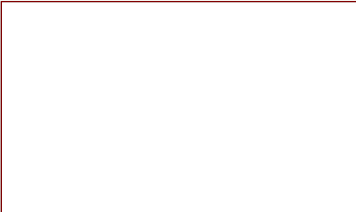
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LH154Q01



File: LH154Q01.kicad\_sch

VS035ZSM



File: VS035ZSM.kicad\_sch

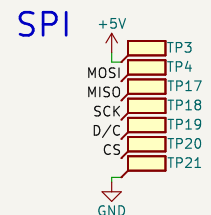
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| Size: A4                      | Date: | Rev:    |
| KiCad E.D.A. kicad 7.0.1      |       | Id: 1/4 |

J3  
Conn\_01x34

34  
33  
32  
31  
30  
29 RGB\_B7  
28 RGB\_B6  
27 RGB\_B5  
26 RGB\_B4  
25 RGB\_B3  
24 RGB\_B2  
23 RGB\_B1  
22 RGB\_B0  
21 RGB\_G7  
20 RGB\_G6  
19 RGB\_G5  
18 RGB\_G4  
17 RGB\_G3  
16 RGB\_G2  
15 RGB\_G1  
14 RGB\_G0  
13 RGB\_R7  
12 RGB\_R6  
11 RGB\_R5  
10 RGB\_R4  
9 RGB\_R3  
8 RGB\_R2  
7 RGB\_R1  
6 RGB\_R0  
5 RGB\_VSYNC  
4 RGB\_EN  
3 RGB\_HSYNC  
2 RGB\_CLK  
1

GND

GND



RGB\_CLK<sup>30</sup> PCLK

RGB\_EN<sup>28</sup> DEN

RGB\_HS<sup>29</sup> HSYNC

RGB\_VS<sup>34</sup> VSYNC

RGB\_R0<sup>35</sup> DATA0

RGB\_R1<sup>36</sup> DATA1

RGB\_R2<sup>37</sup> DATA2

RGB\_R3<sup>38</sup> DATA3

RGB\_R4<sup>39</sup> DATA4

RGB\_R5<sup>40</sup> DATA5

RGB\_R6<sup>41</sup> DATA6

RGB\_R7<sup>42</sup> DATA7

RGB\_G0<sup>43</sup> DATA8

RGB\_G1<sup>44</sup> DATA9

RGB\_G2<sup>45</sup> DATA10

RGB\_G3<sup>46</sup> DATA11

RGB\_G4<sup>47</sup> DATA12

RGB\_G5<sup>48</sup> DATA13

RGB\_G6<sup>49</sup> DATA14

RGB\_G7<sup>50</sup> DATA15

RGB\_B0<sup>51</sup> DATA16

RGB\_B1<sup>52</sup> DATA17

RGB\_B2<sup>53</sup> DATA18

RGB\_B3<sup>54</sup> DATA19

RGB\_B4<sup>55</sup> DATA20

RGB\_B5<sup>56</sup> DATA21

RGB\_B6<sup>57</sup> DATA22

RGB\_B7<sup>58</sup> DATA23

EP<sup>69</sup> NC

NC<sup>70</sup> NC

NC<sup>71</sup> NC

PS1<sup>72</sup> PS0

PS0<sup>73</sup> PS0

MVDD<sup>15</sup> MVDD

MVDD<sup>51</sup> MVDD

MVDD<sup>60</sup> MVDD

MVDD<sup>66</sup> MVDD

VDDIO<sup>16</sup> VDDIO

VDDIO<sup>33</sup> VDDIO

VDDIO<sup>64</sup> VDDIO

VDDIO<sup>68</sup> VDDIO

U1  
SSD2828QN4

DATA0<sup>1</sup> MIPI\_DATA0

DATA0<sup>2</sup> MIPI\_DATA0

DATA1<sup>4</sup> MIPI\_DATA1

DATA1<sup>5</sup> MIPI\_DATA1

DATA2<sup>10</sup> MIPI\_DATA2

DATA2<sup>11</sup> MIPI\_DATA2

DATA3<sup>13</sup> MIPI\_DATA3

DATA3<sup>14</sup> MIPI\_DATA3

CLKP<sup>7</sup> MIPI\_CLK\_P

CLKN<sup>8</sup> MIPI\_CLK\_N

TX\_CLK\_XIO<sup>61</sup> TX\_CLK\_XIO

TX\_CLK\_XIN<sup>63</sup> TX\_CLK\_XIN

SYN\_CLK\_OUT<sup>61</sup> SYN\_CLK\_OUT

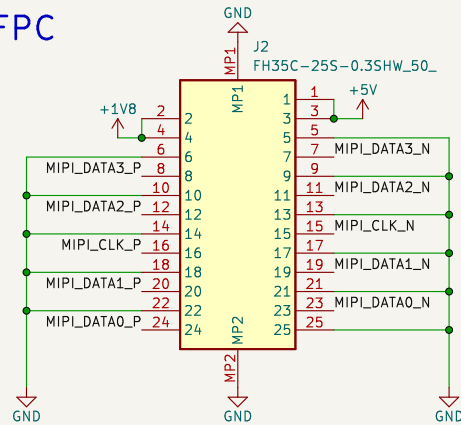


Pin connection diagram for the Raspberry Pi 40-pin header. The diagram shows the connection of various pins to a central component. Pins 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, and 25 are connected to a +5V supply. Pins 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, and 24 are connected to a +1V8 supply. Pins 11 and 25 are also connected to GND. The central component has pins labeled MP1, MP2, and MP3. The diagram is labeled "Raspberry Pi 40-pin header" and "Pin connection diagram".

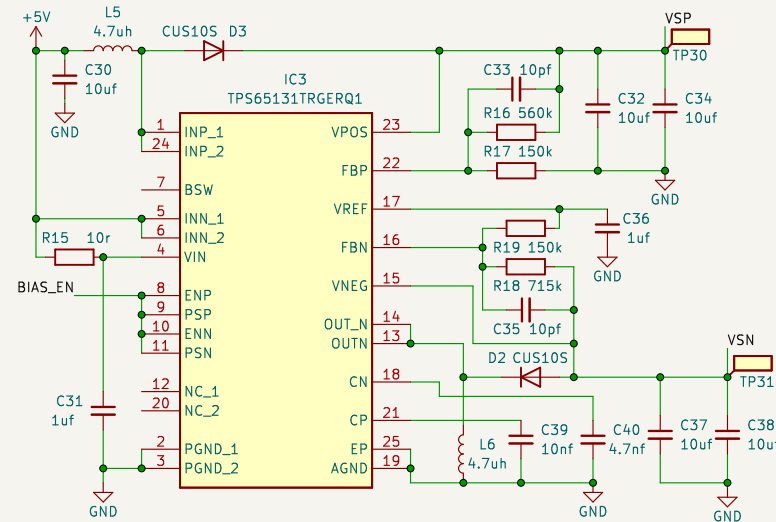
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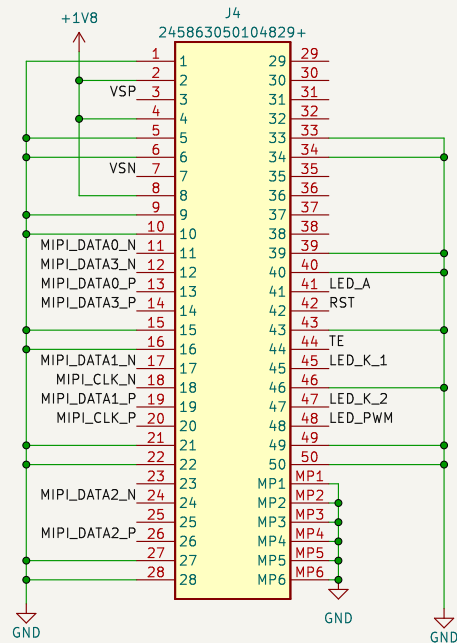
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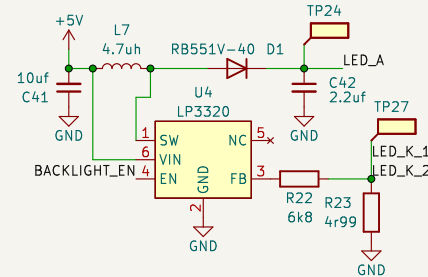
## LCD BIAS



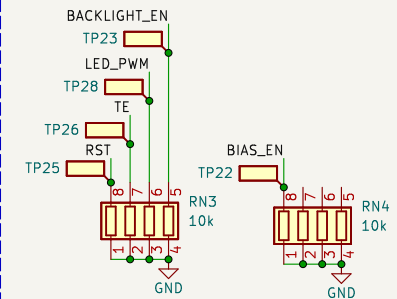
## DISP MEZZANINE



## BACKLIGHT DRIVER



## TEST POINTS



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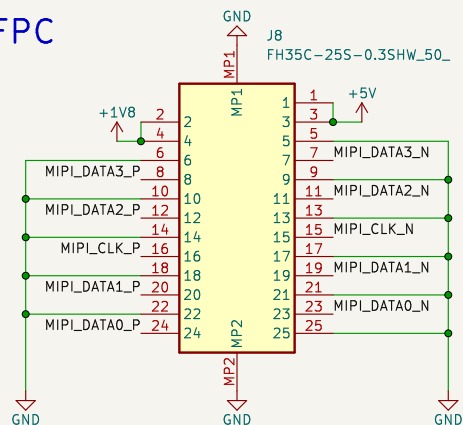
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KiCad E.D.A. kicad 7.0.1

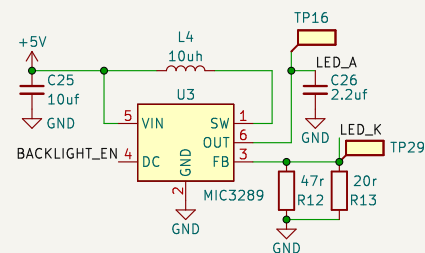
Date:

**Rev:**  
Id: 3/4

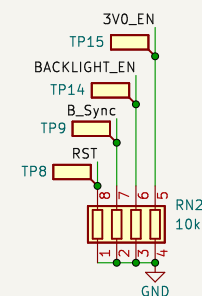
MIPI FPC



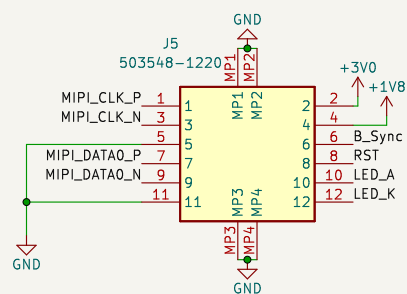
## BACKLIGHT DRIVER



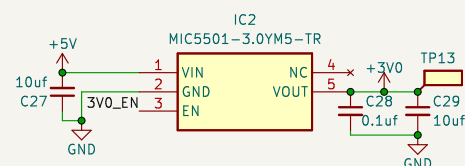
## TEST POINTS



DISP MEZZANINE



## 3V0 BUCK



Sheet:  
File: LH154Q01.kicad\_sch

**Title:**

|                          |       |
|--------------------------|-------|
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| KiCad E.D.A. kicad 7.0.1 |       |

Rev:  
Id: 4/4