

	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

Sheet: DRAM\_Back

Sheet: DRAM\_Front

File: DRAM\_Back.sch

File: DRAM\_Front.sch

Sheet: TopN64Bus

File: TopN64Bus.sch

Sheet: SCSI

File: SCSI.sch

Sheet: MCU

File: MCU.sch

Sheet: FPGA

File: FPGA.sch

Sheet: BottomN64Bus

File: BottomN64Bus.sch

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Sheet: /

File: N64DEV.sch

Title: Rare N64DEV V3 – Overview

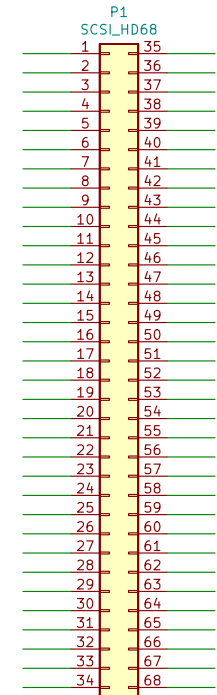
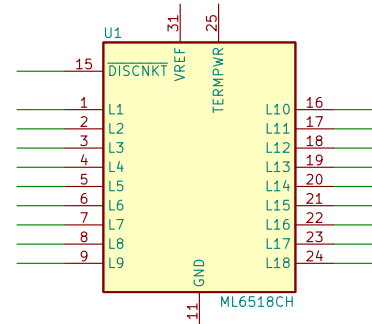
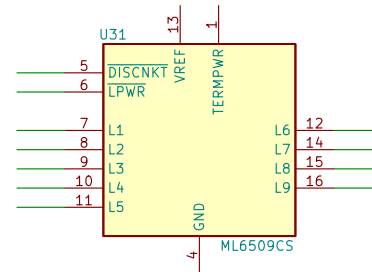
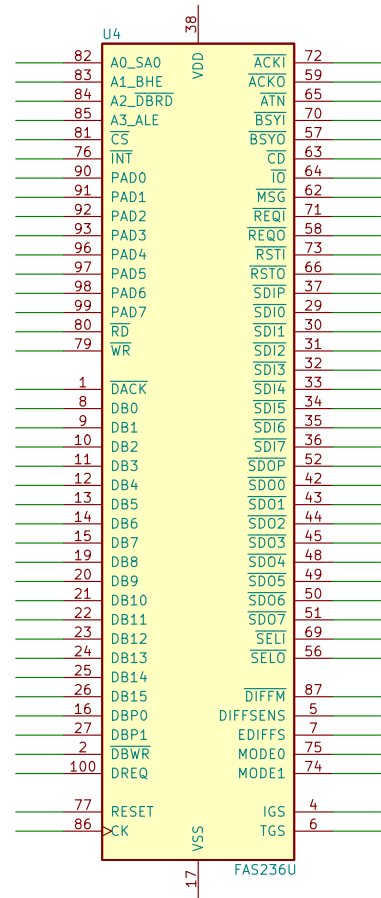
Size: A4

Date: 2021-12-30

Rev: v1.0

KiCad E.D.A. kicad (5.1.9)-1

Id: 1/8



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Sheet: /SCSI/

File: SCSI.sch

**Title: Rare N64DEV V3 – SCSI Interface**

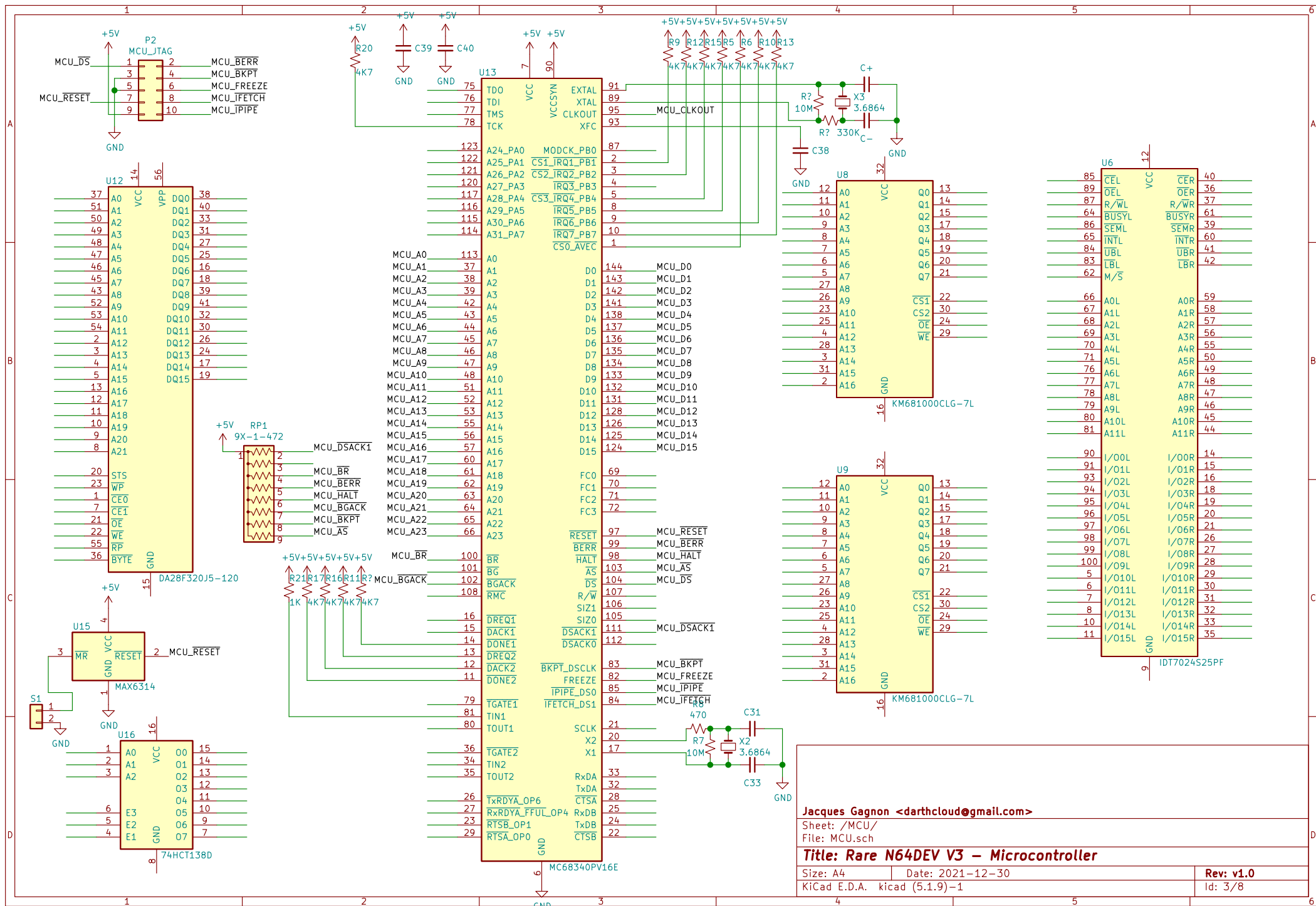
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Date: 2021-12-30

Rev: v1.0

KiCad E.D.A. kicad (5.1.9)-1

Id: 2/8



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Sheet: /MCU/

File: MCU.sch

### Title: Rare N64DEV V3 – Microcontroller

Size: A4

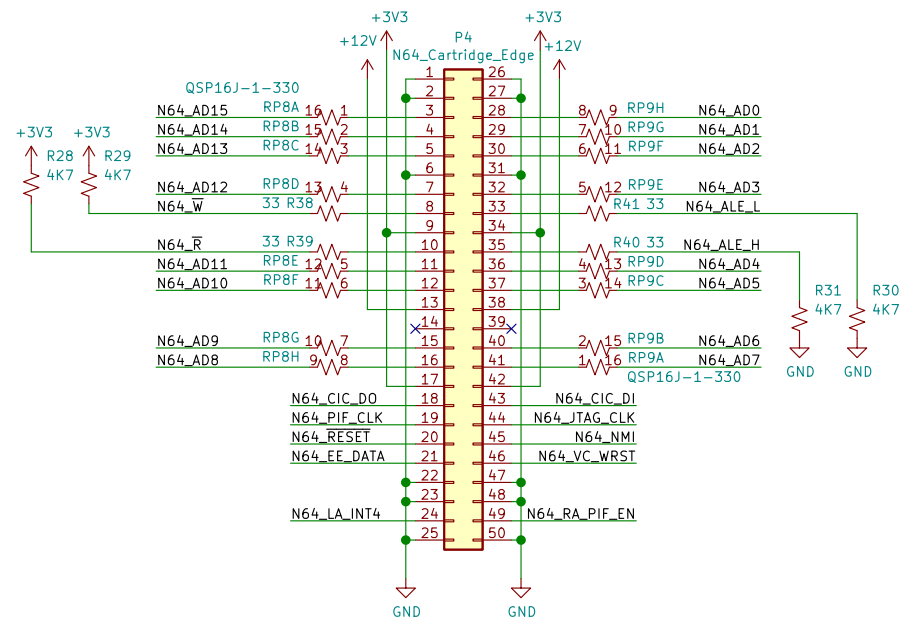
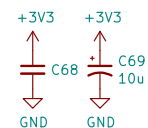
Date: 2021-12-30

Rev: v1.0

KiCad E.D.A. kicad (5.1.9)-1

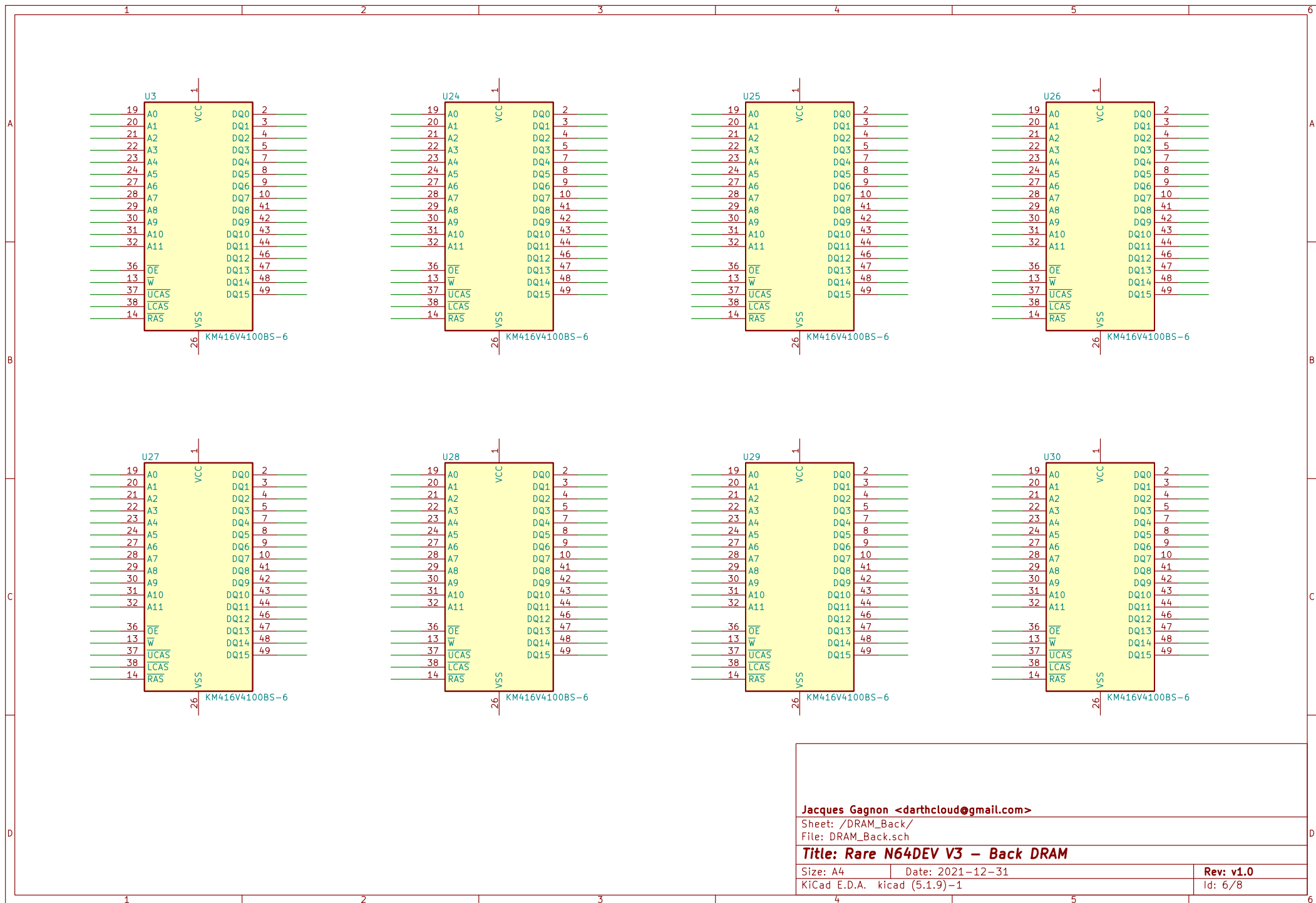
Id: 3/8





LA, RA, VC likely KMC equivalent signal (same pinout?)

Id: 5/8



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Sheet: /DRAM\_Back/

File: DRAM\_Back.sch

**Title: Rare N64DEV V3 – Back DRAM**

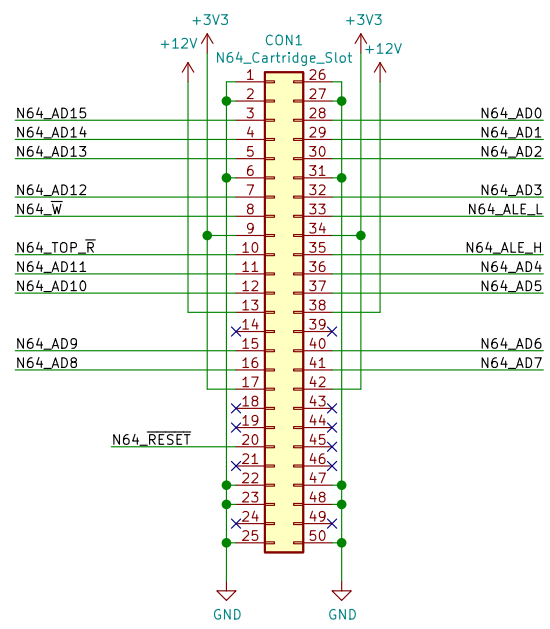
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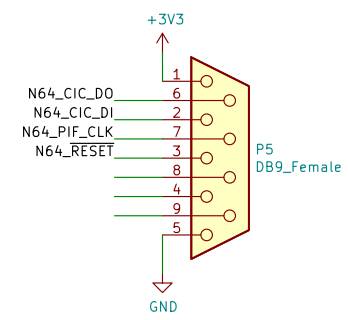
Rev: v1.0

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Id: 6/8



Cartridge label facing up (unlike Partner)  
Only wired for SRAM & Flash save



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Sheet: /TopN64Bus/  
File: TopN64Bus.sch

**Title: Rare N64DEV V3 – Top N64 Bus**

Size: A4	Date: 2021-12-30	Rev: v1.0
KiCad E.D.A. kicad (5.1.9)-1		Id: 7/8

