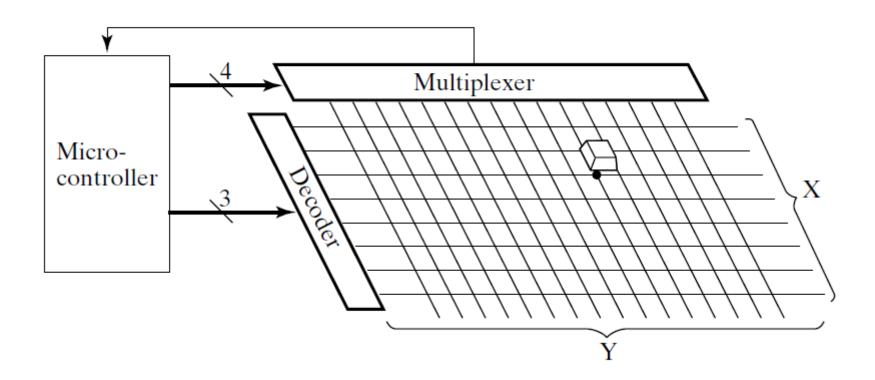
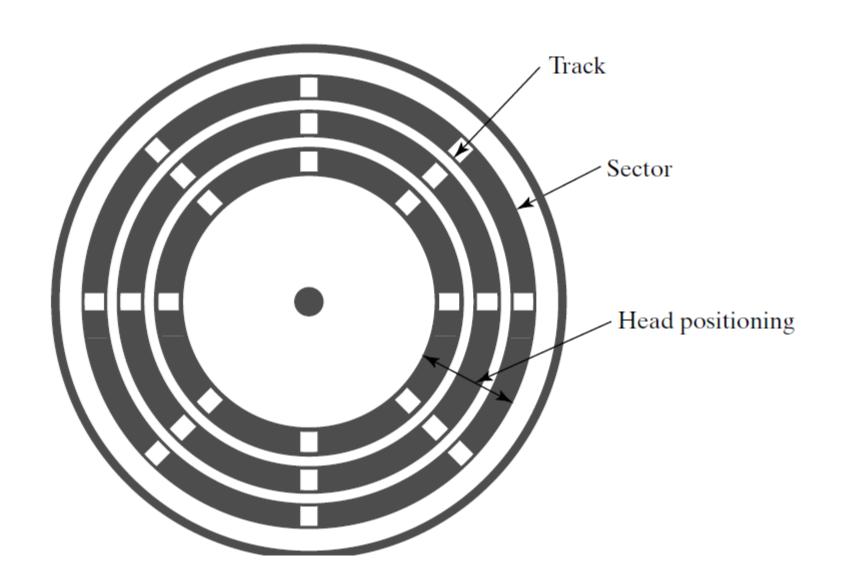
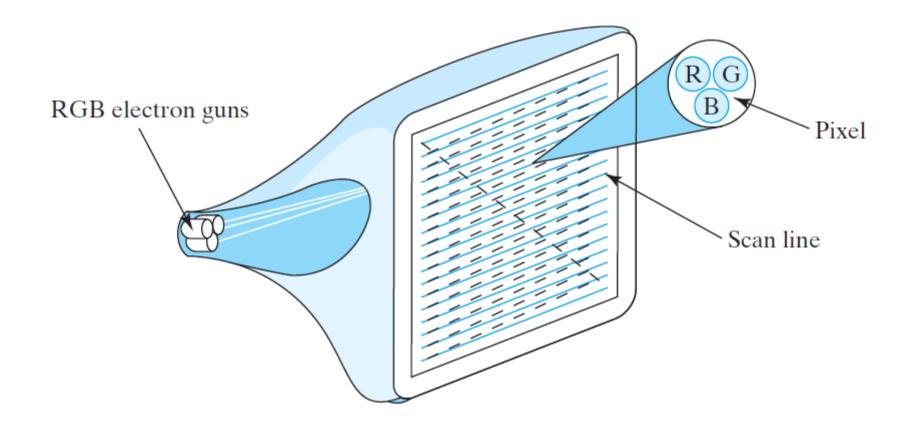
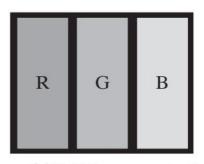
## Input- Output

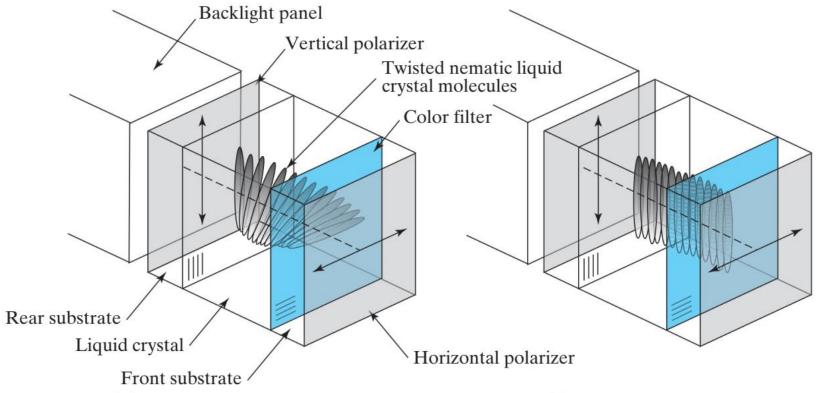






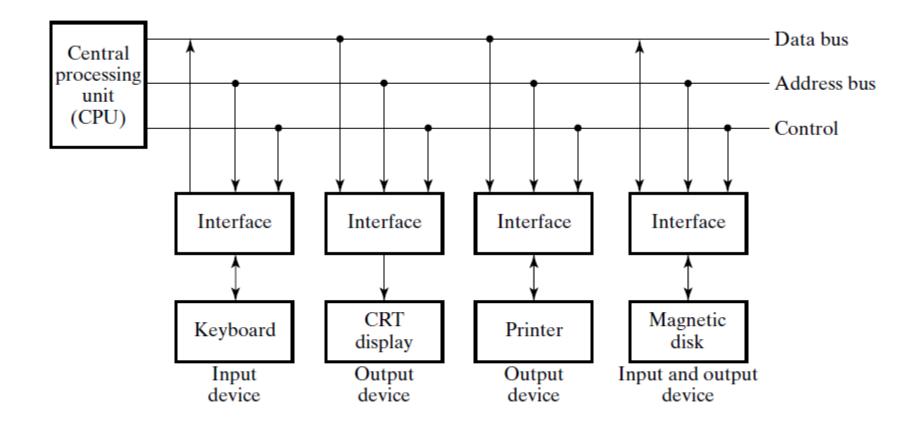


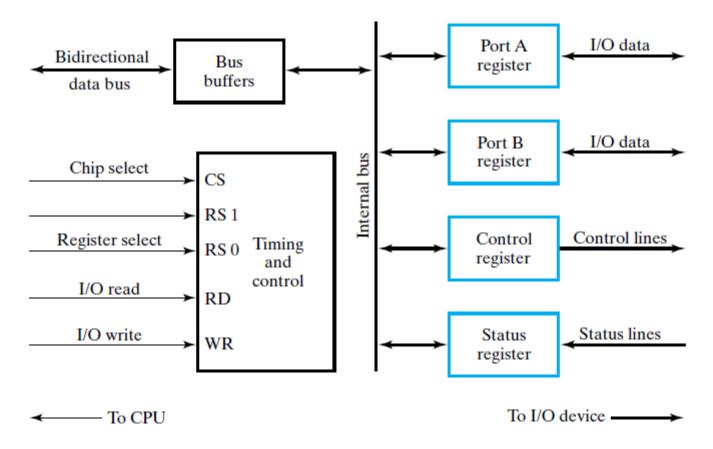
(a) LCD screen pixel



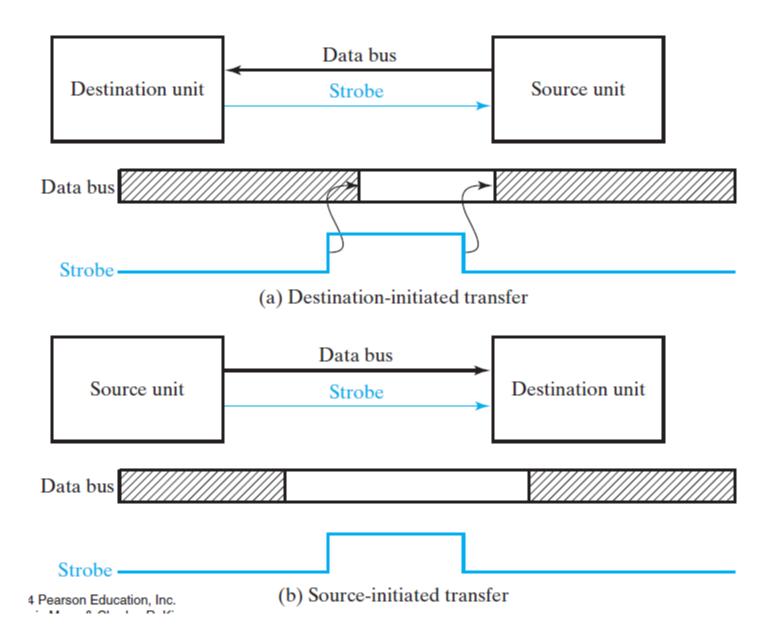
(b) Twisted nematic LCD technology

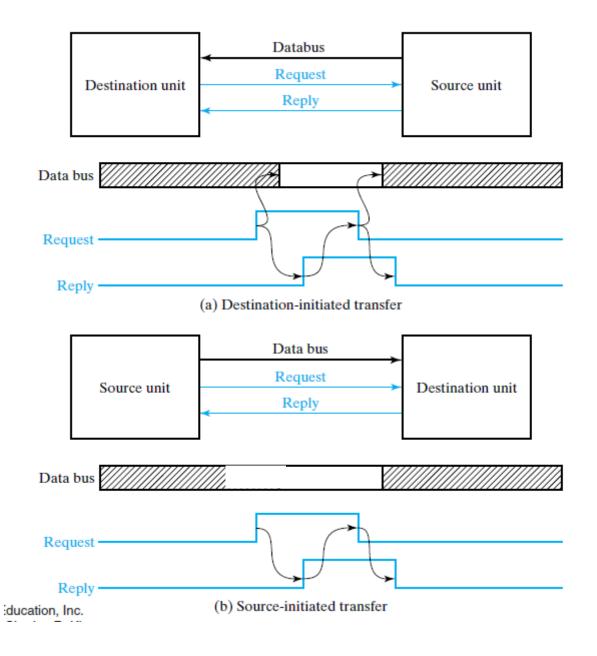
(c) Twisted nematic LCD with maximum voltage applied

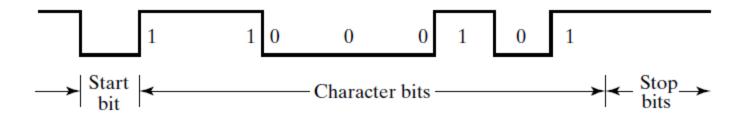


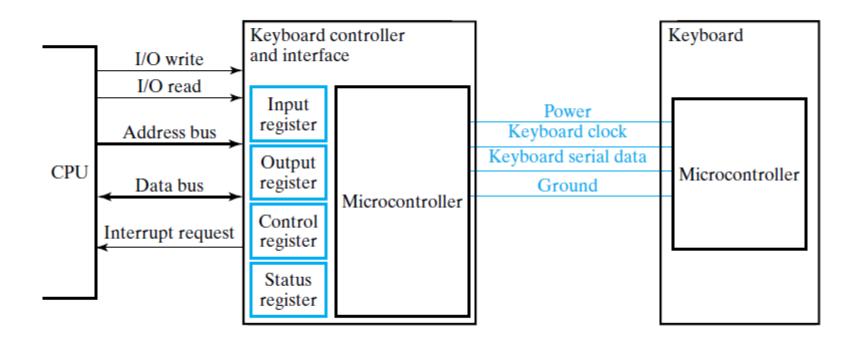


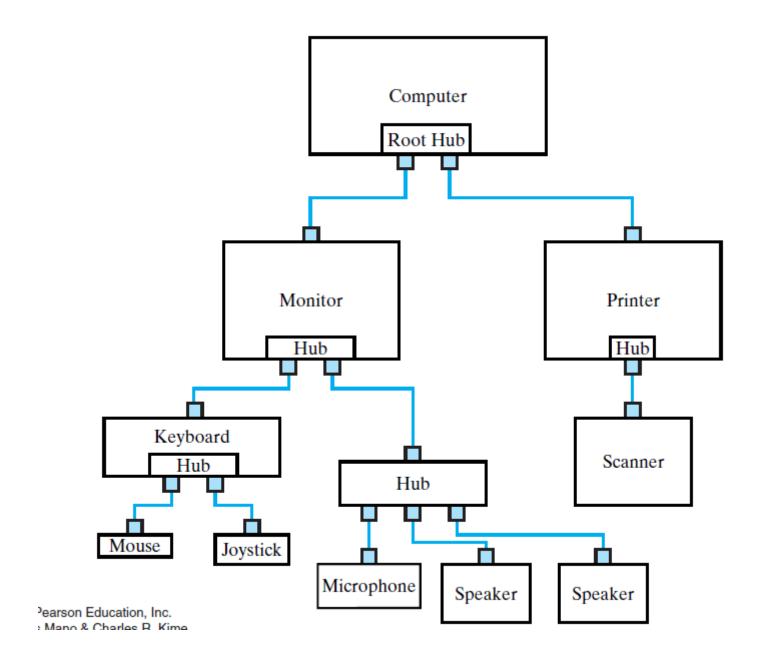
CS	RS1	RS0	Register selected		
0	X	X	None: data bus in high-impedance state		
1	0	0	Port A register		
1	0	1	Port B register		
1	1	0	Control register		
1	1	1	Status register		

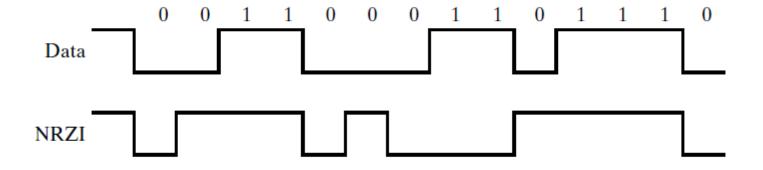






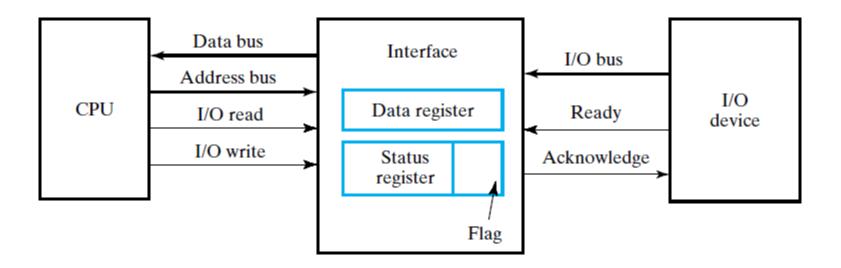


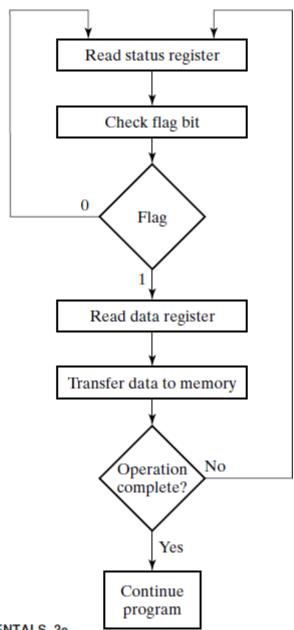




SYNC	PID	Pa	CRC	EOP							
(a) General packet format											
SYNC 8 bits	Type 4 bits 1001	Check 4 bits 0110	Device Addres 7 bits	s A	ndpoint ddress 4 bits	CRC	ЕОР				
(b) Output packet											
SYNC 8 bits	Type 4 bits 1100	Check 4 bits 0011	Data (Up to 1024 bytes)			CRC	ЕОР				
(c) Data packet (Data0 type)											
		SYNC 8 bits	Type 4 bits 0100	Check 4 bits 1011	ЕОР						

(d) Handshake packet (Acknowledge type)





MENTALS, 3e

