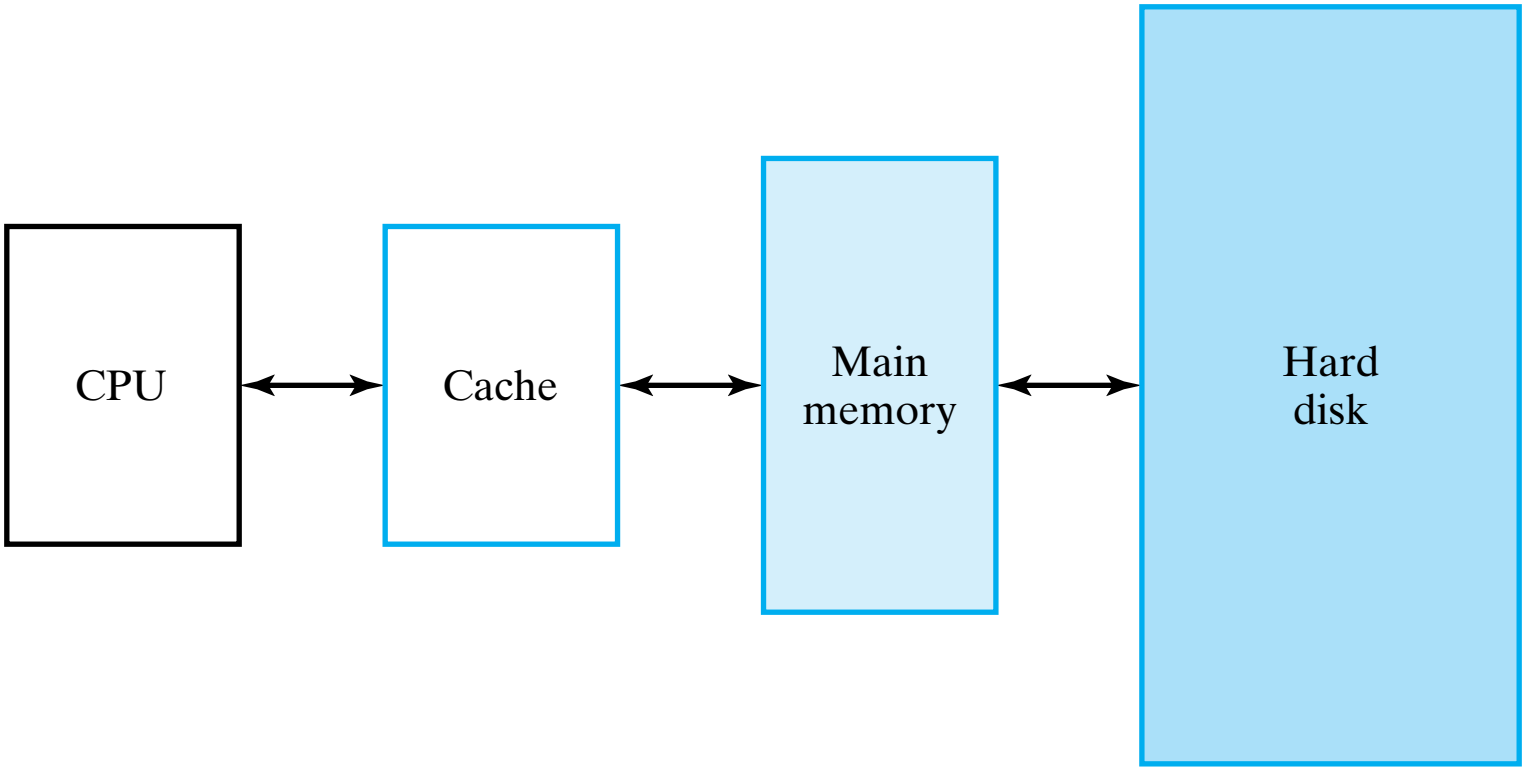
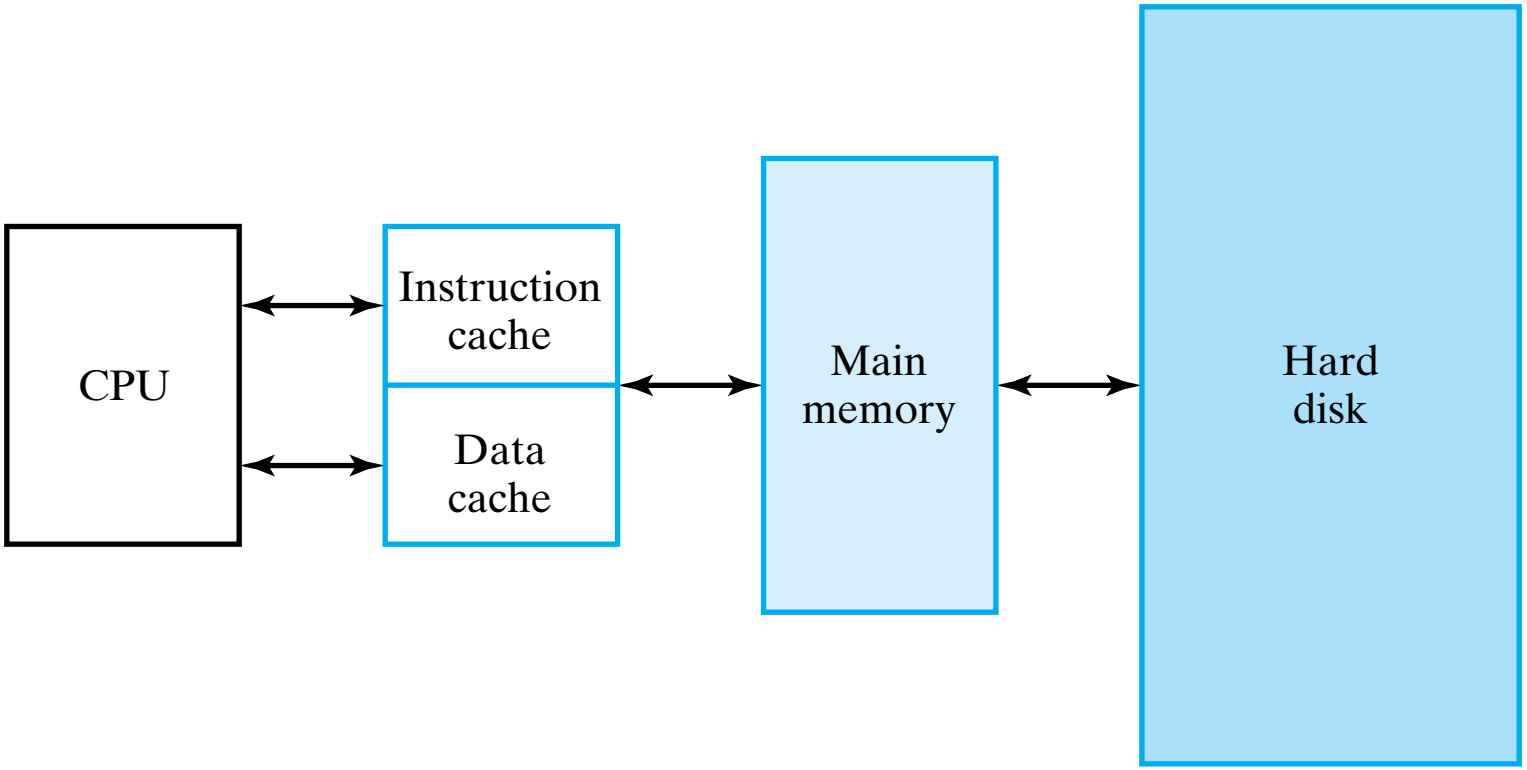
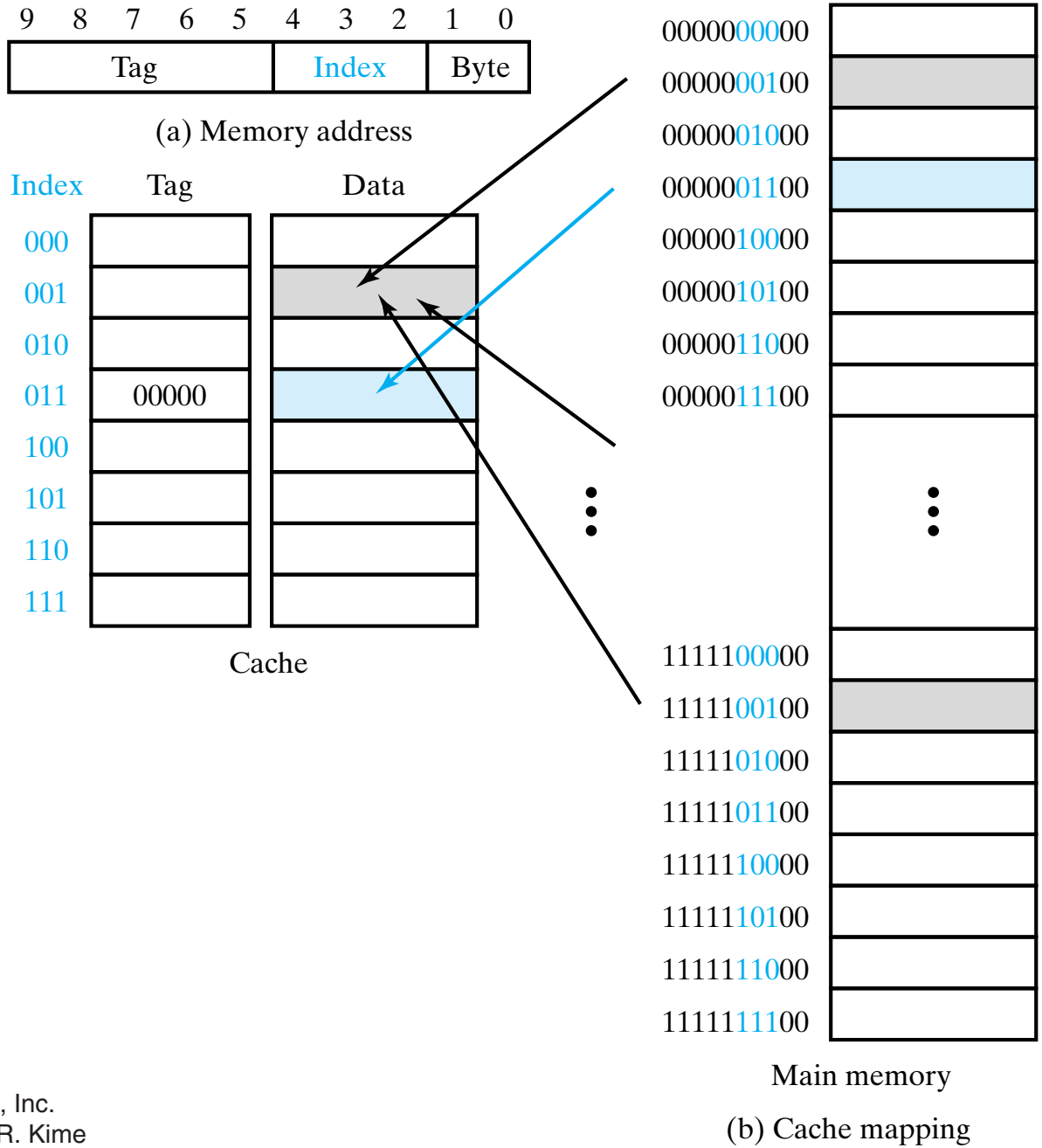
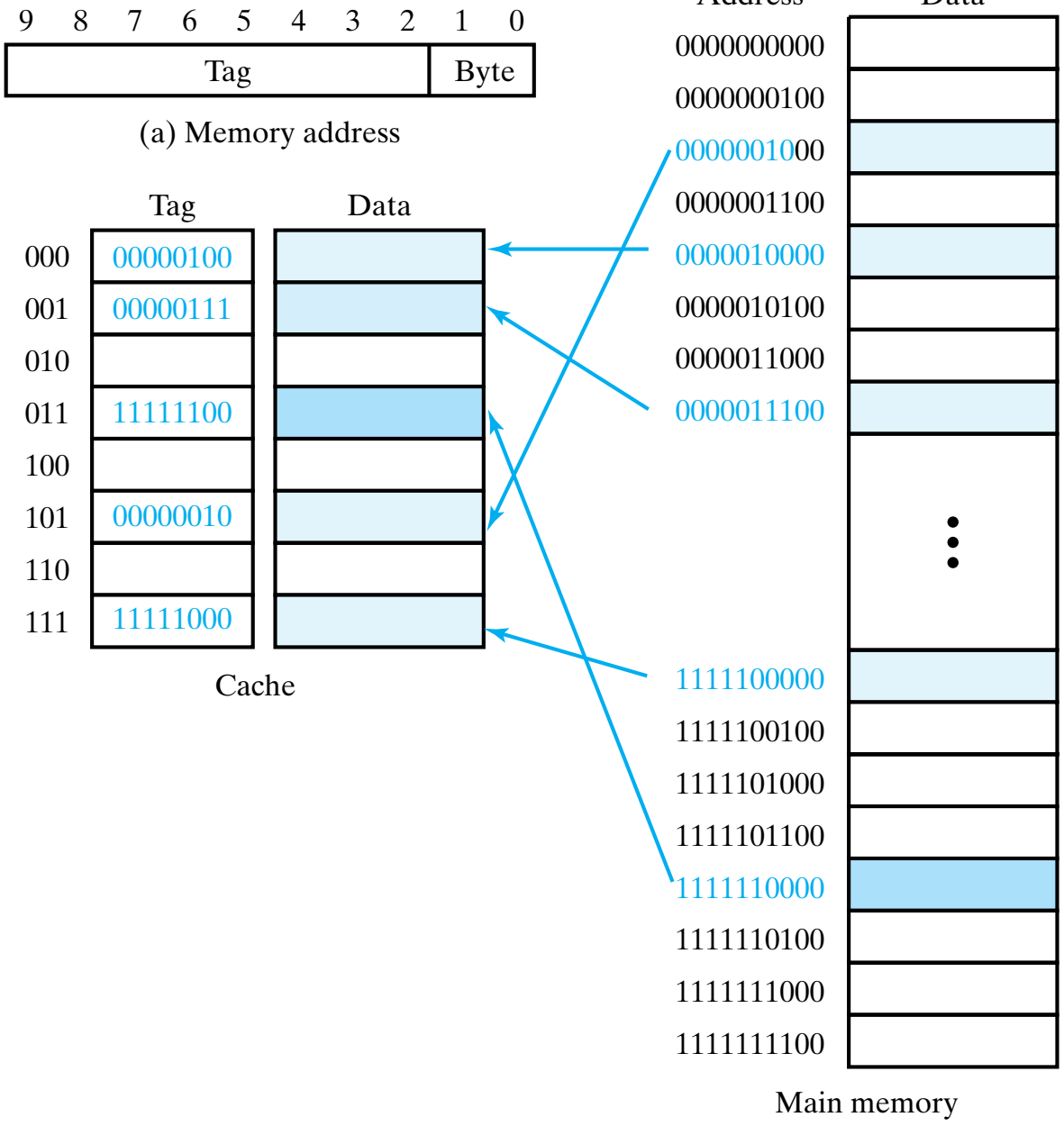
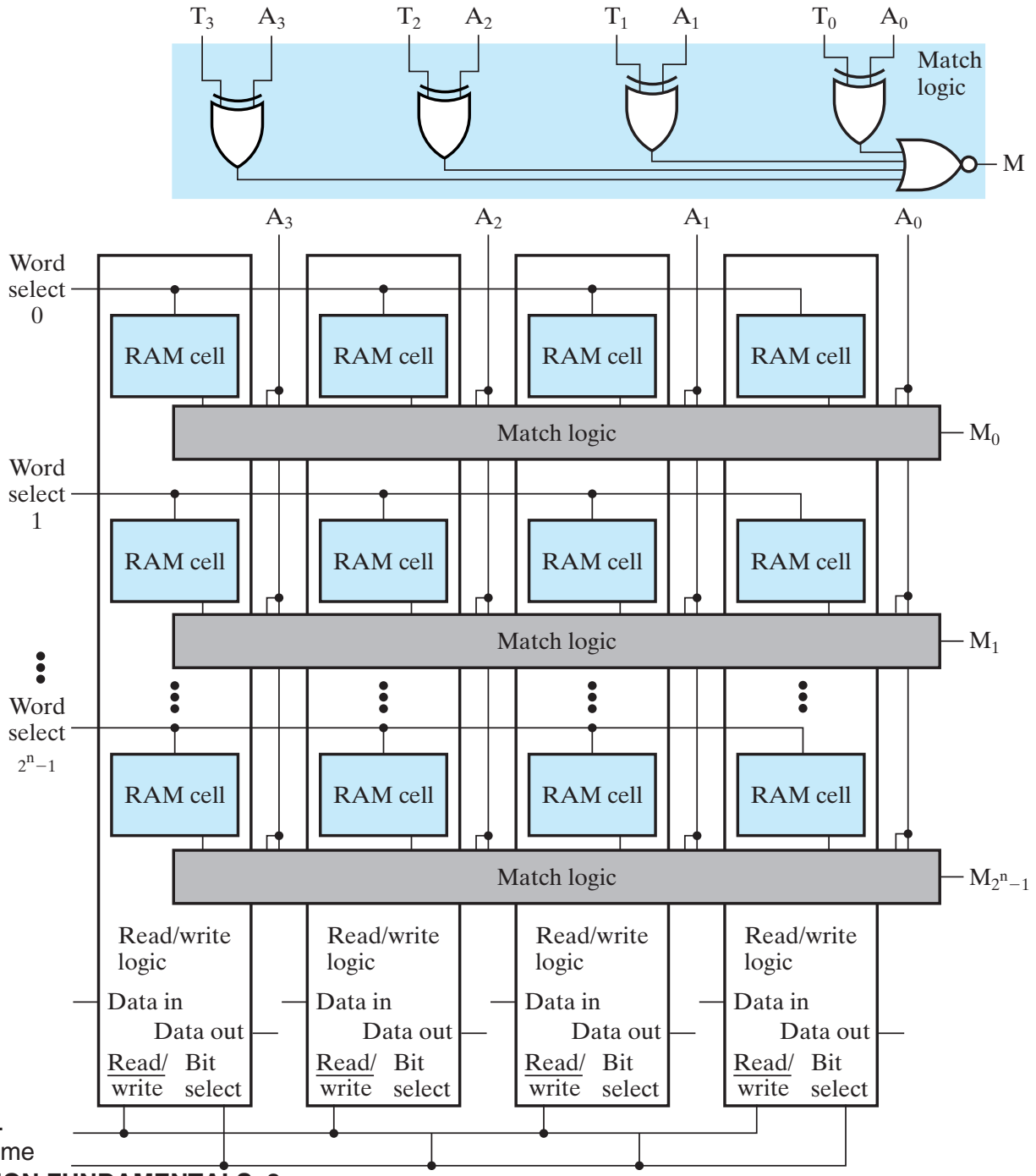
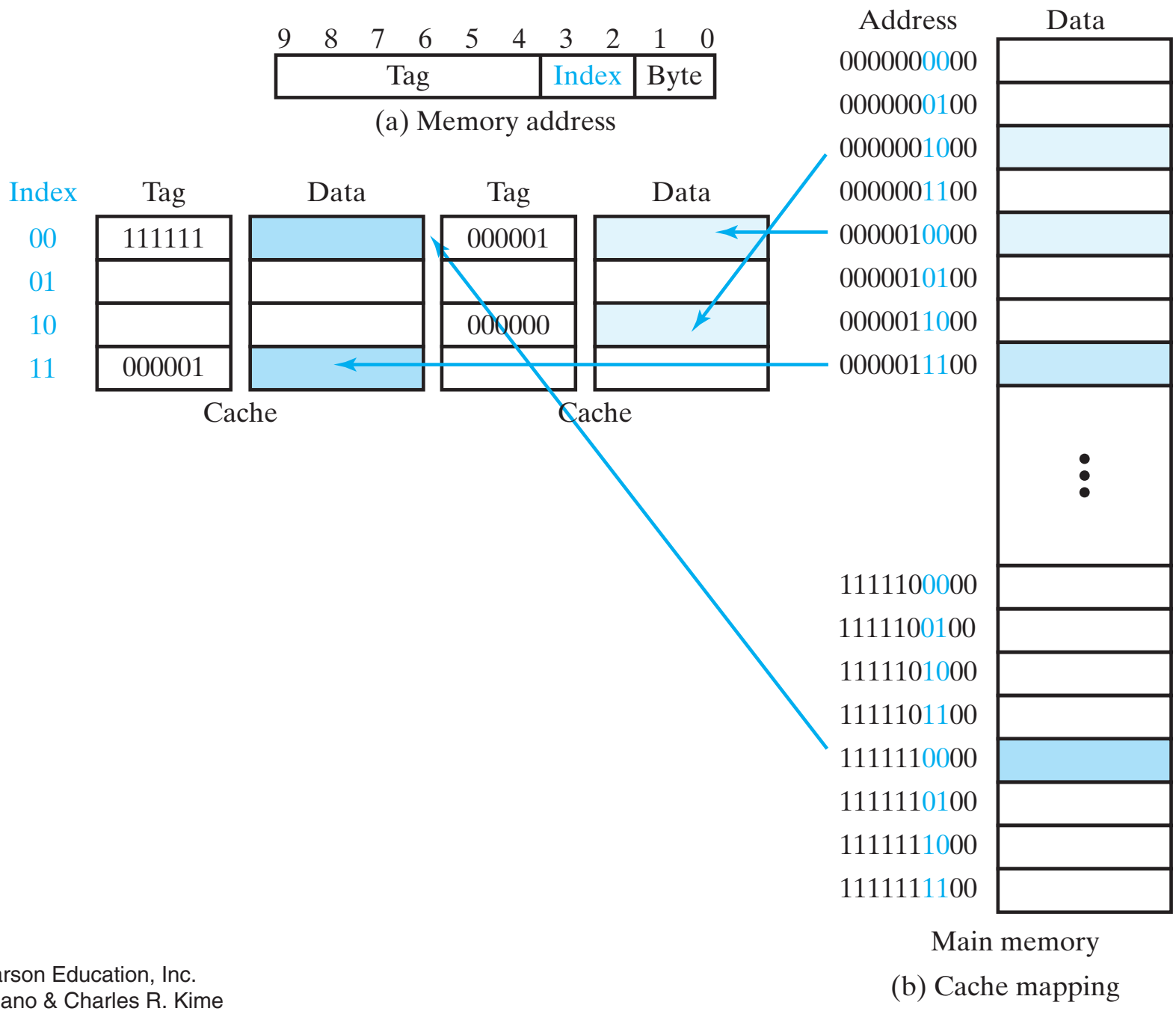


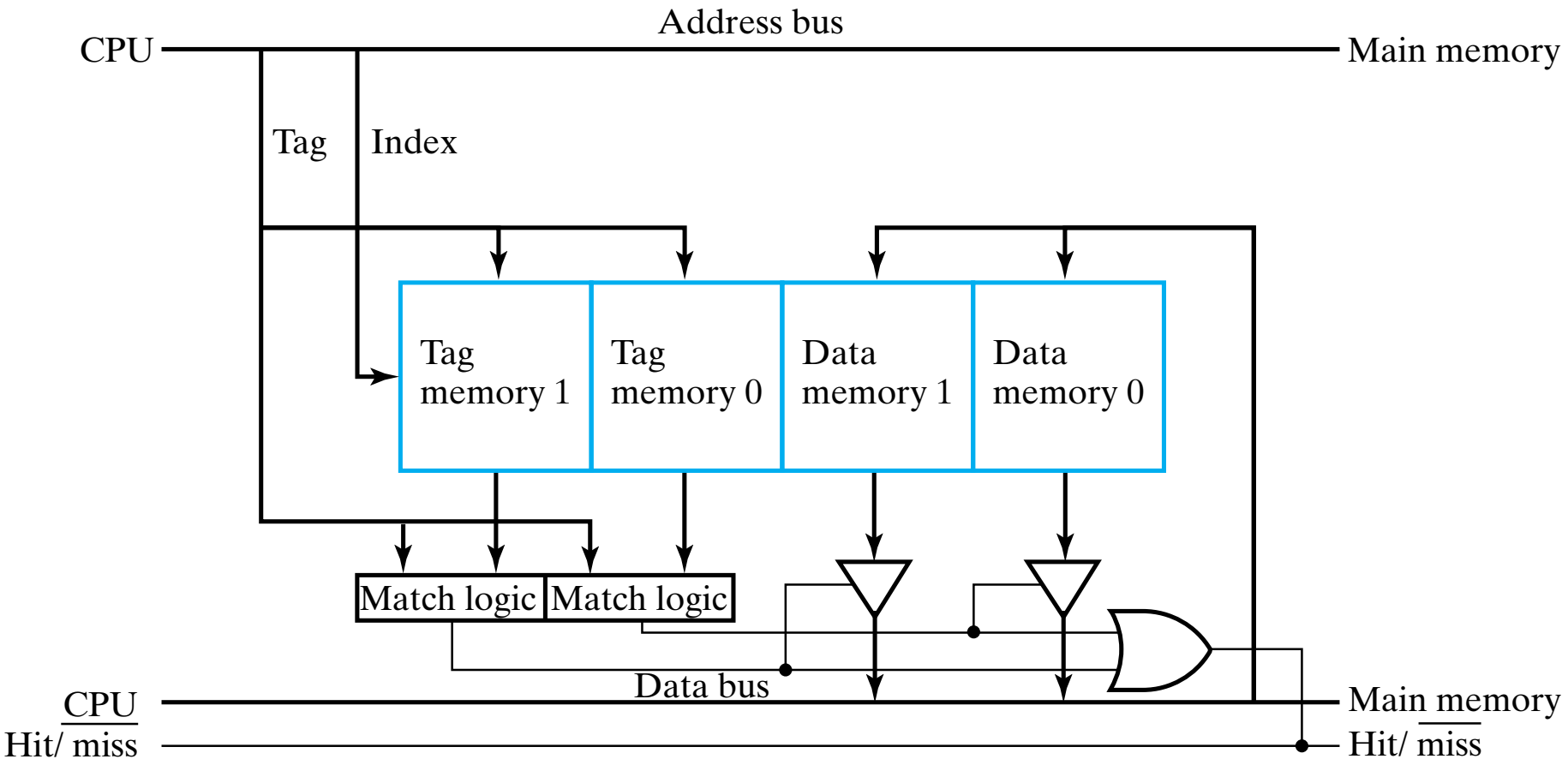


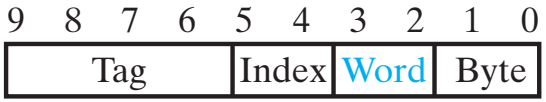




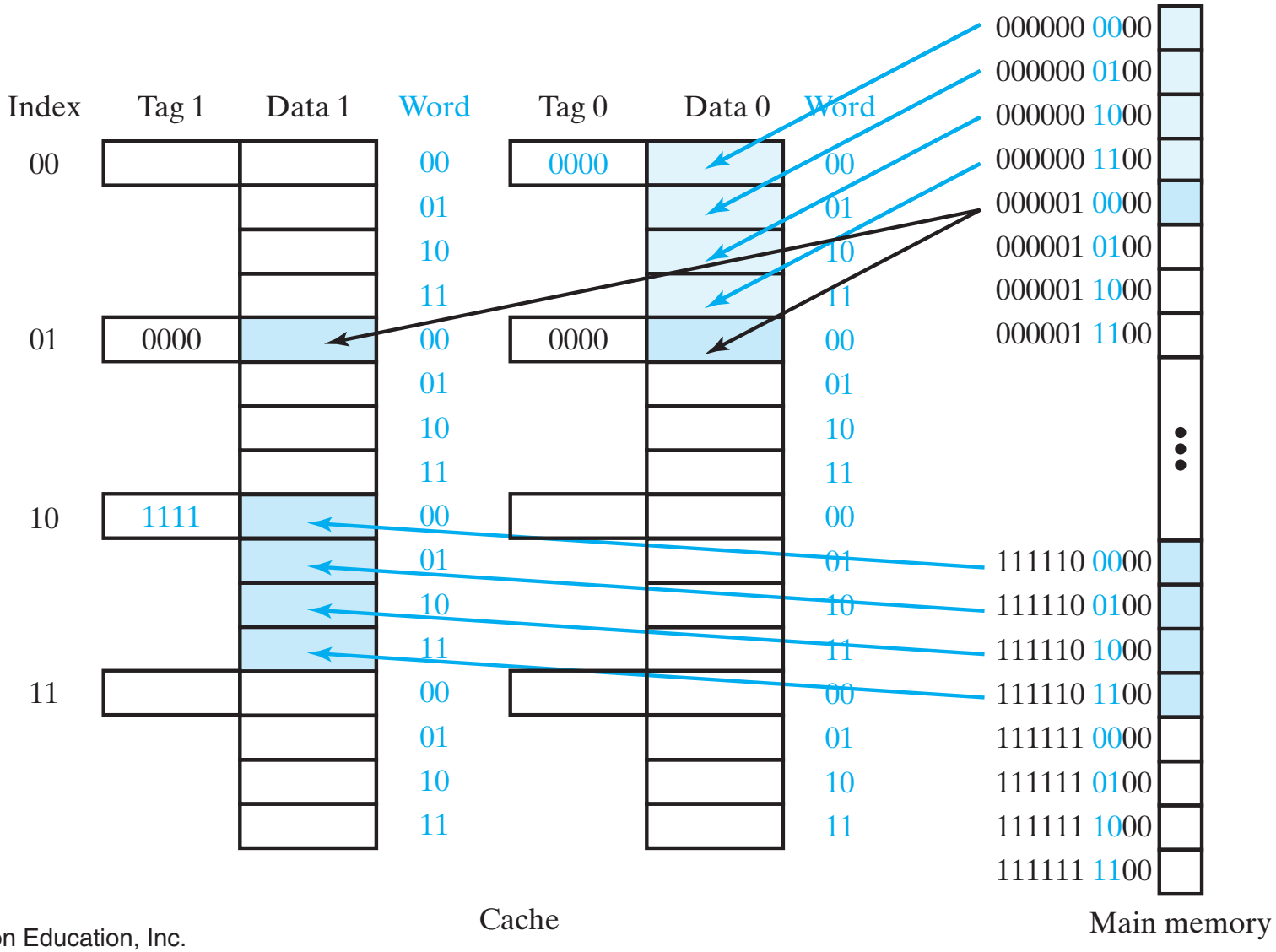




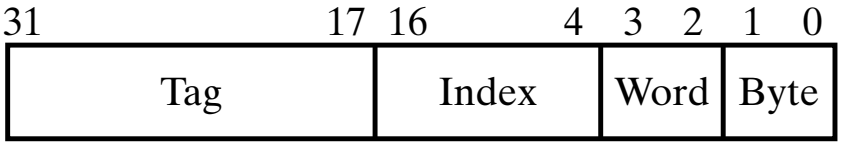




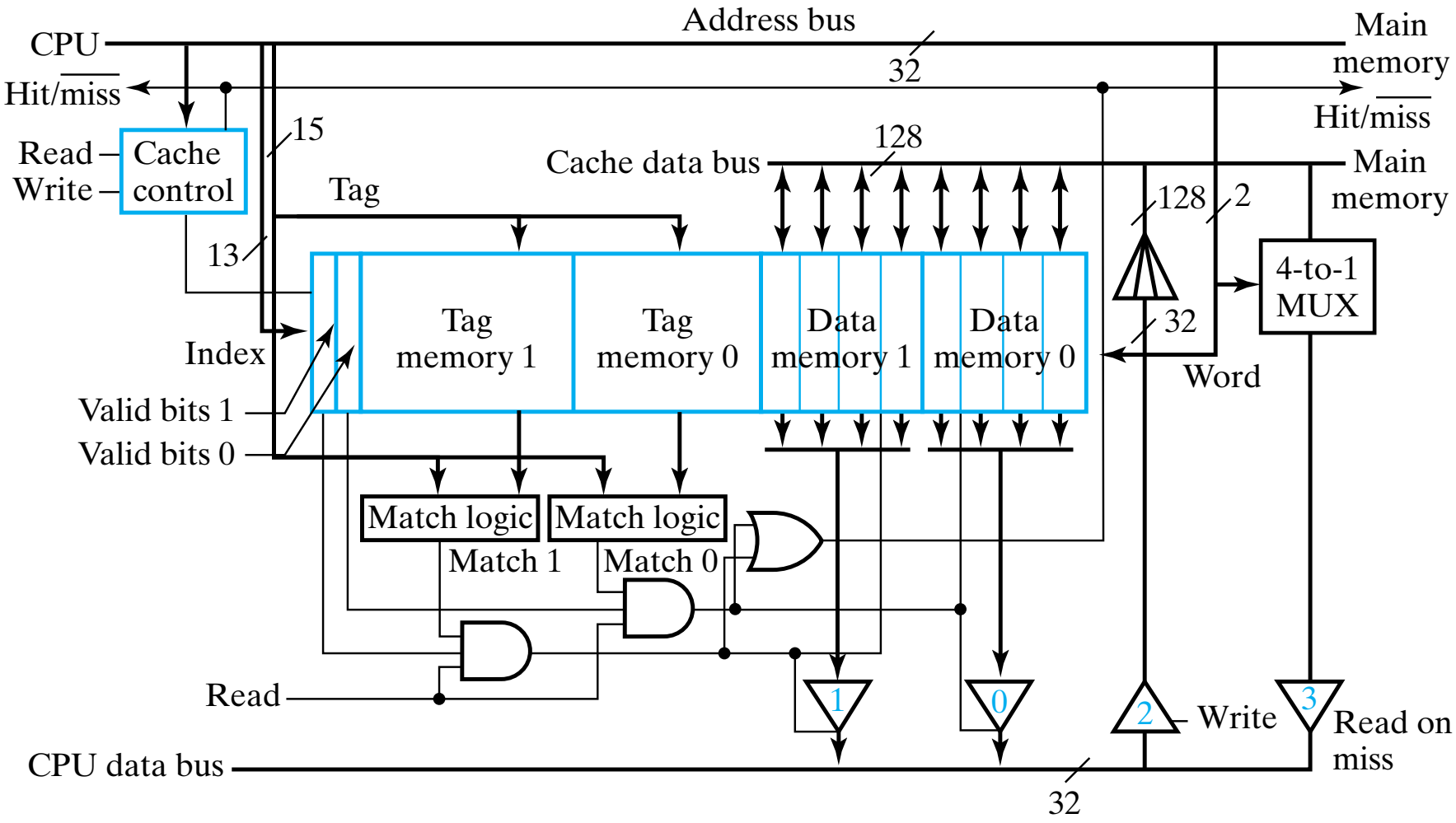
(a) Memory address



(b) Cache mapping



(a) Memory address



(b) Cache diagram