Elektronik Devreler 2021 Bahar – LAB 0

Yükleme için:

https://www.labcenter.com/downloads/

Tutorial için:

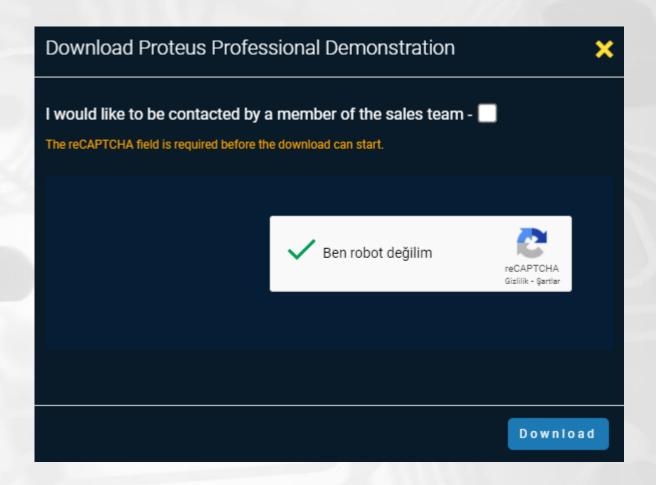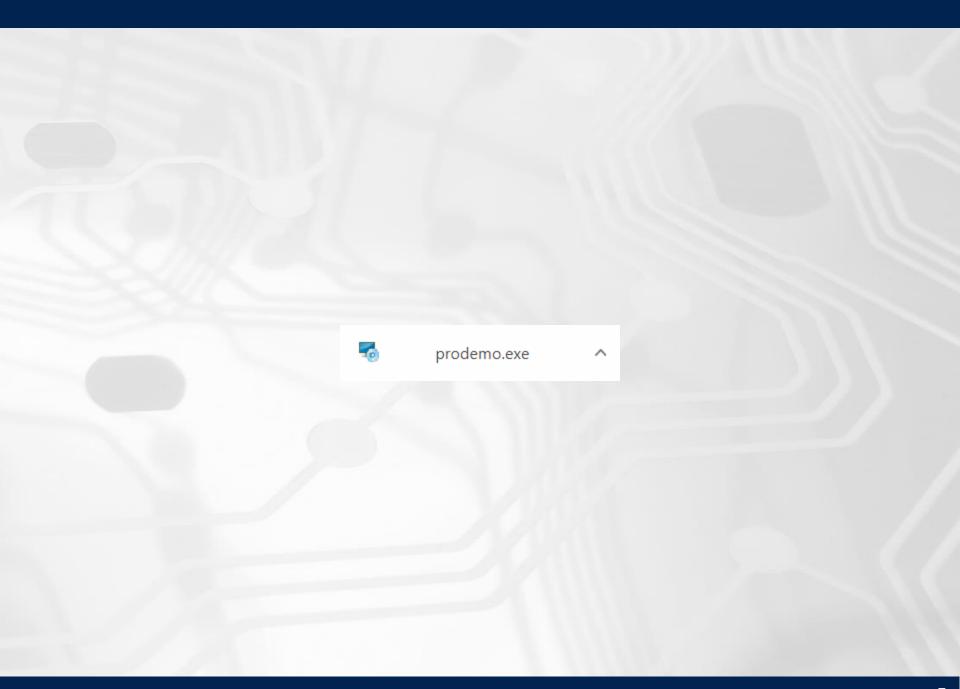https://labcenter.s3.amazonaws.com/downloads/Tutorials.pdf

The Limitations of the demonstration version are as follows:

✓ Not time limited!

✓ Extensive set of sample designs included to help you evaluate all aspects of the software.

✓ You **can** write your own software to run on existing sample designs for evaluation purposes.

✕ You can only print Schematics and Layouts from the sample designs.

✕ You cannot save your work.

✕ You cannot simulate your own microcontroller designs.

Please note using a download manager or accelerator may corrupt the file.

Download the Demonstration version here.

Proteus 8 Demonstration Setup   ✕

Welcome to Proteus 8 Demonstration Setup Wizard

The Setup Wizard will install Proteus 8 Demonstration on your computer. Click Next to continue or close the window to exit the Setup Wizard.

Next >

New Project Wizard

○ Do not create a schematic.
◉ Create a schematic from the selected template.

Design Templates
DEFAULT
Landscape A0
Landscape A1
Landscape A2
Landscape A3
Landscape A4
Landscape US A
Landscape US B
Landscape US C
Portrait A0
Portrait A1
Portrait A2
Portrait A3
Portrait A4
Portrait US A
Portrait US B
Portrait US C
Sample Design

C:\ProgramData\Labcenter Electronics\Proteus 8 Demonstration\Templates\DEFAULT.DTF

Back   Next   Cancel   Help

New Project Wizard

⊙ Do not create a PCB layout.
◯ Create a PCB layout from the selected template.

Layout Templates
Arduino MEGA 2560 rev3
Arduino UNO rev3
DEFAULT
Double Eurocard (2 Layer)
Double Eurocard (4 Layer)
Extended Double Eurocard (2 Layer)
Extended Double Eurocard (4 Layer)
Generic Eight Layer 1.6mm (5 x Signal, 3 x Plane)
Generic Four Layer 1.6mm (2 x Signal, 2 x Plane)
Generic Single Layer
Generic Six Layer 1.6mm (4 x Signal, 2 x Plane)
PANEL
Single Eurocard (2 Layer)
Single Eurocard (4 Layer)
Single Eurocard with Connector

C:\ProgramData\Labcenter Electronics\Proteus 8 Demonstration\Templates\DEFAULT.LTF

| Back | | Next | Cancel | Help |

New Project Wizard

● No Firmware Project

○ Create Firmware Project

○ Create Flowchart Project

Family	
Controller	
Compiler		Compilers...

Create Quick Start Files ☐

Create Peripherals ☐

Back · Next · Cancel · Help

New Project Wizard

Summary

Saving As: C:\Users\BAP_DR\Documents\lab0.pdsprj

✓ Schematic

   Layout

   Firmware

Details

Schematic template: C:\ProgramData\Labcenter Electronics\Proteus 8 Demonstration\Templates\DEFAULT.DTF
No PCB layout
No Firmware Project

Back   Finish   Cancel   Help

Pick Devices

Keywords:

Match whole words? ☐

Show only parts with models? ☐

Category:

- (All Categories)
- (Unspecified)
- Analog ICs
- Capacitors
- CMOS 4000 series
- Connectors
- Data Converters
- Debugging Tools
- Diodes
- ECL 10000 Series

Sub-category:

(All Sub-categories)

Manufacturer:

(All Manufacturers)

Results (No Filter):

Device

No search criteria.
Please enter one or more keywords and/or
select a Category, Sub-category or Manufacturer.

Preview

PCB Preview

OK   Cancel

DEVICES

P L

Simulation Log

Message	Source
ⓘ PROSPICE 8.08.00 (Build 29194) (C) Labcenter Electronics 1993-2021.	
ⓘ Loaded netlist 'C:\Users\BAP_DR\AppData\Local\Temp\LISA1371.SDF' for design 'lab0.pdsprj'	

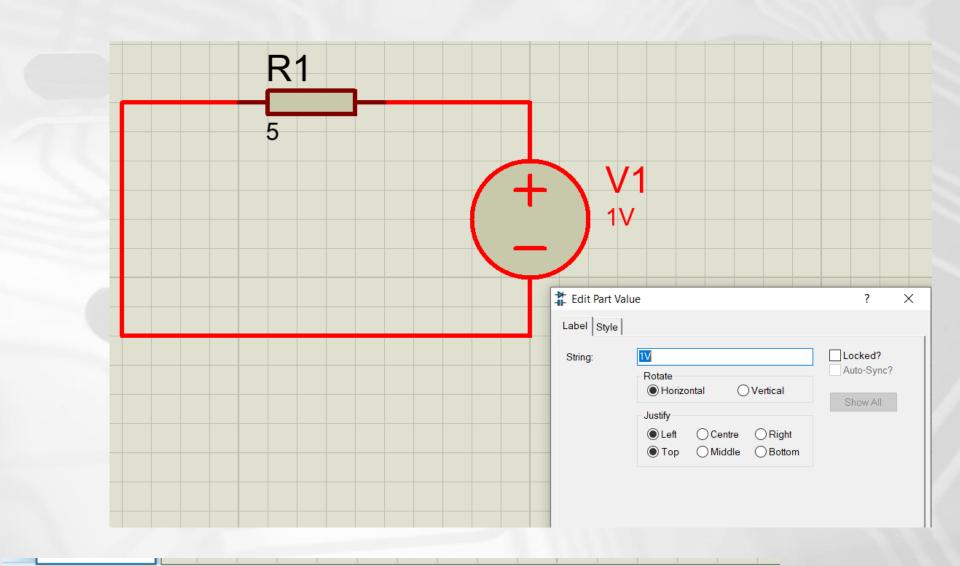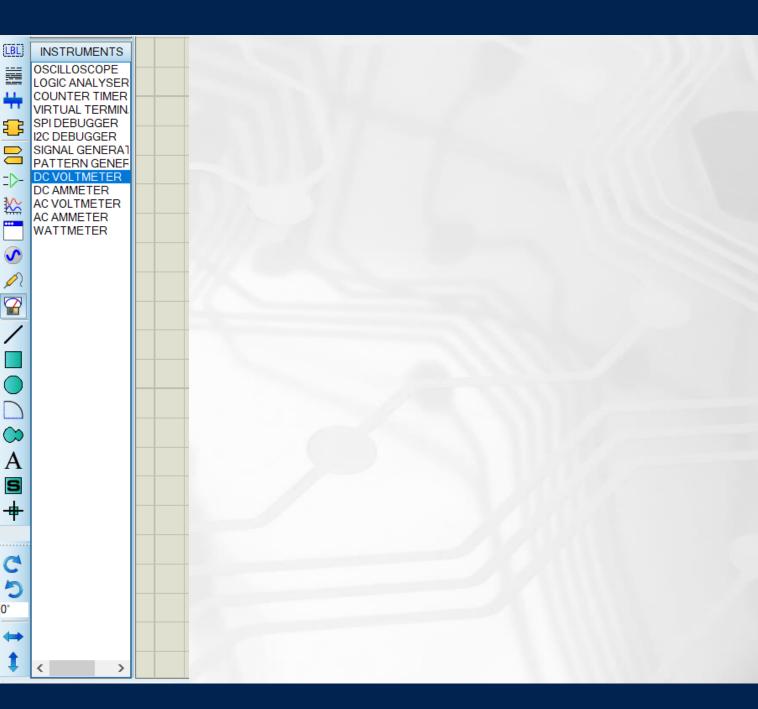ⓘ 2 Message(s)   Shows the currently loaded components.

INSTRUMENTS

- OSCILLOSCOPE
- LOGIC ANALYSER
- COUNTER TIMER
- VIRTUAL TERMIN.
- SPI DEBUGGER
- I2C DEBUGGER
- SIGNAL GENERAT
- PATTERN GENEF
- **DC VOLTMETER**
- DC AMMETER
- AC VOLTMETER
- AC AMMETER
- WATTMETER

Message

ℹ ISIS Release 8.12.00 (Build 30713) (C) Labcenter Electronics 1990- 2021.
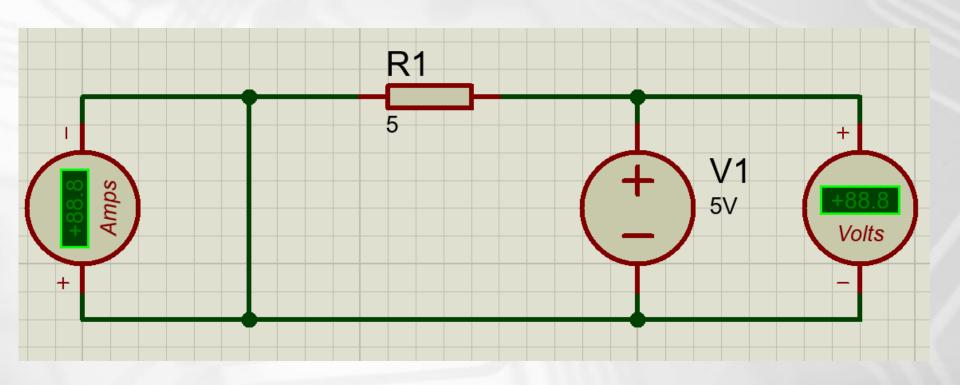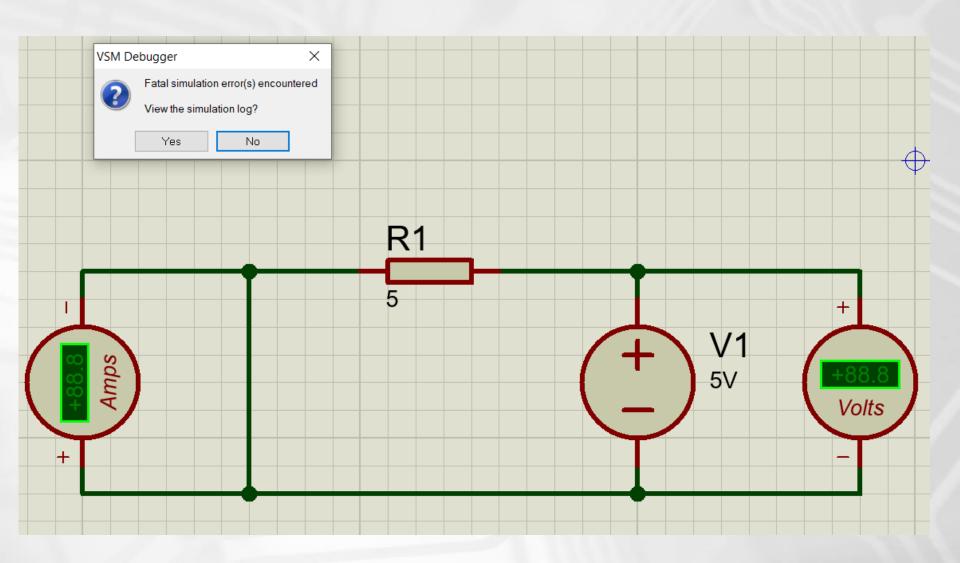
ℹ Compiling design 'C:\Users\BAP_DR\Documents\lab0.pdsprj'.

ℹ Netlist compilation completed OK.

ℹ Netlist linking completed OK.

ℹ Partition analysis completed OK.

ℹ Simulating partition 1

ℹ PROSPICE 8.08.00 (Build 29194) (C) Labcenter Electronics 1993-2021.

ℹ Loaded netlist 'C:\Users\BAP_DR\AppData\Local\Temp\LISA4777.SDF' for design 'lab0.pdsprj'

⚠ [SPICE] singular matrix: check node #$IDC AMMETER#0004_IP1#branch.

⚠ [SPICE] singular matrix: check node #$IDC AMMETER#0004_IP1#branch.

⚠ [SPICE] Gmin stepping failed

⚠ [SPICE] singular matrix: check node #$IDC AMMETER#0004_IP1#branch.

⚠ [SPICE] Source step [0 of 120] failed: source factor =   0.0000

⚠ [SPICE] (null).

❌ Real Time Simulation failed to start.

▶ ▶| ‖ ■ ❌ 1 Error(s)

TERMINALS

- DEFAULT
- INPUT
- OUTPUT
- BIDIR
- POWER
- GROUND
- CHASSIS
- DYNAMIC
- BUS
- NC

Schematic Capture

PROBES

VOLTAGE
CURRENT
TAPE

45

GRAPHS

ANALOGUE
DIGITAL
MIXED
FREQUENCY
TRANSFER
NOISE
DISTORTION
FOURIER
AUDIO
INTERACTIVE
CONFORMANCE
DC SWEEP
AC SWEEP