

Figure 15: **Driver stage:** For the primary and secondary side FETs

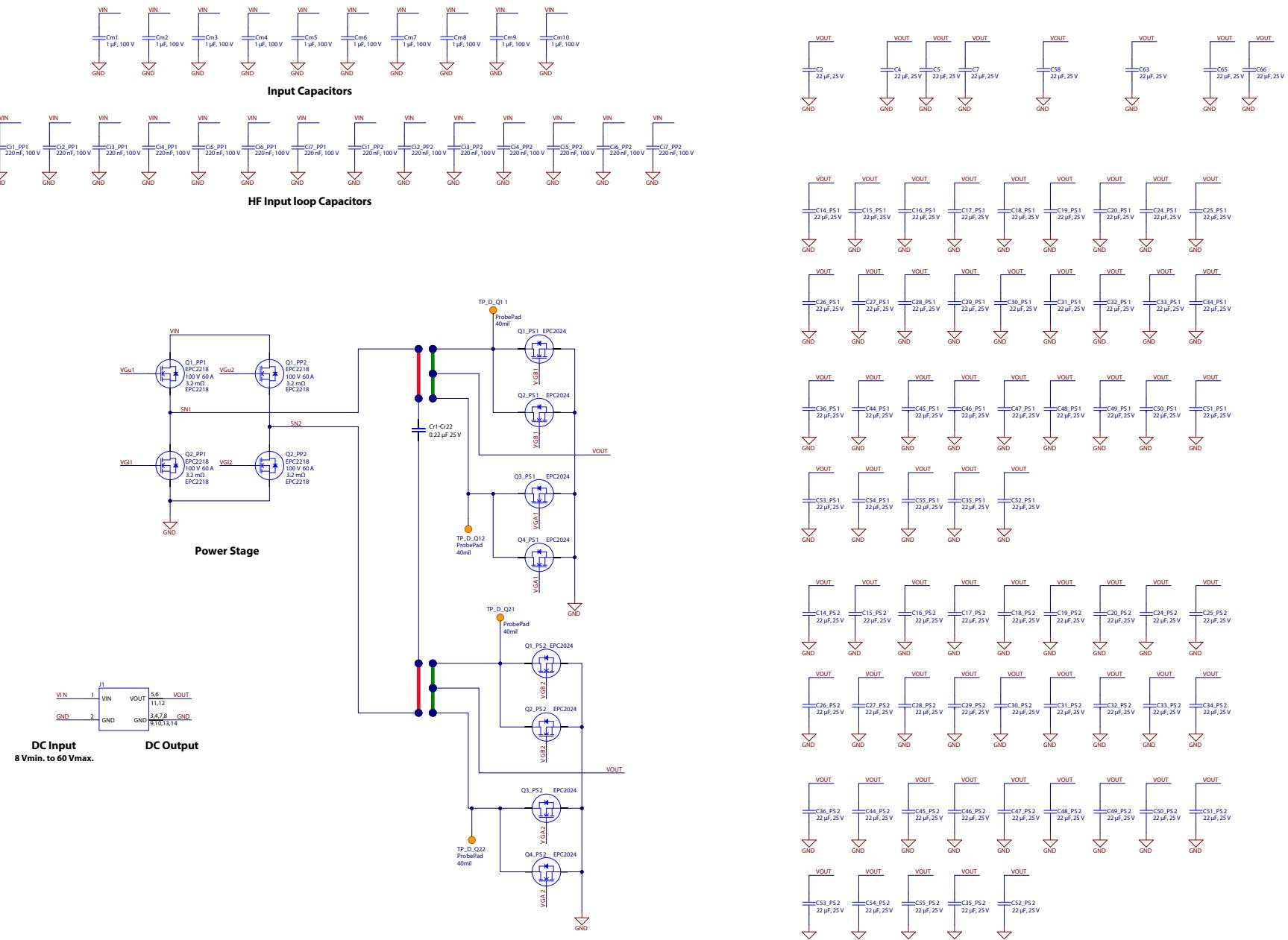


Figure 16: Power stage: Topology including FETs, transformer and input, output and resonant capacitors

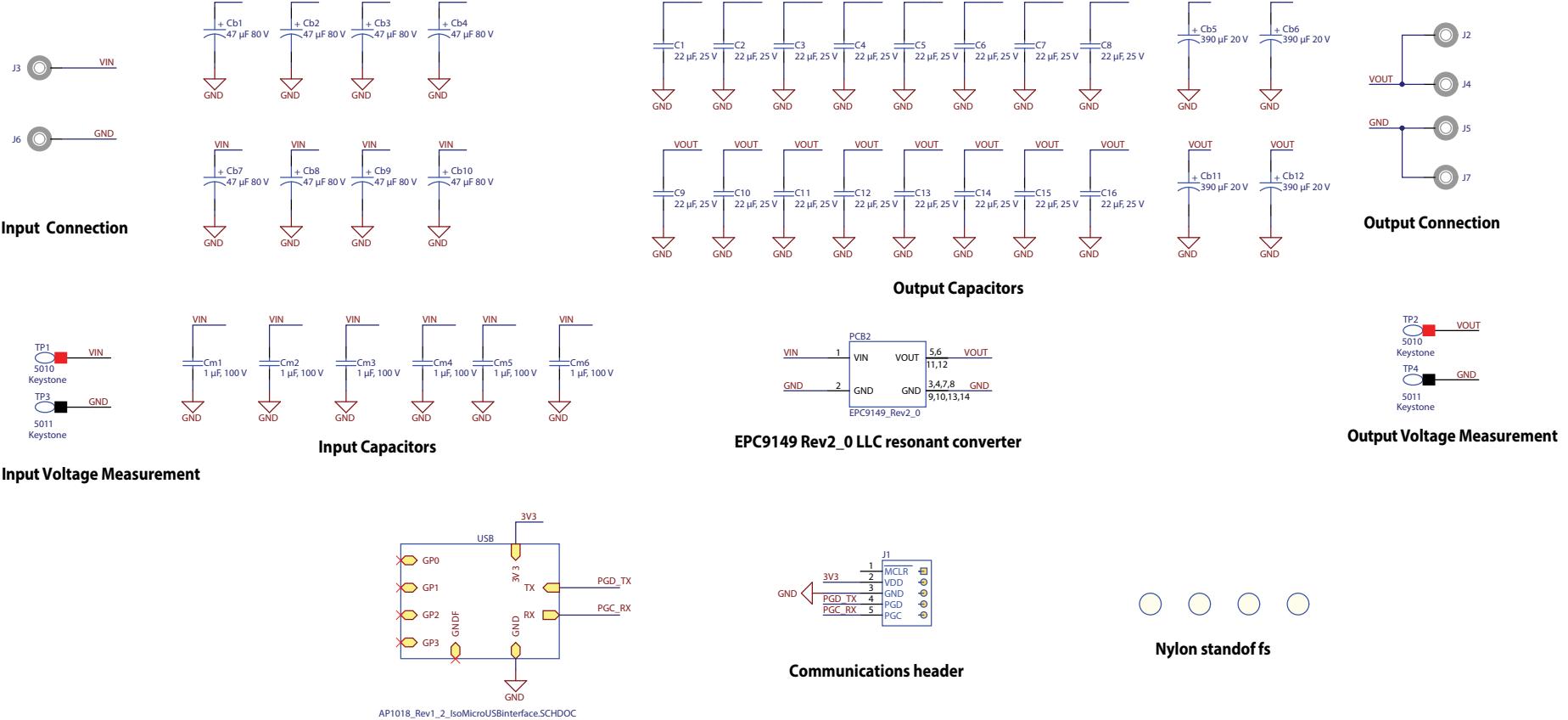


Figure 17: EPC9533 power circuit schematic

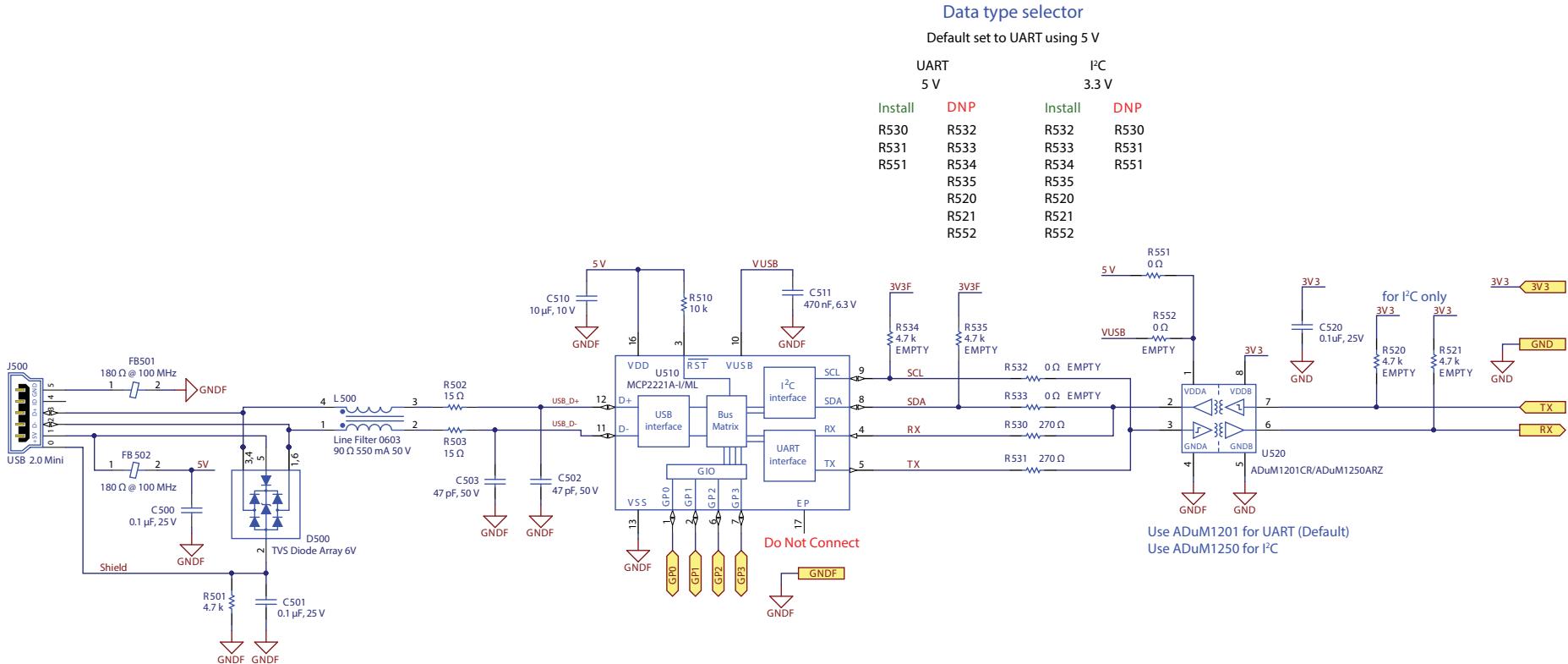


Figure 18: Isolated USB circuit on the EPC9533 motherboard