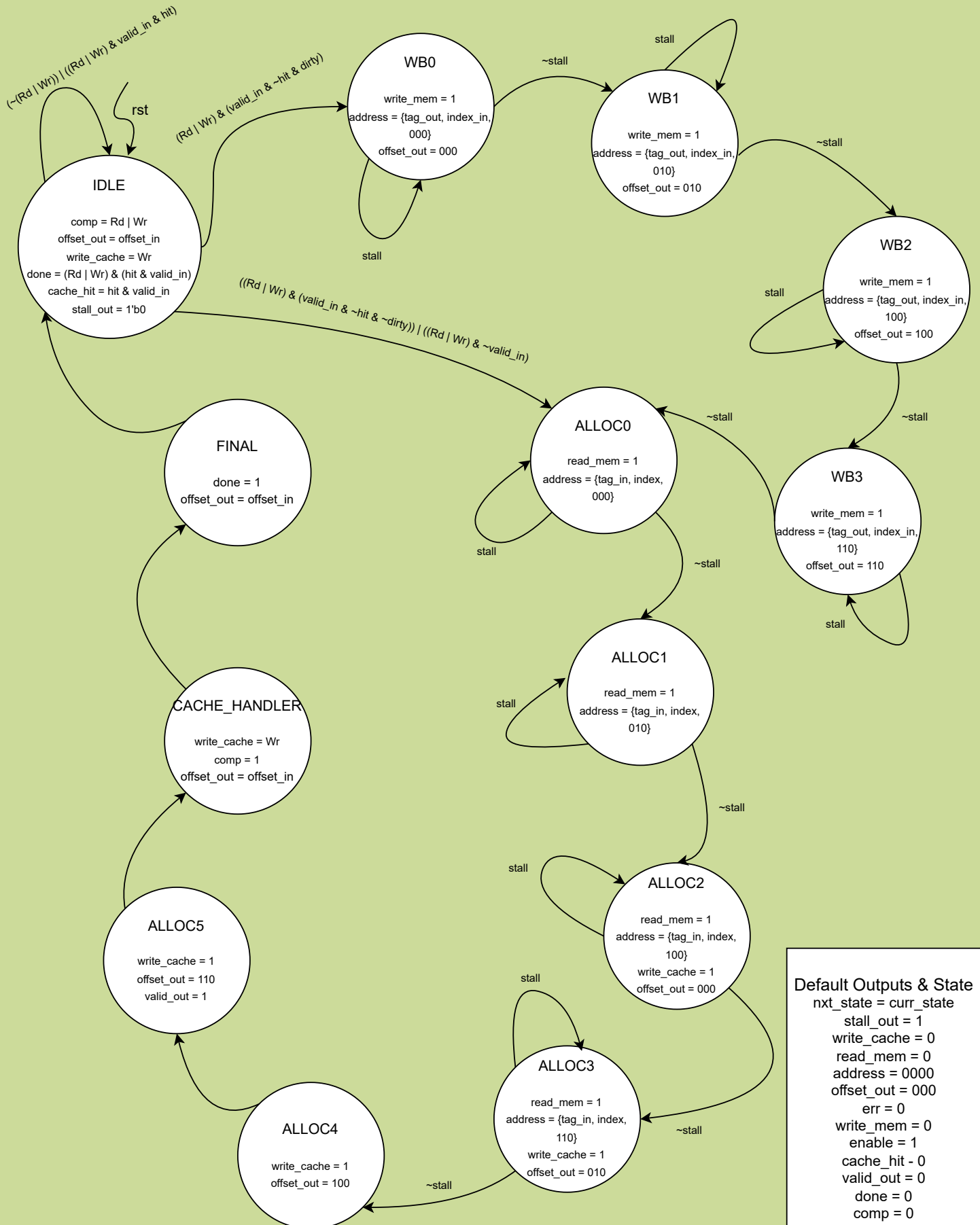
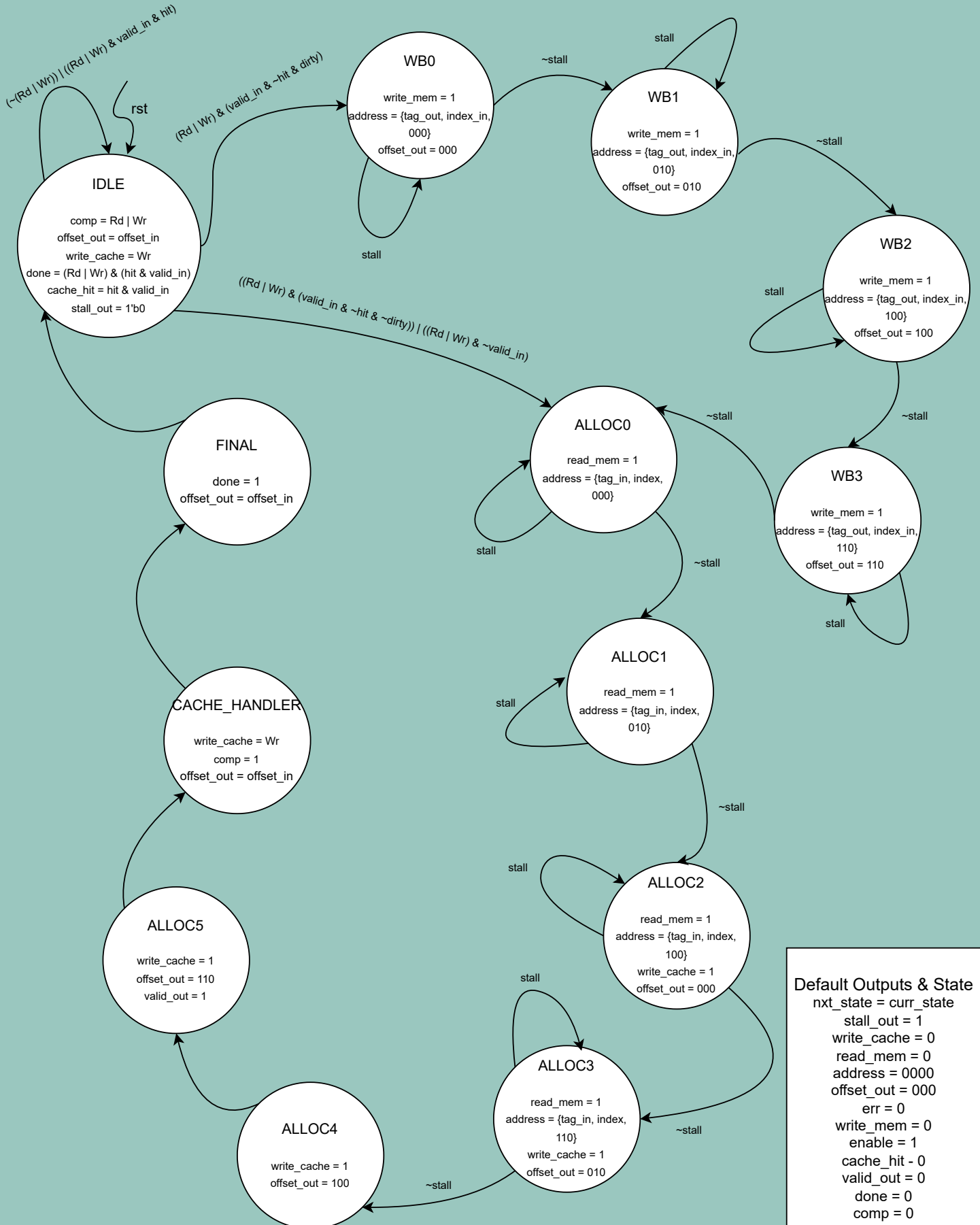


Direct Mapped Cache Controller FSM



Two-Way Set Associative Cache Controller FSM



Default Outputs & State

```

nxt_state = curr_state
stall_out = 1
write_cache = 0
read_mem = 0
address = 0000
offset_out = 000
err = 0
write_mem = 0
enable = 1
cache_hit = 0
valid_out = 0
done = 0
comp = 0
    
```