Teensy 4.1 GPIOs in Edge Pin Order

| Schematic Net Label | BGA Ball | Teensy Pin Label | Alias Label | Write to Register | Read from Register | Data Direction Register | Bit Number | I/O Pin Count | | Summary Commentary |
|---|---|---|---|---|---|---|---|---|---|---|
| AD_B0_03 | G11 | 0 | | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 3 | 1 | | // PinLabel=0; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q3 |
| AD_B0_02 | M11 | 1 | | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 2 | 2 | | // PinLabel=1; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q2 |
| EMC_04 | F2 | 2 | | GPIO9_DR | GPIO9_PSR | GPIO9_GDIR | 4 | 3 | | // PinLabel=2; write to GPIO9_DR; read from GPIO9_PSR; set direction using GPIO9_GDIR; Bit: Q4 |
| EMC_05 | G5 | 3 | | GPIO9_DR | GPIO9_PSR | GPIO9_GDIR | 5 | 4 | | // PinLabel=3; write to GPIO9_DR; read from GPIO9_PSR; set direction using GPIO9_GDIR; Bit: Q5 |
| EMC_06 | H5 | 4 | | GPIO9_DR | GPIO9_PSR | GPIO9_GDIR | 6 | 5 | | // PinLabel=4; write to GPIO9_DR; read from GPIO9_PSR; set direction using GPIO9_GDIR; Bit: Q6 |
| EMC_08 | H3 | 5 | | GPIO9_DR | GPIO9_PSR | GPIO9_GDIR | 8 | 6 | | // PinLabel=5; write to GPIO9_DR; read from GPIO9_PSR; set direction using GPIO9_GDIR; Bit: Q8 |
| B0_10 | D9 | 6 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 10 | 7 | | // PinLabel=6; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q10 |
| B1_01 | B11 | 7 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 17 | 8 | | // PinLabel=7; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q17 |
| B1_00 | A11 | 8 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 16 | 9 | | // PinLabel=8; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q16 |
| B0_11 | A10 | 9 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 11 | 10 | | // PinLabel=9; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q11 |
| B0_00 | D7 | 10 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 0 | 11 | | // PinLabel=10; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q0 |
| B0_02 | E8 | 11 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 2 | 12 | | // PinLabel=11; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q2 |
| B0_01 | E7 | 12 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 1 | 13 | | // PinLabel=12; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q1 |
| B0_03 | D8 | 13 | Built-in LED | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 3 | 14 | | // PinLabel=13; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q3 |
| AD_B1_02 | L11 | 14 | A0 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 18 | 15 | | // PinLabel=14; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q18 |
| AD_B1_03 | M12 | 15 | A1 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 19 | 16 | | // PinLabel=15; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q19 |
| AD_B1_07 | K10 | 16 | A2 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 23 | 17 | | // PinLabel=16; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q23 |
| AD_B1_06 | J12 | 17 | A3 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 22 | 18 | | // PinLabel=17; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q22 |
| AD_B1_01 | K11 | 18 | A4 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 17 | 19 | | // PinLabel=18; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q17 |
| AD_B1_00 | J11 | 19 | A5 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 16 | 20 | | // PinLabel=19; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q16 |
| AD_B1_10 | L13 | 20 | A6 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 26 | 21 | | // PinLabel=20; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q26 |
| AD_B1_11 | J13 | 21 | A7 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 27 | 22 | | // PinLabel=21; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q27 |
| AD_B1_08 | H13 | 22 | A8 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 24 | 23 | | // PinLabel=22; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q24 |
| AD_B1_09 | M13 | 23 | A9 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 25 | 24 | | // PinLabel=23; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q25 |
| AD_B0_12 | K14 | 24 | A10 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 12 | 25 | | // PinLabel=24; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q12 |
| AD_B0_13 | L14 | 25 | A11 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 13 | 26 | | // PinLabel=25; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q13 |
| AD_B1_14 | G12 | 26 | A12 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 30 | 27 | | // PinLabel=26; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q30 |
| AD_B1_15 | J14 | 27 | A13 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 31 | 28 | | // PinLabel=27; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q31 |
| EMC_32 | D5 | 28 | | GPIO8_DR | GPIO8_PSR | GPIO8_GDIR | 18 | 29 | | // PinLabel=28; write to GPIO8_DR; read from GPIO8_PSR; set direction using GPIO8_GDIR; Bit: Q18 |
| EMC_31 | C5 | 29 | | GPIO9_DR | GPIO9_PSR | GPIO9_GDIR | 31 | 30 | | // PinLabel=29; write to GPIO9_DR; read from GPIO9_PSR; set direction using GPIO9_GDIR; Bit: Q31 |
| EMC_37 | E4 | 30 | | GPIO8_DR | GPIO8_PSR | GPIO8_GDIR | 23 | 31 | | // PinLabel=30; write to GPIO8_DR; read from GPIO8_PSR; set direction using GPIO8_GDIR; Bit: Q23 |
| EMC_36 | C3 | 31 | | GPIO8_DR | GPIO8_PSR | GPIO8_GDIR | 22 | 32 | | // PinLabel=31; write to GPIO8_DR; read from GPIO8_PSR; set direction using GPIO8_GDIR; Bit: Q22 |
| B0_12 | C10 | 32 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 12 | 33 | | // PinLabel=32; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q12 |
| EMC_07 | H4 | 33 | | GPIO9_DR | GPIO9_PSR | GPIO9_GDIR | 7 | 34 | | // PinLabel=33; write to GPIO9_DR; read from GPIO9_PSR; set direction using GPIO9_GDIR; Bit: Q7 |
| B1_13 | D14 | 34 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 29 | 35 | | // PinLabel=34; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q29 |
| B1_12 | D13 | 35 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 28 | 36 | | // PinLabel=35; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q28 |
| B1_02 | C11 | 36 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 18 | 37 | | // PinLabel=36; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q18 |
| B1_03 | D11 | 37 | | GPIO7_DR | GPIO7_PSR | GPIO7_GDIR | 19 | 38 | | // PinLabel=37; write to GPIO7_DR; read from GPIO7_PSR; set direction using GPIO7_GDIR; Bit: Q19 |
| AD_B1_12 | H12 | 38 | A14 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 28 | 39 | | // PinLabel=38; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q28 |
| AD_B1_13 | H11 | 39 | A15 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 29 | 40 | | // PinLabel=39; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q29 |
| AD_B1_04 | L12 | 40 | A16 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 20 | 41 | | // PinLabel=40; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q20 |
| AD_B1_05 | K12 | 41 | A17 | GPIO6_DR | GPIO6_PSR | GPIO6_GDIR | 21 | 42 | | // PinLabel=41; write to GPIO6_DR; read from GPIO6_PSR; set direction using GPIO6_GDIR; Bit: Q21 |