| Z47999 | KEMENTERIAN RISET, TEKNOLOGI DAN PENDIDIKAN | • | |
|--------------------------------|---|-----------------|--------------|
| | UNIVERSITAS ANDALAS | 1 | |
| | FAKULTAS TEKNOLOGI INFORMASI | | |
| | JURUSAN SISTEM KOMPUTER/SISTEM INFO | RISTEKDIKTI | |
| TA 2018/2019 Semester Genap | | No.Dokumen | |
| | DOKUMEN NASKAH SOAL UJIAN AKHIR SEMESTER 2018/2019 | Edisl | |
| | | Revisi | |
| | | Berlaku Efektif | |
| | | Halaman | |
| Matakuliah | : Pengantar Analisis Rangkaian | Kode | : CE1204 |
| | | Jenis | : WAJIB |
| Hari/Tgl. | : Selasa, 21 Mei 2019 | Waktu | : 90 menit |
| Smt/Kelas | : II/A dan B | Lokal Ujian | : |
| Dosen | : 1. Dr. Eng. Rian Ferdian, M.T 2. Lathifah Arief, M.T | Sifat Ujian | : Tutup Buku |

Soal:

1. Tentukan vo/vs pada gambar rangkaian berikut ini

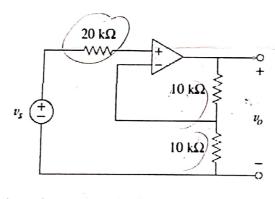
$$V_{S} = \frac{10}{10+10} V_{0}$$

$$V_{S} = \frac{10}{20} V_{0}$$

$$V_{S} = \frac{1}{2} V_{0}$$

$$\frac{V_{S}}{V_{0}} = \frac{1}{2} V_{0}$$

$$\frac{V_{S}}{V_{0}} = \frac{1}{2} V_{0}$$



- (-10 00) 00 = 200's

- (10 v6 + 20 vs)

2. Tentukanlah V_o pada gambar rangkaian berikut

N' = - 2 .12

$$= -3.15$$

$$= -45$$

$$= -45$$

$$= -45$$

$$= -45$$

$$= -45$$

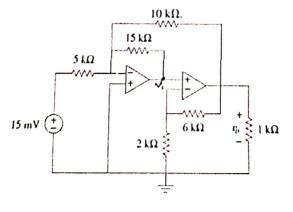
$$= -45$$

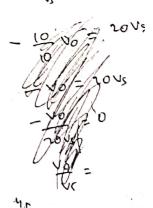
$$= -45$$

$$= -45$$

$$= -45$$

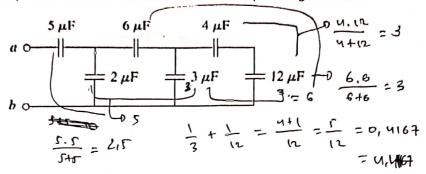
= - 180





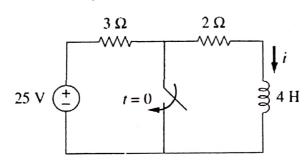
3. Tentukan nilai kapasitansi ekulvalen untuk dari terminal a ke b pada rangkalan berikut

6.27



4. Tentukan arus induktor terhadap waktu untuk rangkaian berikut ini

753 A



$$\frac{10}{10} = \frac{10}{10} = \frac{25}{3+2} = 5 \text{ f}$$

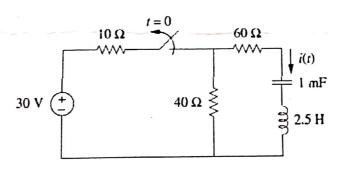
$$\frac{10}{10} = \frac{10}{10} = \frac{4}{10}$$

$$\frac{10}{10} = \frac{10}{10} = \frac{4}{10}$$

$$\frac{10}{10} = \frac{10}{10} = \frac{10}{10}$$

5. Tentukan i(t) pada rangkaian dibawah ini

8.16



*** Selamat ujian, semoga sukses ***

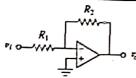
| Dibuat | | Diperiksa | | Disetujui | |
|-----------------|---------------------------|-----------------|----------------------------|-----------------|----------------------|
| Tanggal | 15 Mei 2019 | Tanggal | 15 Mei 2019 | Tanggal | 15 Mei 2019 |
| Oleh | Dr. Eng. Rian Ferdian, MT | Oleh | Mohd. Hafis Hersyah, MT | Oleh | Doddy Ichwana P., MT |
| Jabatan | Dosen Pengasuh MK | Jabatan | Ketua GKM | Jabatan | Ka.Prog.Studi |
| Tanda Tangan | | Tanda Tangan | | Tanda Tangan | |

- 376,96

2

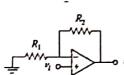
Op amp circuit

Name/output-input relationship





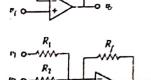




Noninverting amplifier

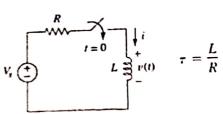
$$v_o = \left(1 + \frac{R_2}{R_1}\right) v_t$$

Voltage follower



Summer
$$v_{\sigma} = -\left(\frac{R_f}{R_1}v_1 + \frac{R_f}{R_2}v_2 + \frac{R_f}{R_3}v_3\right)$$

Orde I RL



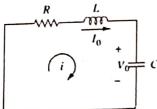
$$R_1$$
 R_2 R_1 R_2 R_2 R_1 R_2 R_2 R_2 R_2 R_3 R_4 R_5 R_5

$$v_o = \frac{R_2}{R_1}(v_2 - v_1)$$

$$v_o = \frac{Q_2}{Q_1}v_1 - \frac{R_2}{R_1}v_1$$

$$V_0 = \frac{(1.1 + R_2/Q_1)}{(1 + R_2/R_2)}v_2 - \frac{R_2}{R_1}v_1$$

Rangkaian Orde II



Jika s1 dan s2 dua bilangan riil yang berbeda maka

$$i(t) = Ae^{s_1t} + Be^{s_2t}$$

$$i(0) = A + B$$

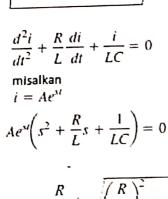
$$\frac{di(0)}{dt} = s_1 A + s_2 B$$

Jika s1 dan s2 dual bilangan riil yang sama maka

$$i(t) = (At + B)e^{st}$$

$$i(0) = B$$

$$\frac{di(0)}{dt} = (1+s)A + B$$



$$s_1 = -\frac{R}{2L} + \sqrt{\left(\frac{R}{2L}\right)^2 - \frac{1}{LC}}$$
$$s_2 = -\frac{R}{2L} - \sqrt{\left(\frac{R}{2L}\right)^2 - \frac{1}{LC}}$$